

Bank Number	Pin Names	Configuration Functions	Emulated MIPI RX Function	VREF Function for HSTL/SSTL	Hard Block PLL REFCLK	LVDS and MIPI Pairs	Clock Region	FBGA529
BL	GPIOL_23						L0	W2
BL	GPIOL_22						L0	V4
BL	GPIOL_21						L0	V5
BL	GPIOL_20_PLLIN1						L0	W4
BL	GPIOL_10_PLLIN1						L0	W3
BL	GPIOL_09						L0	V3
BL	GPIOL_08						L0	W1
BL	GPIOL_07						L0	U1
BL	GPIOL_06_CLK27						L0	V1
BL	GPIOL_05_CLK26						L0	T3
BL	GPIOL_04_CLK25						L0	U2
BL	GPIOL_03_CLK24						L0	W5
BL	TDO							T4
BL	TMS							T5
BL	TCK							U5
BL	TDI							U6
BL	GPIOL_02						L0	U3
BL	GPIOL_01						L0	W6
BL	GPIOL_00_PLLIN1						L0	V6
DDR	DDR_DQ[30]							AA2
DDR	DDR_DQ[31]							AC2
DDR	DDR_DQ[28]							AB1
DDR	DDR_DM[3]							AA3
DDR	DDR_DQ[29]							AA1
DDR	DDR_DQS_N[3]							AC3
DDR	DDR_DQS[3]							AB3
DDR	DDR_DQ[26]							AC5
DDR	DDR_DQ[25]							AA4
DDR	DDR_DQ[27]							AA5
DDR	DDR_DQ[23]							AA6
DDR	DDR_DQ[24]							AB4
DDR	DDR_DQ[20]							AC7
DDR	DDR_DQ[21]							AA7
DDR	DDR_DQ[22]							AC6
DDR	DDR_DQS_N[2]							AC8
DDR	DDR_DM[2]							AA8
DDR	DDR_DQ[19]							AA10
DDR	DDR_DQS[2]							AB8
DDR	DDR_DQ[17]							AA9
DDR	DDR_DQ[18]							AC10
DDR	DDR_DQ[16]							AB9
DDR	DDR_A[3]							W9
DDR	DDR_A[4]							W8
DDR	DDR_A[5]							Y7
DDR	DDR_A[2]							Y9
DDR	DDR_A[0]							AC11
DDR	DDR_A[1]							AA11
DDR	DDR_CAL							V8
DDR	DDR_CK							AC12
DDR	DDR_CK_N							AB12
DDR	DDR_CKE[0]							AA12
DDR	DDR_RST_N							AC13
DDR	DDR_CS_N[0]							Y15
DDR	DDR_CKE[1]							AA13
DDR	DDR_CS_N[1]							W16
DDR	DDR_CS_N[2]							W17
DDR	DDR_CS_N[3]							Y17
DDR	DDR_DQ[14]							AC14
DDR	DDR_DQ[15]							AA14
DDR	DDR_DQ[12]							AB15
DDR	DDR_DM[1]							AA16
DDR	DDR_DQ[13]							AA15
DDR	DDR_DQS[1]							AB16
DDR	DDR_DQS_N[1]							AC16
DDR	DDR_DQ[10]							AA18
DDR	DDR_DQ[11]							AC18
DDR	DDR_DQ[9]							AC17
DDR	DDR_DQ[6]							AC22
DDR	DDR_DQ[8]							AA17
DDR	DDR_DQ[4]							AB20
DDR	DDR_DQ[5]							AB19
DDR	DDR_DQ[7]							AA21
DDR	DDR_DQS_N[0]							AC21
DDR	DDR_DM[0]							AB23
DDR	DDR_DQ[3]							AC19
DDR	DDR_DQS[0]							AB21
DDR	DDR_DQ[2]							AA20
DDR	DDR_DQ[1]							AA23
DDR	DDR_DQ[0]							AA22
TL	GPIOL_26_PLLIN1				DDR_PLL_REFCLK		L15	V18
TL	GPIOL_27_CLK28						L15	T19
TL	GPIOL_28_CLK29						L15	T20
TL	GPIOL_29_CLK30						L15	V19
TL	GPIOL_30_CLK31						L15	T18
TL	GPIOL_31						L15	U20
TL	GPIOL_32_PLLIN1				DDR_PLL_REFCLK		L15	U18
TL	GPIOL_33						L15	V23
TL	GPIOL_34						L15	V21
TL	GPIOL_35						L15	V22
TL	GPIOL_36_PLLIN1				DDR_PLL_REFCLK		L15	U23
TL	GPIOL_37						L15	U21
TL	GPIOL_38						L15	V20
TL	GPIOL_39						L15	U17
TL	GPIOL_40						L15	W22
TL	GPIOL_41						L15	W23
TL	GPIOL_42						L15	W21
TL	GPIOL_43						L15	W19
TL	GPIOL_44						L15	W20
BANK2A	REF_RES_2A							T22
BANK2A	GPIOT_P_00_PLIUN0		RX_DATA_P0_I0		DDR_PLL_REFCLK	GPIOT_PN_00	T0	R22
BANK2A	GPIOT_N_00		RX_DATA_N0_I0			GPIOT_PN_00	T0	R21
BANK2A	GPIOT_N_01		RX_DATA_N1_I0			GPIOT_PN_01	T0	P23
BANK2A	GPIOT_P_01_EXTFB		RX_DATA_P1_I0			GPIOT_PN_01	T0	R23
BANK2A	GPIOT_N_02		RX_CLK_N_I0			GPIOT_PN_02	T0	R19
BANK2A	GPIOT_P_02		RX_CLK_P_I0			GPIOT_PN_02	T0	R18
BANK2A	GPIOT_P_03		RX_DATA_P2_I0			GPIOT_PN_03	T0	P21
BANK2A	GPIOT_N_03		RX_DATA_N2_I0			GPIOT_PN_03	T0	P20
BANK2A	GPIOT_N_04		RX_DATA_N3_I0			GPIOT_PN_04	T0	N23
BANK2A	GPIOT_P_04		RX_DATA_P3_I0			GPIOT_PN_04	T0	N22
BANK2A	GPIOT_N_05		RX_DATA_N4_I0	VREF2A		GPIOT_PN_05	T0	P19
BANK2A	GPIOT_P_05		RX_DATA_P4_I0			GPIOT_PN_05	T0	P18
BANK2A	GPIOT_P_06		RX_DATA_P0_I1			GPIOT_PN_06	T0	L23

Legend	Bank Number	Powered by:
	BANK2A	VCCIO2A
	BANK2B	VCCIO2B
	BANK2C	VCCIO2C
	BANK3A	VCCIO3A
	BANK3B	VCCIO3B
	BANK3C	VCCIO3C
	BANK4A	VCCIO4A
	BANK4B	VCCIO4B
	BANK4C	VCCIO4C
	BL	VCCIO33_BL
	BR	VCCIO33_BR
	TL	VCCIO33_TL
	TR	VCCIO33_TR
	DDR	

BANK2A	GPLOT_N_06		RX_DATA_N0_I1			GPLOT_PN_06	T0	L22
BANK2A	GPLOT_N_07		RX_DATA_N1_I1			GPLOT_PN_07	T0	N21
BANK2A	GPLOT_P_07		RX_DATA_P1_I1			GPLOT_PN_07	T0	N20
BANK2A	GPLOT_N_08		RX_CLK_N_I1			GPLOT_PN_08	T0	N17
BANK2A	GPLOT_P_08		RX_CLK_P_I1			GPLOT_PN_08	T0	N16
BANK2A	GPLOT_P_09		RX_DATA_P2_I1			GPLOT_PN_09	T0	N19
BANK2A	GPLOT_N_09		RX_DATA_N2_I1			GPLOT_PN_09	T0	N18
BANK2A	GPLOT_N_10		RX_DATA_N3_I1			GPLOT_PN_10	T0	M22
BANK2A	GPLOT_P_10		RX_DATA_P3_I1			GPLOT_PN_10	T0	M21
BANK2B	REF_RES_28							K23
BANK2B	GPLOT_N_11		RX_DATA_N0_I2			GPLOT_PN_11	T0	J23
BANK2B	GPLOT_P_11_PLLI0		RX_DATA_P0_I2		DDR_PLL_REFCLK	GPLOT_PN_11	T0	J22
BANK2B	GPLOT_P_12_EXTFB		RX_DATA_P1_I2			GPLOT_PN_12	T0	G22
BANK2B	GPLOT_N_12		RX_DATA_N1_I2			GPLOT_PN_12	T0	H22
BANK2B	GPLOT_P_13_CLK16_P		RX_CLK_P_I2			GPLOT_PN_13	T0	M19
BANK2B	GPLOT_N_13_CLK16_N		RX_CLK_N_I2			GPLOT_PN_13	T0	M18
BANK2B	GPLOT_P_14_CLK17_P		RX_DATA_P2_I2			GPLOT_PN_14	T0	L21
BANK2B	GPLOT_N_14_CLK17_N		RX_DATA_N2_I2			GPLOT_PN_14	T0	K21
BANK2B	GPLOT_N_15_CLK18_N		RX_DATA_N3_I2			GPLOT_PN_15	T0	G23
BANK2B	GPLOT_P_15_CLK18_P		RX_DATA_P3_I2			GPLOT_PN_15	T0	F23
BANK2B	GPLOT_P_16_CLK19_P		RX_DATA_P4_I2		VREF2B	GPLOT_PN_16	T0	L16
BANK2B	GPLOT_N_16_CLK19_N		RX_DATA_N4_I2			GPLOT_PN_16	T0	M16
BANK2B	GPLOT_P_17_CLK20_P		RX_DATA_P0_I3			GPLOT_PN_17	T0,T1	E23
BANK2B	GPLOT_N_17_CLK20_N		RX_DATA_N0_I3			GPLOT_PN_17	T0,T1	E22
BANK2B	GPLOT_N_18_CLK21_N		RX_DATA_N1_I3			GPLOT_PN_18	T1	J21
BANK2B	GPLOT_P_18_CLK21_P		RX_DATA_P1_I3			GPLOT_PN_18	T1	J20
BANK2B	GPLOT_P_19_CLK22_P		RX_CLK_P_I3			GPLOT_PN_19	T1	L18
BANK2B	GPLOT_N_19_CLK22_N		RX_CLK_N_I3			GPLOT_PN_19	T1	L17
BANK2B	GPLOT_P_20_CLK23_P		RX_DATA_P2_I3			GPLOT_PN_20	T1	G21
BANK2B	GPLOT_N_20_CLK23_N		RX_DATA_N2_I3			GPLOT_PN_20	T1	F21
BANK2B	GPLOT_N_21		RX_DATA_N3_I3			GPLOT_PN_21	T1	K19
BANK2B	GPLOT_P_21		RX_DATA_P3_I3			GPLOT_PN_21	T1	K20
BANK2B	GPLOT_P_22		RX_DATA_P4_I3			GPLOT_PN_22	T1	L20
BANK2B	GPLOT_N_22		RX_DATA_N4_I3			GPLOT_PN_22	T1	L19
BANK2C	GPLOT_P_23_PLLI0		RX_DATA_P0_I4		DDR_PLL_REFCLK	GPLOT_PN_23	T1	D23
BANK2C	GPLOT_N_23		RX_DATA_N0_I4			GPLOT_PN_23	T1	C23
BANK2C	GPLOT_P_24_EXTFB		RX_DATA_P1_I4			GPLOT_PN_24	T1	B23
BANK2C	GPLOT_N_24		RX_DATA_N1_I4			GPLOT_PN_24	T1	B22
BANK2C	GPLOT_P_25		RX_CLK_P_I4			GPLOT_PN_25	T1	K18
BANK2C	GPLOT_N_25		RX_CLK_N_I4			GPLOT_PN_25	T1	K17
BANK2C	GPLOT_P_26		RX_DATA_P2_I4			GPLOT_PN_26	T1	H20
BANK2C	GPLOT_N_26		RX_DATA_N2_I4			GPLOT_PN_26	T1	G20
BANK2C	GPLOT_N_27		RX_DATA_N3_I4			GPLOT_PN_27	T1	E20
BANK2C	GPLOT_P_27		RX_DATA_P3_I4			GPLOT_PN_27	T1	E21
BANK2C	GPLOT_P_28		RX_DATA_P4_I4		VREF2C	GPLOT_PN_28	T1	J18
BANK2C	GPLOT_N_28		RX_DATA_N4_I4			GPLOT_PN_28	T1	J17
BANK2C	GPLOT_P_29		RX_DATA_P0_I5			GPLOT_PN_29	T1	F19
BANK2C	GPLOT_N_29		RX_DATA_N0_I5			GPLOT_PN_29	T1	E19
BANK2C	GPLOT_N_30		RX_DATA_N1_I5			GPLOT_PN_30	T1	D20
BANK2C	GPLOT_P_30		RX_DATA_P1_I5			GPLOT_PN_30	T1	D21
BANK2C	GPLOT_P_31		RX_CLK_P_I5			GPLOT_PN_31	T1	H19
BANK2C	GPLOT_N_31		RX_CLK_N_I5			GPLOT_PN_31	T1	H18
BANK2C	GPLOT_P_32		RX_DATA_P2_I5			GPLOT_PN_32	T1	C21
BANK2C	GPLOT_N_32		RX_DATA_N2_I5			GPLOT_PN_32	T1	C22
BANK2C	GPLOT_N_33		RX_DATA_N3_I5			GPLOT_PN_33	T1	G17
BANK2C	GPLOT_P_33		RX_DATA_P3_I5			GPLOT_PN_33	T1	F17
BANK2C	GPLOT_P_34		RX_DATA_P4_I5			GPLOT_PN_34	T1	G19
BANK2C	GPLOT_N_34		RX_DATA_N4_I5			GPLOT_PN_34	T1	G18
BANK2C	REF_RES_2C							A22
TR	GPIOR_56						R14	E17
TR	GPIOR_61						R15	G15
TR	GPIOR_63						R15	E15
TR	GPIOR_58						R14	H15
TR	GPIOR_59						R15	E16
TR	GPIOR_57						R14	F15
TR	GPIOR_60						R15	G16
BANK3A	REF_RES_3A							E18
BANK3A	GPIOR_N_45		RX_DATA_N3_I6			GPIOR_PN_45	R14	B20
BANK3A	GPIOR_P_45_PLLI0		RX_DATA_P3_I6			GPIOR_PN_45	R14	C20
BANK3A	GPIOR_N_44		RX_DATA_N2_I6			GPIOR_PN_44	R14	B19
BANK3A	GPIOR_P_44_EXTFB		RX_DATA_P2_I6			GPIOR_PN_44	R14	C19
BANK3A	GPIOR_N_43		RX_CLK_N_I6			GPIOR_PN_43	R13,R14	D18
BANK3A	GPIOR_P_43		RX_CLK_P_I6			GPIOR_PN_43	R13,R14	D17
BANK3A	GPIOR_P_42		RX_DATA_P1_I6			GPIOR_PN_42	R13	A21
BANK3A	GPIOR_N_42		RX_DATA_N1_I6			GPIOR_PN_42	R13	A20
BANK3A	GPIOR_N_41		RX_DATA_N0_I6			GPIOR_PN_41	R13	C18
BANK3A	GPIOR_P_41		RX_DATA_P0_I6		VREF3A	GPIOR_PN_41	R13	B18
BANK3A	GPIOR_N_40		RX_DATA_N7_I7			GPIOR_PN_40	R13	C17
BANK3A	GPIOR_P_40		RX_DATA_P7_I7			GPIOR_PN_40	R13	C16
BANK3A	GPIOR_N_39		RX_DATA_N6_I7			GPIOR_PN_39	R12	A18
BANK3A	GPIOR_P_39		RX_DATA_P6_I7			GPIOR_PN_39	R12	A17
BANK3A	GPIOR_P_38		RX_DATA_P5_I7			GPIOR_PN_38	R12	A16
BANK3A	GPIOR_N_38		RX_DATA_N5_I7			GPIOR_PN_38	R12	B16
BANK3A	GPIOR_N_37		RX_DATA_N4_I7			GPIOR_PN_37	R12	D15
BANK3A	GPIOR_P_37		RX_DATA_P4_I7			GPIOR_PN_37	R12	C15
BANK3A	GPIOR_N_36		RX_CLK_N_I7			GPIOR_PN_36	R11,R12	A15
BANK3A	GPIOR_P_36		RX_CLK_P_I7			GPIOR_PN_36	R11,R12	B15
BANK3B	REF_RES_3B							C14
BANK3B	GPIOR_N_31		RX_DATA_N5_I8			GPIOR_PN_31	R10	D14
BANK3B	GPIOR_P_31_PLLI1		RX_DATA_P5_I8			GPIOR_PN_31	R10	E14
BANK3B	GPIOR_N_30		RX_DATA_N4_I8			GPIOR_PN_30	R10	H14
BANK3B	GPIOR_P_30		RX_DATA_P4_I8			GPIOR_PN_30	R10	G14
BANK3B	GPIOR_N_29		RX_DATA_N3_I8			GPIOR_PN_29	R9	E13
BANK3B	GPIOR_P_29		RX_DATA_P3_I8			GPIOR_PN_29	R9	F13
BANK3B	GPIOR_P_28		RX_CLK_P_I8			GPIOR_PN_28	R9	B13
BANK3B	GPIOR_N_28		RX_CLK_N_I8			GPIOR_PN_28	R9	C13
BANK3B	GPIOR_N_27_CLK8_N		RX_DATA_N2_I8			GPIOR_PN_27	R9	G13
BANK3B	GPIOR_P_27_CLK8_P		RX_DATA_P2_I8			GPIOR_PN_27	R9	H13
BANK3B	GPIOR_N_26_CLK9_N		RX_DATA_N1_I8			GPIOR_PN_26	R8,R9	A14
BANK3B	GPIOR_P_26_CLK9_P		RX_DATA_P1_I8			GPIOR_PN_26	R8,R9	A13
BANK3B	GPIOR_N_25_CLK10_N		RX_DATA_N0_I8			GPIOR_PN_25	R8	C12
BANK3B	GPIOR_P_25_CLK10_P		RX_DATA_P0_I8			GPIOR_PN_25	R8	D12
BANK3B	GPIOR_P_24_CLK11_P		RX_DATA_P7_I9			GPIOR_PN_24	R8	G12
BANK3B	GPIOR_N_24_CLK11_N		RX_DATA_N7_I9			GPIOR_PN_24	R8	H12
BANK3B	GPIOR_N_23_CLK12_N		RX_DATA_N6_I9			GPIOR_PN_23	R7,R8	E12
BANK3B	GPIOR_P_23_CLK12_P		RX_DATA_P6_I9		VREF3B	GPIOR_PN_23	R7,R8	E11
BANK3B	GPIOR_N_22_CLK13_N		RX_DATA_N5_I9			GPIOR_PN_22	R7	A12
BANK3B	GPIOR_P_22_CLK13_P		RX_DATA_P5_I9			GPIOR_PN_22	R7	B12
BANK3B	GPIOR_N_21_CLK14_N		RX_DATA_N4_I9			GPIOR_PN_21	R7	F11
BANK3B	GPIOR_P_21_CLK14_P		RX_DATA_P4_I9			GPIOR_PN_21	R7	G11
BANK3B	GPIOR_P_20_CLK15_P		RX_CLK_P_I9			GPIOR_PN_20	R6,R7	C11
BANK3B	GPIOR_N_20_CLK15_N		RX_CLK_N_I9			GPIOR_PN_20	R6,R7	D11
BANK3B	GPIOR_N_19		RX_DATA_N3_I9			GPIOR_PN_19	R6	A11
BANK3B	GPIOR_P_19		RX_DATA_P3_I9			GPIOR_PN_19	R6	A10
BANK3B	GPIOR_N_18		RX_DATA_N2_I9			GPIOR_PN_18	R6	H10
BANK3B	GPIOR_P_18		RX_DATA_P2_I9			GPIOR_PN_18	R6	H11
BANK3B	GPIOR_N_17		RX_DATA_N1_I9			GPIOR_PN_17	R6	B10
BANK3B	GPIOR_P_17		RX_DATA_P1_I9			GPIOR_PN_17	R6	C10

BANK3B	GPOR_P_16_PLLIN1			RX_DATA_P0_I9			GPOR_PN_16	R5	E10
BANK3B	GPOR_N_16			RX_DATA_N0_I9			GPOR_PN_16	R5	F10
BANK3C	GPOR_P_08			RX_DATA_P7_I11			GPOR_PN_08	R3	E9
BANK3C	GPOR_N_08			RX_DATA_N7_I11			GPOR_PN_08	R3	D9
BANK3C	GPOR_P_07			RX_DATA_P6_I11	VREF3C		GPOR_PN_07	R3	B9
BANK3C	GPOR_P_06			RX_DATA_P5_I11			GPOR_PN_06	R2	A8
BANK3C	GPOR_N_06			RX_DATA_N5_I11			GPOR_PN_06	R2	A9
BANK3C	GPOR_P_05			RX_DATA_P4_I11			GPOR_PN_05	R2	E8
BANK3C	GPOR_N_05			RX_DATA_N4_I11			GPOR_PN_05	R2	D8
BANK3C	GPOR_P_04			RX_CLK_P_I11			GPOR_PN_04	R2	C7
BANK3C	GPOR_N_04			RX_CLK_N_I11			GPOR_PN_04	R2	C8
BANK3C	GPOR_P_03			RX_DATA_P3_I11			GPOR_PN_03	R1, R2	A7
BANK3C	GPOR_N_03			RX_DATA_N3_I11			GPOR_PN_03	R1, R2	B7
BANK3C	GPOR_P_02			RX_DATA_P2_I11			GPOR_PN_02	R1	E6
BANK3C	GPOR_N_02			RX_DATA_N2_I11			GPOR_PN_02	R1	E7
BANK3C	GPOR_P_01_EXTF8			RX_DATA_P1_I11			GPOR_PN_01	R1	A5
BANK3C	GPOR_N_01			RX_DATA_N1_I11			GPOR_PN_01	R1	A6
BANK3C	GPOR_P_00_PLLIN0			RX_DATA_P0_I11			GPOR_PN_00	R1	C5
BANK3C	GPOR_N_00			RX_DATA_N0_I11			GPOR_PN_00	R1	C6
BANK3C	REF_RES_3C								D6
BR	GPOR_65							R0	G9
BR	GPOR_70							R0	G7
BR	GPOR_69							R0	H8
BR	GPOR_68							R0	F7
BR	GPOR_71							R0	H9
BR	GPOR_72							R0	F9
BR	GPOR_66							R0	G8
BANK4A	REF_RES_4A								B5
BANK4A	GPOR_P_34			RX_DATA_P4_I12			GPOR_PN_34	B1	F5
BANK4A	GPOR_N_34			RX_DATA_N4_I12			GPOR_PN_34	B1	G5
BANK4A	GPOR_P_33_CDI31	CDI31		RX_DATA_P3_I12			GPOR_PN_33	B1	A4
BANK4A	GPOR_N_33_CDI30	CDI30		RX_DATA_N3_I12			GPOR_PN_33	B1	A3
BANK4A	GPOR_P_32_CDI29	CDI29		RX_DATA_P2_I12			GPOR_PN_32	B1	B4
BANK4A	GPOR_N_32_CDI28	CDI28		RX_DATA_N2_I12			GPOR_PN_32	B1	C4
BANK4A	GPOR_P_31_CDI27	CDI27		RX_CLK_P_I12			GPOR_PN_31	B1	G6
BANK4A	GPOR_N_31_CDI26	CDI26		RX_CLK_N_I12			GPOR_PN_31	B1	H6
BANK4A	GPOR_P_30_CDI25	CDI25		RX_DATA_P1_I12			GPOR_PN_30	B1	D5
BANK4A	GPOR_N_30_CDI24	CDI24		RX_DATA_N1_I12			GPOR_PN_30	B1	E5
BANK4A	GPOR_P_29_CDI23	CDI23		RX_DATA_P0_I12			GPOR_PN_29	B1	A2
BANK4A	GPOR_N_29_CDI22	CDI22		RX_DATA_N0_I12			GPOR_PN_29	B1	B2
BANK4A	GPOR_P_28_CDI21	CDI21		RX_DATA_P4_I13	VREF4A		GPOR_PN_28	B1	J7
BANK4A	GPOR_N_28_CDI20	CDI20		RX_DATA_N4_I13			GPOR_PN_28	B1	J6
BANK4A	GPOR_P_27_CDI19	CDI19		RX_DATA_P3_I13			GPOR_PN_27	B1	C3
BANK4A	GPOR_N_27_CDI18	CDI18		RX_DATA_N3_I13			GPOR_PN_27	B1	C2
BANK4A	GPOR_P_26_CDI17	CDI17		RX_DATA_P2_I13			GPOR_PN_26	B1	D3
BANK4A	GPOR_N_26_CDI16	CDI16		RX_DATA_N2_I13			GPOR_PN_26	B1	D2
BANK4A	GPOR_P_25_CDI15	CDI15		RX_CLK_P_I13			GPOR_PN_25	B1	E4
BANK4A	GPOR_N_25_CDI14	CDI14		RX_CLK_N_I13			GPOR_PN_25	B1	F4
BANK4A	GPOR_P_24_EXTF8			RX_DATA_P1_I13			GPOR_PN_24	B1	B1
BANK4A	GPOR_N_24_CDI13	CDI13		RX_DATA_N1_I13			GPOR_PN_24	B1	C1
BANK4A	GPOR_P_23_CDI12	CDI12		RX_DATA_P0_I13			GPOR_PN_23	B1	E3
BANK4A	GPOR_N_23_PLLIN0			RX_DATA_N0_I13			GPOR_PN_23	B1	E2
BANK4B	GPOR_P_22_CDI11	CDI11		RX_DATA_P4_I14			GPOR_PN_22	B1	K6
BANK4B	GPOR_N_22_CDI10	CDI10		RX_DATA_N4_I14			GPOR_PN_22	B1	L6
BANK4B	GPOR_P_21_CDI9	CDI9		RX_DATA_P3_I14			GPOR_PN_21	B1	H5
BANK4B	GPOR_N_21_CDI8			RX_DATA_N3_I14			GPOR_PN_21	B1	J5
BANK4B	GPOR_P_20_CDI7_CLK7_N	CDI7		RX_DATA_P2_I14			GPOR_PN_20	B1	J4
BANK4B	GPOR_N_20_CDI6_CLK7_P	CDI6		RX_DATA_N2_I14			GPOR_PN_20	B1	K4
BANK4B	GPOR_P_19_CDI5_CLK6_P	CDI5		RX_CLK_P_I14			GPOR_PN_19	B1	L7
BANK4B	GPOR_N_19_CDI4_CLK6_N	CDI4		RX_CLK_N_I14			GPOR_PN_19	B1	L8
BANK4B	GPOR_P_18_CLK5_P			RX_DATA_P1_I14			GPOR_PN_18	B1	F3
BANK4B	GPOR_N_18_CLK5_N			RX_DATA_N1_I14			GPOR_PN_18	B1	G3
BANK4B	GPOR_P_17_CLK4_N			RX_DATA_P0_I14			GPOR_PN_17	B0, B1	H3
BANK4B	GPOR_N_17_CLK4_P			RX_DATA_N0_I14			GPOR_PN_17	B0, B1	H2
BANK4B	GPOR_P_16_EXTPCLK_CLK3_P	EXT_CONFIG_CLK		RX_DATA_P4_I15	VREF4B		GPOR_PN_16	B0	L4
BANK4B	GPOR_N_16_CLK3_N			RX_DATA_N4_I15			GPOR_PN_16	B0	L5
BANK4B	GPOR_P_15_CLK2_P			RX_DATA_P3_I15			GPOR_PN_15	B0	J3
BANK4B	GPOR_N_15_CLK2_N			RX_DATA_N3_I15			GPOR_PN_15	B0	J2
BANK4B	GPOR_P_14_TEST_N_CLK1_N	TEST_N		RX_DATA_P2_I15			GPOR_PN_14	B0	K3
BANK4B	GPOR_N_14_TEST_N_CLK1_P	TEST_N		RX_DATA_N2_I15			GPOR_PN_14	B0	L3
BANK4B	GPOR_P_13_CBSEL0_CLK0_P	CBSEL0		RX_CLK_P_I15			GPOR_PN_13	B0	M6
BANK4B	GPOR_N_13_CBSEL1_CLK0_N	CBSEL1		RX_CLK_N_I15			GPOR_PN_13	B0	M7
BANK4B	GPOR_P_12_EXTF8			RX_DATA_P1_I15			GPOR_PN_12	B0	E1
BANK4B	GPOR_N_12_SSU_N	SSU_N		RX_DATA_N1_I15			GPOR_PN_12	B0	F1
BANK4B	GPOR_P_11_PLLIN0			RX_DATA_P0_I15			GPOR_PN_11	B0	G2
BANK4B	GPOR_N_11_PLLIN0			RX_DATA_N0_I15			GPOR_PN_11	B0	G1
BANK4B	REF_RES_4B								J1
BANK4C	GPOR_P_10_CDI3	CDI3		RX_DATA_P4_I16			GPOR_PN_10	B0	K1
BANK4C	GPOR_N_10_CDI2	CDI2		RX_DATA_N4_I16			GPOR_PN_10	B0	L1
BANK4C	GPOR_P_09_CDI1	CDI1		RX_DATA_P3_I16			GPOR_PN_09	B0	M2
BANK4C	GPOR_N_09_CDI0	CDI0		RX_DATA_N3_I16			GPOR_PN_09	B0	L2
BANK4C	GPOR_P_08_SSL_N	SSL_N		RX_DATA_P2_I16	VREF4C		GPOR_PN_08	B0	N4
BANK4C	GPOR_N_08			RX_DATA_N2_I16			GPOR_PN_08	B0	M4
BANK4C	GPOR_P_07			RX_CLK_P_I16			GPOR_PN_07	B0	N1
BANK4C	GPOR_N_07			RX_CLK_N_I16			GPOR_PN_07	B0	N2
BANK4C	GPOR_P_06			RX_DATA_P1_I16			GPOR_PN_06	B0	P3
BANK4C	GPOR_N_06			RX_DATA_N1_I16			GPOR_PN_06	B0	N3
BANK4C	GPOR_P_05			RX_DATA_P0_I16			GPOR_PN_05	B0	N5
BANK4C	GPOR_N_05			RX_DATA_N0_I16			GPOR_PN_05	B0	N6
BANK4C	GPOR_P_04			RX_DATA_P3_I17			GPOR_PN_04	B0	P5
BANK4C	GPOR_N_04			RX_DATA_N3_I17			GPOR_PN_04	B0	P6
BANK4C	GPOR_P_03			RX_DATA_P2_I17			GPOR_PN_03	B0	R1
BANK4C	GPOR_N_03			RX_DATA_N2_I17			GPOR_PN_03	B0	R1
BANK4C	GPOR_P_02_CSI	CSI		RX_CLK_P_I17			GPOR_PN_02	B0	P1
BANK4C	GPOR_N_02_CSI	CSI		RX_CLK_N_I17			GPOR_PN_02	B0	P8
BANK4C	GPOR_P_01_EXTF8			RX_DATA_P1_I17			GPOR_PN_01	B0	R2
BANK4C	GPOR_N_01_CCK	CCK		RX_DATA_N1_I17			GPOR_PN_01	B0	R3
BANK4C	GPOR_P_00_PLLIN0			RX_DATA_P0_I17			GPOR_PN_00	B0	R5
BANK4C	GPOR_N_00_PLLIN0			RX_DATA_N0_I17			GPOR_PN_00	B0	R4
BANK4C	CRESET_N	CRESET_N							R6
BANK4C	CDONE	CDONE							T6
BANK4C	REF_RES_4C								T2
-	VCC								J9
-	VCC								J11
-	VCC								J13
-	VCC								J15
-	VCC								K10
-	VCC								K12
-	VCC								K14
-	VCC								L9
-	VCC								L11
-	VCC								L13
-	VCC								L15
-	VCC								M10
-	VCC								M12
-	VCC								M14
-	VCC								N9

-	VCC						N11
-	VCC						N13
-	VCC						N15
-	VCC						T8
-	VCC						T16
-	VCC						U8
-	VCC						U16
-	VCCA_BL						R7
-	VCCA_BR						J8
-	VCCA_TL						R17
-	VCCA_TR						H16
-	VCCAUX						K8
-	VCCAUX						J16
-	VCCAUX						R8
-	VCCAUX						R16
-	VCCIO2A						P17
-	VCCIO2A						P22
-	VCCIO2A						R20
-	VCCIO2B						H21
-	VCCIO2B						K22
-	VCCIO2B						M17
-	VCCIO2C						F18
-	VCCIO2C						H17
-	VCCIO2C						K16
-	VCCIO33_BL						U4
-	VCCIO33_BL						V2
-	VCCIO33_BR						F8
-	VCCIO33_TL						U22
-	VCCIO33_TL						U19
-	VCCIO33_TR						F16
-	VCCIO3A						B17
-	VCCIO3A						D16
-	VCCIO3B						D13
-	VCCIO3B						F12
-	VCCIO3B						F14
-	VCCIO3B						G10
-	VCCIO3C						B8
-	VCCIO3C						D7
-	VCCIO4A						D4
-	VCCIO4A						F6
-	VCCIO4A						G4
-	VCCIO4B						K7
-	VCCIO4B						M5
-	VCCIO4B						M8
-	VCCIO4C						N7
-	VCCIO4C						P2
-	VCCIO4C						P4
-	VDD_PHY						P9
-	VDD_PHY						P11
-	VDD_PHY						P12
-	VDD_PHY						P14
-	VDD_PHY						P15
-	VDD_PHY						R10
-	VDD_PHY						R11
-	VDD_PHY						R13
-	VDD_PHY						R14
-	VDD_PHY						T11
-	VDD_PHY						T13
-	VDD_PHY						T14
-	VDD_PHY						T15
-	VDD_PHY						T9
-	VDD_PHY						T10
-	VDDPLL_MCB_TOP_PHY						U12
-	VDDQ_CK_PHY						T12
-	VDDQ_PHY						AB6
-	VDDQ_PHY						AB10
-	VDDQ_PHY						AB14
-	VDDQ_PHY						AB18
-	VDDQ_PHY						W7
-	VDDQ_PHY						W12
-	VDDQ_PHY						W18
-	VDDQ_PHY						Y1
-	VDDQ_PHY						Y3
-	VDDQ_PHY						Y13
-	VDDQ_PHY						Y19
-	VDDQ_PHY						Y21
-	VDDQ_PHY						Y23
-	VDDQ_PHY						Y5
-	VDDQ_PHY						Y11
-	VDDQX_PHY						U9
-	VDDQX_PHY						U10
-	VDDQX_PHY						U13
-	VDDQX_PHY						V11
-	VDDQX_PHY						V12
-	VDDQX_PHY						V14
-	VDDQX_PHY						U15
-	VDDQX_PHY						V9
-	VQPS						U7
-	GND						A1
-	GND						A19
-	GND						A23
-	GND						AA19
-	GND						AB2
-	GND						AC4
-	GND						AB5
-	GND						AB7
-	GND						AC9
-	GND						AB11
-	GND						AB13
-	GND						AC15
-	GND						AB17
-	GND						AC20
-	GND						AB22
-	GND						AC1
-	GND						AC23
-	GND						B3
-	GND						B6
-	GND						B11
-	GND						B14
-	GND						B21
-	GND						D1
-	GND						D10
-	GND						D19
-	GND						D22
-	GND						F2
-	GND						F20
-	GND						F22
-	GND						H1

-	GND							H4
-	GND							H7
-	GND							H23
-	GND							J10
-	GND							J12
-	GND							J14
-	GND							J19
-	GND							K2
-	GND							K5
-	GND							K9
-	GND							K11
-	GND							K13
-	GND							K15
-	GND							L10
-	GND							L12
-	GND							L14
-	GND							M1
-	GND							M3
-	GND							M9
-	GND							M11
-	GND							M13
-	GND							M15
-	GND							M20
-	GND							M23
-	GND							N8
-	GND							N10
-	GND							P10
-	GND							P13
-	GND							P16
-	GND							R9
-	GND							R12
-	GND							R15
-	GND							T1
-	GND							T7
-	GND							T17
-	GND							T21
-	GND							T23
-	GND							U11
-	GND							U14
-	GND							V7
-	GND							V10
-	GND							V13
-	GND							V15
-	GND							V17
-	GND							Y2
-	GND							Y4
-	GND							Y6
-	GND							Y8
-	GND							Y10
-	GND							N12
-	GND							N14
-	GND							Y12
-	GND							Y14
-	GND							Y16
-	GND							Y18
-	GND							Y20
-	GND							Y22
-	NC							W10
-	NC							W11
-	NC							W13
-	NC							W14
-	NC							V16
-	NC							W15