

Dr.ATUL KUMAR DWIVEDI

Personal

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- ☞ Aadhaar number :265887722634
 - ☞ Passport Number :
 - ☞ PAN Number :ASHPD4461B
 - ☞ Date of birth : 25th August 1986
 - ☞ Father's Name : DharmpalDwivedi
 - ☞ Mother's Name : Suman Devi
 - ☞ Marital status : Married
 - ☞ Spouse Name : PoornimaDwivedi
 - ☞ C. G. Address : Village post Sildaha, District:Mungeli (Bilaspur), C. G. 495335
 - ☞ Jhansi Address :c/o Dr. Vihangesh Kumar Dixit DL/23B Veerangananagar Kanpur
road Jhansi (U.P) PIN 284128
 - ☞ Mobile no : 9713424754
 - ☞ Email : atuldwivedi@live.in
 - ☞ Nationality : Indian
 - ☞ Linguistic abilities : Hindi (Read, Write, Speak), English(Read, Write, Speak),
 - ☞ Teaching Interests : Integrated Circuits, Electronic Devices and Circuits ,Signals and Systems,
CMOSDesign, MEMS, VLSI Signal and image processing
 - ☞ Software Expertise : C, C++, Data structures,MATLAB, Assembly, VHDL, Verilog,Simulation
Tool Simulink, Comsolmultiphysics,Coventorware, Cadence, ModelSim,
Xilinx ISE
 - ☞ Research Interests :Evolutionary optimization, Digital Filter design, FPGA,VLSI, Bio-
medical signal andImageProcessing; MEMS design

Academics

☞ NET :

- Exam name : National Eligibility Test (NET)
- University : University Grant Commission (U.G. C.), India
- Year : 2012
- Branch/Subjects : Electronics

- Roll no : 47880011
- Marks obtained : NA
- Total Marks : NA
- Percentage :NA

☪ GATE :

- Exam name : GATE
- University : M. H. R. D. India
- Year : 2009
- Branch/Subjects : Electronics
- Roll no : EC6670119
- Marks obtained : 499
- Total Marks : 100
- Percentage :96.29

☪ Doctorate :

- Exam name :Doctor of Philosophy
- Passing year : 2017
- Branch/Subjects :VLSI Signal Processing
- Title : Optimization Based Design and Implementation of Digital FIR Filters
- Advisor : Dr.Subhojit Ghosh
- Co-Advisor : Dr.Narendra D. Londhe
- Roll no : 13317002
- Certificate no :Examcell/PhD/PDC/974
- College :National Institute of Technology (NIT),Raipur,India
- University : National Institute of Technology (NIT), Raipur, India
- CGPA(Marks) : 8.74
- Total Marks : 10
- Percentage :87.4
- Admission date : 05/08/2013
- RDC date : 05/01/2015
- Submission date : 06/10/2016
- Completion date : 17/03/2017

☪ Post Graduation :

- Exam name :Master of Technology (M. Tech.)
- Year :2011

- Branch/Subjects :VLSI Design
- Roll no : MT09MVD003
- Certificate No : MT09MVD003
- Thesis title : Microfluidic Comb Sensor Design for E. Coli Bacteria in water
- College :Visvesvaraya National Institute of Technology, Nagpur(VNIT Nagpur)
 ,India
- University :Visvesvaraya National Institute of Technology, Nagpur(VNIT Nagpur)
 ,India
- Supervisor : Prof.R.B.Deshmukh
- Co-Supervisor : Prof.R.M.Patrikar
- Marks obtained : 8.74
- Total Marks : 10
- Percentage :87.4
- Class/division :Honours
- Date of admission: 1 July 2009
- Date of Completion: 30 June 2011

☪ Graduation :

- Exam name : Bachelor of Engineering (B. E.)
- Passing Month :06
- Year :2009
- Branch/Subjects :Electronics and Telecommunication Engineering
- Roll no :3142805054
- Certificate No : 77985
- College :Shri Shankaracharya College of Engineering andTechnology,
 Bhilai
- University : Chhattisgarh Swami Vivekananda Technical University,
 Bhilai (C. G.)
- Marks obtained : 5551
- Total Marks : 7000
- CPI :8.71
- Percentage :79.30
- Class/division : Honours

☪ 12thStandard :

- Exam name :HigherSecondarySchoolCertificate Examination(HSSCE)
- Year :2004

- Roll no :22315052
- Certificate No. : 040845155
- Subjects : Physics, Chemistry, Biology, Maths,Hindi,English
- School : G.H.S.S. S.Bilaspur (Chhattisgarh)
- Board : Chhattisgarh Board of Secondary Education Raipur, India
- Marks obtained : 445
- Total Marks : 500
- Percentage :89.00%
- Class/division : First

☉ 10thStandard :

- Exam name : High School Certificate Examination (HSCE)
- Year : 2002
- Certificate no : 02254227
- Roll No : 12129763
- Subjects :Hindi, Sanskrit, maths, social science, science, english
- School : N.G.H.S.S. T, Tikamgarh (M.P.),
- Board : Madhya Pradesh Board of Secondary Education Bhopal,
- Marks obtained : 404
- Total Marks : 500
- Percentage :80.80%
- Class/division : First

Experience

☉ Teaching **BIET Jhansi**

- College name : Bundelkhand Institute of Engineering &Technology, Jhansi (India)
- University: Dr. A. P. J. Abdul Kalam Technical University, Lucknow
- DOJ : 11/12/ 2017 to till now
- Subjects taught : Basic electronics, Analog Electronics, Integrated Circuits
- Designation : Assistant Professor
- Pay Scale :
- Pay scale : 15600-39100 AGP 6000/-
- Total Salary :
- Basic pay : 61200
- Nature of work : Teaching/Research

- Certificate no :

☪ Teaching **BIT Durg**

- College name : Bhilai Institute of Technology, Durg (India)
- University : Chhattisgarh swami Vivekananda Technical University, Bhilai
- DOJ : 17/07/ 2012 to 07/12/2017,
- Subjects taught : Basic electronics, Analog Electronics, Digital electronics, Signals and Systems, C, C++
- Designation : Assistant Professor
- Pay scale : 15600-600-45600/6000
- Total Salary : 36144
- Basic pay : 19200/-
- Nature of work : Teaching/Research
- Certificate no :

☪ Teaching **RCET Bhilai**

- College name : Rungta College of engineering and technology, Bhilai (India)
- Designation : Assistant Professor
- DOJ : 01/07/ 2011
- DOL : 17/07/2012.
- Total Experience: 01 year 00 months
- Pay scale : 15600-600-45600
- Basic pay : 15600
- Gross salary : 26,000
- Certificate no : HR/RCET/CERT/16/386

☪ Research :

VNIT Nagpur, National MEMS design centre, research scholar and teaching assistant,

July 2009 to June 2011

NIT Raipur, Junior research fellow:

August 2013 to September 2015; Senior research fellow till October 2016.

Theory Teaching

☪ Academic Year 2018-2019 : Subject: Integrated Circuits (EEC 501)

- Level of Teaching : UG

- Semester: 5
- No of Students Registered: 60
- Credit: 4
- No of hrs allotted per week: 4
- Credit Hrs:16

Research Profiles

☞ Google Scholar id: HhF5BPkAAAAJ

Profile link:<https://scholar.google.com/citations?user=HhF5BPkAAAAJ&hl=en>

☞ Microsoft Researcherid: HhF5BPkAAAAJ

Profile link:<https://academic.microsoft.com/>

☞ Research gate id: Atul_Dwivedi12

Profile link:https://www.researchgate.net/profile/Atul_Dwivedi12

☞ ORCID id: 0000-0003-4221-806X

Profile link:<https://orcid.org/0000-0003-4221-806X>

☞ Web of Science Researcher id: AAI-6103-202

Profile link:<https://publons.com/researcher/3489296/dr-atul-kumar-dwivedi/>

☞ Web of Science Researcher id: AAI-6103-202

Profile link:<https://publons.com/researcher/3489296/dr-atul-kumar-dwivedi/>

☞ Vidvan Profile by NMEICT GOI id: 125387

Profile link:<https://vidwan.inflibnet.ac.in/profile/125387>

☞ Youtube Profile

Profile link:youtube.com/eratuldwivedi

Research Projects

☞ Title: Artificial Intelligence based stress analysis system using electrocardiogram and automated questionnaire,

- Co-PI: Dr. ShahanazAyub, B .I. E. T. Jhansi
- Agency: Dr. A. P. J Abdul Kalam Technical University, Lucknow
- Sanctioned date: 29 June 2020
- Amount sanctioned: 5 lacs

Books/Book Chapters

☯ Book Chapter,

☯ Authors: Atul Kumar Dwivedi, R. N. Patel

Title:Digital Filter Design using Quantum Inspired Multi-Objective Cat Swarm Optimization Algorithm,

Book Name: Quantum Inspired Computational Intelligence,

ISBN: 9780128044094 imprint Morgan Kaufmann,

Publication:Elsevier publication,

September 2016,

pages 327-359.

☯ Book

Author: Atul Kumar Dwivedi, R. B. Deshmukh, R. M. Patrikar

Title:Microfluidic Sensor design for E.Coli Bacteria in Water,

Publisher: Lap lambert academic publishing, Jermamy,

Month and Year: October, 19, 2011,

ISBN: 3845434686

International Journals

1. Krishanu Kundu*, Narendra Nath Pathak and Atul Kumar Dwivedi,

Title: “Optimization of Linear antenna array using Binary Cat Swarm Optimization (BCSO)”,

Journal: International Journal of Sensors, Wireless Communications and Control

Vol Na

Issue: NA

ISSN:2210-3287

Year: 2019

doi:10.2174/2210327909666190710121921

2. Atul KumarDwivedi, Subhojit Ghosh, Narendra D. Londhe,

Title: Review and Analysis of Evolutionary Optimization-Based Techniques for FIR Filter Design,

Journal: Springer’sCircuits, Systems and Signal Processing

Publication:Springer Publication

Vol:37

Issue:10,

ISSN: 1531-5878

Page: 4409–4430

February 2018,

doi:<https://doi.org/10.1007/s00034-018-0772-1>

(SCI/Scopus indexed,

Impact factor 1.694

UGC approved, ID no: 18638

3. Atul Kumar Dwivedi, Babita Sahu,

Title: Enhanced Image Object Recognition system using Correlation Filter based on Optimization,

International Journal of Scientific Research in Computer Science, Engineering and Information Technology (IJSRCSEIT),

UGC approved, UGC journal: 64718;

Certificate no: IJSRCSEIT/certificate/volume 2/issue 2/443

ISSN : 2456-3307,

Volume 2, Issue 2,

pp.895-900,

March-April.2017

Impact factor: **0.876**

4. Atul Kumar Dwivedi, Subhojit Ghosh, Narendra D. Londhe,

Low power 2D FIR Filter Design Using Modified Artificial Bee Colony Algorithm with Experimental Validation Using FPGA,

Journal: IET Science Measurement and Technology, England

ISSN: 17518830, 17518822,

Vol 36

Issue 1,

Pages 156-180.

January 2017

doi:10.1049/iet-smt.2016.0069.

SCI/Scopus indexed,

Impact factor 1.285

UGC approved ID: 30102

5. Atul Kumar Dwivedi, Subhojit Ghosh, Narendra D. Londhe,

Title: A Modified Artificial Bee Colony Optimization Based FIR Filter Design with Experimental Validation Using FPGA,

Journal: IET Signal Processing, England

Vol 10,

issue 8,

ISSN: 1751-9675,

Pages 955-964

October 2016

doi:10.1049/iet-spr.2015.0214.

SCI/Scopus indexed, Impact factor **1.325**,

UGC approved, journal id no: 30103.

6. Atul Kumar Dwivedi, Subhojit Ghosh, Narendra D. Londhe,

Title: Low Power FIR Filter Design Using Hybrid Artificial Bee Colony Algorithm with Experimental Validation over FPGA,

Journal: Springer's Circuits, Systems and Signal Processing

Publication: Springer Publication, Switzerland

Vol 36,

Issue 1,

ISSN: 1531-5878

pages 1–31.

March 2016,

doi:10.1007/s00034-016-0297-4.

(SCI/Scopus indexed),

Impact factor **1.694**

UGC approved, ID no: 18638

7. Atul Kumar Dwivedi, Subhojit Ghosh, Narendra D. Londhe,

Title: Low Power FIR Filter Design Using Modified Multi-objective Artificial Bee Colony Algorithm,

Journal : Engineering Applications of Artificial Intelligence Elsevier Publication

Publication: Elsevier Publication,

Vol 55,

ISSN :0952-1976

October 2016,

pages: 58–69.

doi:10.1016/j.engappai.2016.06.006.

(SCI/Scopus indexed),

Impact factor: **2.368**)

UGC approved, journal id: 19614

8. Atul Kumar Dwivedi, Raghvendra Bhushan,

A Novel Algorithm for Speech Based Authentication of Digital Images Using DWT

Vol 1,

Issue 7,

ISSN: 2348- 4853,

International journal of advanced foundation and research in computer,

July 2014,

page no 54-64.

Impact factor: **3.599**

UGC Approved, journal ID: 16638

International Conferences Published

1. Atul Kumar Dwivedi, Rishabh Kumar Baudha, Poornima Pateriya, C. H. Kamesh Rao

Title: Design of Tapered Shape Notch Cut Multi-slotted patch Antenna for WiMax/Wi-Fi Applications,

Role: **Presented** and Contributed

Conference: International Conference on Power, Control and Computing Technologies (ICPC2T)

Organized by NIT Raipur and IEEE,

ISBN : 978-1-7281-4997-4

Date: 3rd to 5th January 2020,

Pages: 1-6.

Publication: IEEE

Indexing: Scopus

Link: [click here](#)

2. Phani Teja Bankupalli, Subhojit Ghosh, Lalit Kumar Sahu, Atul Kumar Dwivedi

Title: Parameter estimation of PEM Fuel Cell Electrical Equivalent Model using Hybrid Optimization,

Role: **Presented** and Contributed

Conference: Inproceedings of 8th International Conference on Power Systems (ICPS), 2019

Organized by MNIT Jaipur and IEEE,

ISBN : 978-1-7281-4103-9

Date: 20th to 22th December 2019,

Pages: 1-6.

Publication: IEEE

Indexing: Scopus

Link: [click here](#)

3. Atul Kumar Dwivedi

Title: Fractional Delay Finite Impulse Response Filter Synthesis using Differential Search Algorithm,

Conference: Inproceedings of International Conference on information and communication technology for competitive strategies (ICTCS 2019)

Organized by CRC Press and GR Foundation,

ISBN :9781003052098

Date: 13th to 14th December 2019,

Pages: 545-551.

Publication: CRC Press

Indexing: Scopus

Link: [click here](#)

4. Atul Kumar Dwivedi, Sandeep Kumar Bhatt, Subhojit Ghosh

Title: Fractional order Butterworth filter design using Artificial Bee colony algorithm

Conference: In proceedings of IEEE 7th International Symposium on Embedded Computing and System Design (ISED)

Organized by IEEE and NIT, Durgapur, India

ISBN: 2473-9413

Vol :

Date: 18-20 Dec. 2017,

Pages: page 17 (5)

doi: 10.1109/ISED.2017.8303938 ISBN: 247-3-9413

Indexed : Scopus

Link: [click here](#)

5. Atul Kumar Dwivedi, R. Aparna, Sonika Arora,

Title: Fractional Order Digital FIR Filter Design using Binary Cat Swarm Optimization,

Conference: in proceedings of IET international conference on applications of soft computing techniques in engineering and technology,

Organized by IET digital library and St. Peter Engineering college, Hyderabad, India,

Date: 25th to 27th October 2016,

ISBN: 978-1-78561-785-0

Pages: 1-5.

Publication: IET [click here](#)

Indexing:

6. Swagota Bera, Monisha Sharma, S. SubramanyaSikhar, Atul Kumar Dwivedi,

Title: An efficient blind steganalysis using higher order statistics for the neighbourhood difference matrix

Conference: in Proceedings of IEEE international conference on Wireless Communications, Signal Processing and Networking (WiSPNET), Chennai, India

pp 211-215

Date: March 2016,

ISBN: 978-1-4673-9338-6

doi:10.1109/WiSPNET.2016.7566122

7. Atul Kumar Dwivedi, S. Ghosh, N. D. Londhe,

Role: **Presented** and Contributed

Title: Bit Level FIR Filter Optimization Using Hybrid Artificial Bee Colony Algorithm,
in proceedings of 2015 Annual IEEE India Conference (INDICON Dec 2015),

pp. 1–6, Delhi, India

ISBN: 2325-9418

doi:10.1109/INDICON.2015.7443741.

8. A. K. Dwivedi, “IEEE Global early career faculty virtual mini-conference, September 19, 2014

9. Raghvendra Bhushan, Bibhudendra Acharya, Atul Kumar Dwivedi,

Title : A novel data place encryption algorithm,

Conference: in proceedings of IEEE conference on devices, circuits and communications,
September 12, 13, 2014, BIT Mesra, Ranchi, India,

ISBN: 978-1-4799-6052

Date: September 12, 13, 2014,

Pages 1-6

Year: 2014

doi: 10.1109/APS.2014.6905245

10. Atul Kumar Dwivedi, R. B. Deshmukh, R.M. Patrikar,

Title : Modeling and Simulation of Capacitive Sensor for E.Coli bacteria in water ,

Conference: Published in 16th International Workshop on Physics of Semiconductor Devices, IIT
Kanpur, edited by Vandana Singh, Y. N. Mohapatra, B. Mazhari, M. Katiyar, Proceedings of SPIE
Vol. 8549, 854914 · © 2012 SPIE · CCC code: 0277-786/12/\$18 ·

ISBN: 9780819493002

doi: 10.1117/12.927438, OCT 2012.

11. Sumeet Kailash Gupta, Atul Kumar Dwivedi, R. B. Deshmukh, R. M. Patrikar, “Lab on Chip for detection of E.Coli bacteria in water using capacitance modelling”, in proceedings of the 2011 international comsol conference in Bangalore.

12. Atul Kumar Dwivedi, R.B. Deshmukh, R.M. Patrikar,

Role: **Presented** and Contributed

Title: Biodetection using Capacitance modulation,

Conference name: Proceedings of comsol international conference in Bangalore,

Date: November, 17-19, 2010.

Organized By :Comsol Multiphysics

Summary: Sole Author 0; First Author 6, Co-author 2

National conferences

1. Devendra Mehra, Atul Kumar Dwivedi,

Title:“A review on soil classification using artificial intelligence”,

Role: **Presented** and Contributed

Conference Details:in souvenir of national conference on emerging trends in science, engineering and technology (NCETSET-2019), organized by Sagar Institute of Science, Technology and Research, Bhopal, India, 12-13th April 2019

2. Shailbala, Atul Kumar Dwivedi,

Title:“Review of Quadrature Mirror Filter Bank Design using Evolutionary Optimization Algorithms”,

ConferenceDetails:in souvenir of national conference on recent trends in electrical, electronics and communication, Engineering (RTEECE-19), organized by Bundelkhand Institute of Engineering and Technology, Jhansi, India, February 2019, page 16.

ISBN: NA

Date :February 2019

3. Babita Sahu, Atul Kumar Dwivedi,

Title:“Review and Comparative Analysis on Applications of Correlation filter”

Role: **Presented** and Contributed

Conference Details: in proceedings of national conference “BITCON 2017: Recent Challenges in Electronics Engineering for national development”, organized by Bhilai Institute of Technology, Durg, India, March 2017, page no 108-115

ISBN: NA

Date : March 2017

4. Atul Kumar Dwivedi

Title :“Charge based Capacitance measurement (CBCM) circuit design for Capacitive Sensors”

Role: **Presented** and Contributed

Conference Details: *in proceedings of national conference “URVATECH”, organized by SSITM Bhilai, 17th March 2011 to 18th March 2011.*

ISBN:NA

Date :17th March 2011 to 18th March 2011.

1. AmolMorankar, R. M. Patrikar, AtulKumarDwivedi,

Title :“*Design and simulation of MEM resonator on SOI*”

Role: **Presented** and Contributed

ConferenceDetails:National conference “RAMEMS”, being organized by IIT-BHU, Varanasi,

ISBN:9780819493002

Date :7th -9th March 2011.

2. Atul KumarDwivedi,R. B. Deshmukh, R. M. Patrikar,

Title :“*Microfluidic Biosensor for E. Coli in Water*”

Conference Details:National conference “RAMEMS”, being organized by IIT-BHU, Varanasi,

ISBN:9780819493002

Date :7th -9th March 2011.

News/Media Publications

- ☞ 28 December 2017, Nai Dunia Raipur, Bee colony optimization for filtering noise in mobile phones
- ☞ 08 September 2018, Dainik Jagran Jhansi, news related to press conference for workshop on opportunities and challenges in holistic development of Bundelkhand region

Reviewer/Editorial positions

- ☞ Member of editorial board in International Journal of Science & Engineering Development Research, (ISSN : 2455-2631), Member ID : 111242
- ☞ Reviewer in IEEE Transactions on Circuits and Systems II: Express Briefs

- ☞ Program Committee Members in sixth international conference on signal and image processing (SIP 2017), February 25-26, 2017, Sydney, Australia

Membership of Professional Bodies

- ☞ Member, Institute of Electrical and Electronics Engineers (IEEE), world's largest technical professional organization dedicated to advancing technology for the benefit of humanity. Member since: 26 March 2016, Membership no: 92525493
- ☞ Member, International Association of Engineers (IAENG), a non-profit international association for engineers and computer scientists. Member since 17 April 2018, Membership no: 213034
- ☞ Life Member, since 2019, The Indian Society for Technical Education (ISTE), a national, professional, non-profit Society registered under the Indian Societies. Member since 01 July 2019, membership number : LM 128016
- ☞ Life member Indian Science Congress Association (ISCA), a professional body under department of science & technology, ministry of science & technology, government of India, Since 12 June 2019, membership number: L38002

Awards and Accolades

- ☞ Top performing mentor award for mentees as topper/Gold medallist in recognition of the role as mentor for the NPTEL online certification course “Introduction to Programming in C” from July to December 2017
- ☞ 2nd prize in COMSOL Conference Bangalore, October 29-30, 2010.
- ☞ Qualified National Eligibility Test (NET) in Electronics by UGC India, 2012
- ☞ 96.29 percentile in GATE 2009.
- ☞ MHRD Scholarship during M.Tech and Ph.D.
- ☞ 3rd Merit position in 2009 batch of Electronics SSCET Bhilai.
- ☞ 1st position in school in HSSC and SSC exams.

Institutional Administrative Responsibilities

- ☞ Assistant GATE co-ordinator Institute level, BIET Jhansi from 03/08/2019 till now.
- ☞ Warden, Dr. A. P. J. Abdul Kalam Hostel, B. I. E. T. Jhansi, from 02 May 2018 till now.

Responsibilities: Hostel management which includes management related room allotment, formation mess committee, hostel discipline, hostel mess and improvement budget, hostel civil and electrical maintenance etc

- ☛ Member, department of electronics, National Institute of Ranking framework (NIRF) committee, Bhilai Institute of Technology, Durg from 10 October 2016 to April 18 2017.

Responsibilities: To prepare required data for parameter related to teaching, learning and resources (TLR)

- ☛ Member, Research and Development Committee, Bhilai Institute of Technology, Durg from 1 July 2017 to December 07, 2018

Responsibilities: To inspire and assist stakeholders of the institute for research and development. To prepare annual plan and strategy for research growth in the institute. To identify special focus area of research and development, considering availability of resources in the institute.

- ☛ Member, NAAC, Criterion-1, curricular aspects committee, Bhilai Institute of Technology, Durg for NAAC team visit during 21st to 23rd August 2017

Responsibilities: To manage data and documents related to curriculum design and development, curriculum planning and implementation, academic flexibility, curriculum enrichment and feedback system

Departmental Administrative Responsibilities

- ☛ Officer in charge entrepreneurship, department of electronics and communication engineering, BIET Jhansi from 15/03/2019 till now

- ☛ Officer in charge departmental library, department of electronics and communication engineering, BIET Jhansi from 01/03/2018 till now

- ☛ Representative at institute level for placement, department of electronics and Communication engineering, BIET Jhansi from 03/08/2018

- ☛ NBA In-charge criterion-7, department of electronics and communication engineering, BIET Jhansi,

- ☛ NBA In-charge, criterion-2, department of electronics and communication engineering, Bhilai Institute of Technology, Durg from 3 June 2017 to 07 December 2017

Responsibilities: To manage data and documents related to criterion 2 of NBA curriculum design and enrichment.

- ☛ Lab In charge, basic electronics lab, department of electronics and communication engineering, Bhilai institute of technology, Durg during session 2016-2017

Responsibilities: To plan, prepare, analyze, supervise and manage all Laboratory related activities

Conducted/Organized

☛ Convenor, Five days workshop on “Embedded systems for communication engineering”

1. Title : Embedded systems for communication engineering
2. Sponsored by : TEQIP-III
3. Period : 17-21 August 2019
4. Number of participants attended : 36

☛ Member Organizing Committee for ICFCCT-19 Conference,MIET College, UP

1. Title : 2nd International Conference on Future Communication & Computing Technology
2. Sponsored by : Meerut Institute of Technology
3. Period : 17th - 18th July 2019
4. Number of participants attended : 30

☛ Member, Advisory Committee for ICASAE-19,Hotel the Golden Apple, Lucknow

1. Title : International conference on Academic Science And Engineering
2. Sponsored by : Mendeley
3. Period : 07th - 08th June, 2019
4. Number of participants attended : 372

☛ Publicity Chair, International Conference on Advanced Communication & Computational Technology (ICACCT-2019) during 6-7 December 2019, at NIT Kurukshetra

1. Title : N. A.
2. Sponsored by : N. A.
3. Period : N. A.
4. Number of participants attended : N. A.

☛ Conference Secretary, National conference on “Recent trends in electrical, electronics and communication engineering (RTEECE-19), during February 23-24, 2019 at BIET Jhansi

1. Title : Recent trends in electrical, electronics and communication engineering (RTEECE-19).
2. Sponsored by : TEQIP-III
3. Period : 23-24, 2019.
4. Number of participants attended : 40

☛ Member, TPC, International Conference on Advanced Communication & Computational Technology (ICACCT-2019) during 6-7 December 2019, at NIT Kurukshetra

1. Title : N. A.
2. Sponsored by : N. A.
3. Period : N. A.
4. Number of participants attended : N. A.

☞ Co-ordinator, faculty development program on “Recent advances in communication engineering” during December 26-30, 2018 in Bundelkhand Institute of Engineering and technology, Jhansi (U.P.)

1. Title : N. A.
2. Sponsored by : N. A.
3. Period : N. A.
4. Number of participants attended : 40

☞ Member, media and press committee for organizing the workshop on “opportunities and challenges for holistic development of Bundelkhand region”. During September 8-9, 2018 by Bundelkhand Institute of Engineering and Technology, Jhansi

1. Title : N. A.
2. Sponsored by : N. A.
3. Period : N. A.
4. Number of participants attended : 40

☞ Chief faculty co-ordinator in a National level workshop on “Application of Android in designing Biped Robot”, organized by Department of Electronics and Telecommunication, BIT DurgIn association with Innovation cell IIT Bombay during 10-04-2017 to 11-04-2017, number of participants 30.

☞ Member, organizing committee, BITCON 2017, Department of Electronics and Telecommunication, Bhilai Institute of Technology, Durg held on 28th March 2017, participants: 34(Academia), 4(Industry).

☞ Program Committee Members in sixth international conference on signal and image processing (SIP 2017), technically sponsored by AIRCC publishing corporation, February 25-26, 2017, Sydney, Australia

☞ Member program committee, 3rd Annual International workshop on Wireless Communication and Network, December 8-10, 2017, Beijing China

Projects Supervised

Ph. D.

☞ Co-supervisor in project :

- Title: “XYZ”,

- Candidate Name: Devendra Mehra, VLSI Design, department of electronics and communication, Bundelkhand Institute of engineering and technology, Jhansi (U. P.).

○

☞ Co-Supervisor in project: “Low power FIR filter design using sparse representation”, Ph. D.” Savita Shrivastava, department of electronics and communication, Bundelkhand Institute of engineering and technology, Jhansi (U. P.).

☞ Supervisor in project:

- Title: “Low complexity, sharp cut off quadrature mirror filter design”,
- Candidate Name: Ph. D.” Hitendra Sigh, VLSI Design, department of electronics and communication, Bundelkhand Institute of engineering and technology, Jhansi (U. P.).

M. Tech.

☞ Supervisor in project, “Study the effect of different dielectric constant on the same microstrip antenna Design”, session 2018-2019, M. Tech, Zeeshan Alam, Digital communication, Bundelkhand Institute of Engineering and Technology, Jhansi (U.P.) :

Date of completion: 21-08-2019

☞ Supervisor in project, “Quadrature mirror filter design using evolutionary optimization”, session 2018-2019, M. Tech, ShailBala, Digital communication, Bundelkhand Institute of Engineering and Technology, Jhansi (U.P.)

Date of completion: 21-08-2019

☞ Supervisor in project: Brain Tumor Detection Using Correlation Filters, Session 2016-2017, BabitaSahu, M. Tech, Bhilai Institute of Technology Durg, Chhattisgarh Swami Vivekananda Technical University, Bhilai,

Date of completion: 31/10/2017

☞ Joint supervisor in project: Fractional order digital FIR filter design using binary cat swarm optimization, Session 2015-2016, R. Aparna, M. Tech, CSIT Durg, Chhattisgarh Swami Vivekananda Technical University, Bhilai,

B. Tech.

- ☛ Supervisor in project: Speech Steganography using least significant bit method, session 2018-2019, B. Tech, Shivi Gupta, Riya Srivas, Roopam Rajput, Bundelkhand Institute of Engineering and Technology, Jhansi (U.P.)
- ☛ Supervisor in project: Design of Pseudo Random Pattern Generator using LFSR and its application in CDMA, session 2018-2019, B. Tech, Samir Anand, Tushar Gupta, Pramod Kumar, Bundelkhand Institute of Engineering and Technology, Jhansi (U.P.)
- ☛ Supervisor in project: Variable Fractional Delay FIR Filters with low frequencies, session 2018-2019, B. Tech, Ankit Kumar, Uma, Yashanki, Bundelkhand Institute of Engineering and Technology, Jhansi (U.P.)
- ☛ Supervisor in project: Speed dialling extension for landline telephones using AVR microcontroller, session 2016-2017, Anshaj Shrivastava, Sajiv Philip, Saurabh Sharma, B. E., Chhattisgarh Swami Vivekananda Technical University, Bhilai,

Lecture/Speech/Seminar/Outreach/Invited Activities/Lectures/SeminarsDelivered

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1. Expert lecture on “Image processing using MATLAB”, SR group of institution, Jhansi on 2nd September, 2019.
 2. Expert lecture on “Optimization based Image Analysis”, in national workshop on programming, simulation and optimization using MATLAB, held at NIT Raipur during 22-26 August 2019.
 3. Expert lecture on “Multi objective optimization”, in Speaker in National workshop on Programming, Simulation and Optimization using MATLAB, held at NIT Raipur on 25 August 2019.
 4. NTA (NEET) Observer/ NTA representative, appointed by National Testing agency, Ministry of HRD, Government of India on 05/05/2019
 5. Expert Lecture on “Applications of evolutionary optimization in communication engineering” in faculty development program on “Recent advances in communication engineering” during December 26-30, 2018 organized by Bundelkhand Institute of Engineering and technology, Jhansi (U.P.)
 6. Industrial tour of 3rd year ECE students on 22 March 2018 at P. T. P. P. (UPRVNLM), Parichha (U. P.).
 7. Keynote speech on “Evolutionary optimization in signal processing” in “IEEE International Conference on Wireless Communications Signal Processing and Networking (WISPNET 2016)”, at SSN group of institutions Chennai, during March 23-25, 2016.
 8. Expert lecture on “MATLAB for INDUSTRIAL APPLICATIONS” in the special course on “Applications of power electronics and electrical drives in AC locomotives and LHB Coaches” organised by NIT Raipur, during June 29, 2016 to July 11, 2016.

9. Expert lecture in the workshop on “MATLAB and Simulink”, atSSIPMT Raipur, held during January 20-23, 2016.

Short Term Courses/Workshops/Faculty development programs attended/participated

1. Completed successfully AICTE approved online swayam ARPIT refresher course for career advancement scheme (CAS) promotion “pedagogical innovations and research methodology” with “A” grade in the proctored examination held on 16.02.2020.

Duration :	16 weeks
Start Date :	01 Oct 2019
End Date :	31 Jan 2020
Exam Date :	16 Feb 2020

2. Participated and successfully completed online one week faculty development program on “Digital Image Processing with Research Methodology” from 15 June 2020 to 19 June 2020 organized by Department of computer science KARPGM Institute of Engineering and engineering and computer society of India.
3. Participated and successfully completed one week faculty development program on “Latex” from 1st June 2020 to 7th June 2020 organized by spoken tutorial IIT Bombay and SVPKM institute of technology, Dhule.
4. Successfully completed one week faculty development program on “Application of Scilab in Engineering” from 25th May 2020 to 30th May 2020 conducted by spoken tutorial , IIT Bombay and A.D. Patel institute of Technology New VV Nagar , a constituent college of CVM University , Vallabh Vidyanagar, Gujarat.
5. Participated and successfully completed AICTE Training and Learning (ATAL) academy sponsored online FDP on “Artificial intelligence” from 24-04-2020 to 01-05-2020 conducted by Motilal National Institute of technology, Allahabad, Prayagraj , India
6. Participated in faculty development program on “current trends in electronics and communication engineering” organized by department of electronics and communication engineering, BIET Jhansi, during December 24-28, 2019 under the aegis of TEQIP-III(5 days). Certificate no: FDP/ECE/CTECE-2019/07.
7. Two day workshop on “Interactive learning Pedagogy in Electronics Engineering with modern simulation tool (Trang-19.0), organized by department of electronics and communication engineering, BIET Jhansi from 27th to 28th September 2019.

8. An one-week faculty development program on “Latex and MATLAB: Indispensable tools for research” during 08/07/19 to 13/07/19 organized by Bundelkhand Institute of Engineering and technology, Jhansi (U.P.)
9. One-week faculty development program on “Recent advances in communication engineering” during 26/12/18 to 30/12/18 organized by Bundelkhand Institute of Engineering and technology, Jhansi (U.P.)
10. One-week short term course on “Role of nano composites in science and engineering” during from 18/12/18 to 22/12/18 organized by Bundelkhand Institute of Engineering and technology, Jhansi (U.P.)
11. World bank TEQIP-III sponsored one week FDP on “Science, Technology, Engineering and Management”, organized by department of applied science, humanities and management, BIET Jhansi from 04/10/18 to 08/10/18 (5 days).
12. World bank TEQIP-III sponsored workshop on “Applied statistics for experimenters”, organized by Bundelkhand Institute of Engineering and Technology Jhansi from March 20, 2018 to March 21, 2018.
13. World bank TEQIP-III sponsored summer training program on active learning for senior faculty, organized by Indian Institute of technology Kanpur, from June 11, 2018 to June 15, 2018 (5 days)
14. World bank TEQIP-III sponsored one week FDP on Communication control and Networking, organized by department of electronics and communication, BIET Jhansi from February 19, 2018 to February 23, 2018 (5 days).
15. An AICTE approved faculty development program(FDP201x) on “Pedagogy for online and blended teaching learning process” conducted by Indian Institute of technology, Bombay from September 14, 2017 to October 12, 2017 under Pandit Madan Mohan Malviya National Mission for Teachers and Teaching, MHRD, Government of India
16. An AICTE approved faculty development program(FDP101x) on “Foundation Program in ICT for Education” conducted by Indian Institute of technology, Bombay from August 3, 2017 to September 17, 2017 (15days) under Pandit Madan Mohan Malviya National Mission for Teachers and Teaching, MHRD, Government of India
17. A course on “Introduction to data-structures and programming in C” jointly organized by E and ICT academies of IIT Guwahati, IIT Roorkee, IIITDM Jabalpur, NIT Patna, NIT Jaipur, NIT Warangal, from 01-07-2017 to 10-07-2017(10 days).
18. A Short term training program on “Medical Signal and Image Processing-II (MSIP 2017)” held under continuing education cell from 22-05-2017 to 27-05-2017(One week, 6 days) organized by National Institute of Technology, Raipur.
19. An ISTE one month Short term training program on “CMOS, Mixed Signal and Radio Frequency VLSI Design” online conducted by Indian Institute of Technology, Kharagpur from 26th December, 2016 to 4th February, 2017(35 days). (The workshop was help under the National Mission on education through ICT (MHRD)).
20. National Workshop on “Scientific Paper Writing” conducted by Bhilai Institute of Technology, Durg, India on 15th October 2016.

21. A Short term training program on “Soft computing and intelligent techniques in science and engineering (SCI-TSE)”, under TEQIP-II, organised by Department of computer science and engineering, National Institute of Technology, Raipur, during 30th September 2016 to 4th October 2016 (one week).
22. Short term course on, “Linear and non-linear signal processing and its application on bio-medical signals”, under TEQIP-II, organised by department of bio-medical engineering, National Institute of technology, Raipur, during 9th to 11th September, 2016
23. Online course on, “Writing your manuscript” by Springer author academy, during July 2016
24. An online MOOCCourse on “MATLAB programming for Numerical Computation”, organized by NPTEL in partnership with MATHWORKS and NASSCOM, during Jan to March 2016
25. A virtual workshop on “IEEE Early Career Faculty Development course” organized by IEEE, on December 23, 2015
26. A complementary seminar on “MATLAB and Simulink for Engineering education”, organized by Mathworks at the Gateway International, Raipur, during February 24, 2015
27. A Workshop on “Uses of E-resources”, organized by NIT Raipur, on September, 11, 2015.
28. A workshop on “Academic ethics and IPR” organized by NIT Raipur and CCOST Raipur, during April 04-05, 2014
29. A course on “Modelling and Simulation using MATLAB”, organized by iversity global community for higher education, during April 2014 to August 2014.
30. A short term course on ”Advances in VLSI Signal Processing” offered by Indian Institute of Technology Kharagpur under continuing education program from December 20, 2012 to December 24, 2012 (one week, 5 days).
31. A workshop on “Electronic System Design and Manufacturing” organized by Ministry of communication and Bhilai Institute of Technology, Durg on August 25, 2012.
32. A Tutorial on “Micro-Electro-Mechanical-Systems” organized by VNIT Nagpur and RKNEC Nagpur, during September, 29, 2010
33. A program “A Course in MATLAB: Programming and Modelling” conducted by Chhattisgarh training and Research organisation, Bhilai during April- May 2010.
34. A Workshop on “MATLAB Applications in Engineering”, organized by The institution of Engineers (I), Bhilai Chapter and SSCET Bhilai during January 19-20 2007.

References

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