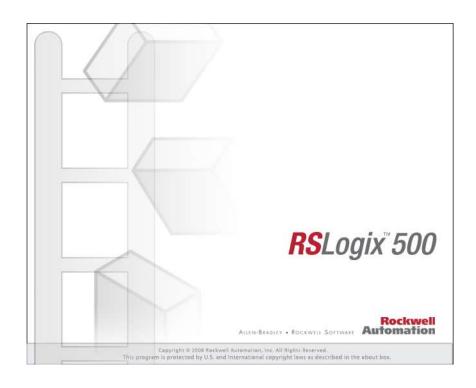
RSLogix Micro Project Report



Processor Information

Processor Type: Bul.1763 MicroLogix 1100 Series A

Processor Name: UNTITLED

Total Memory Used: 616 Instruction Words Used - 204 Data Table Words Used

Total Memory Left: 6040 Instruction Words Left

Program Files: 5

Data Files: 12

Program ID: 2474

I/O Configuration

)		
L		
2		
3		
1		

Bul.1763

MicroLogix 1100 Series A

Channel Configuration

```
CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Edit Resource/Owner Timeout: 60
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Passthru Link ID: 1
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Write Protected: No
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Comms Servicing Selection: Yes
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Message Servicing Selection: Yes
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex 1st AWA Append Character: \d
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex 2nd AWA Append Character: \a
  Source ID: 1 (decimal)
  Baud: 19200
  Parity: NONE
  Control Line: No Handshaking
  Error Detection: CRC
  Embedded Responses: Auto Detect
  Duplicate Packet Detect: Yes
  ACK Timeout(x20 ms): 50
  NAK Retries: 3
  ENO Retries: 3
CHANNEL 1 (SYSTEM) - Driver: Ethernet
  CHANNEL 1 (SYSTEM) - Driver: Ethernet Edit Resource/Owner Timeout: 60
  CHANNEL 1 (SYSTEM) - Driver: Ethernet Passthru Link ID: 1
  CHANNEL 1 (SYSTEM) - Driver: Ethernet Write Protected: No
  CHANNEL 1 (SYSTEM) - Driver: Ethernet Comms Servicing Selection: Yes
  CHANNEL 1 (SYSTEM) - Driver: Ethernet Message Servicing Selection: Yes
  Hardware Address: 00:0F:73:01:72:04
  IP Address: 192.168.1.112
  Subnet Mask: 255.255.255.0
  Gateway Address: 192.168.1.1
  Msg Connection Timeout (x 1mS): 15000
  Msg Reply Timeout (x mS): 3000
  Inactivity Timeout (x Min): 30
  Bootp Enable: No
  Dhcp Enable No
  SNMP Enable: No
  HTTP Enable: Yes
  Auto Negotiate Enable: Yes
  Port Speed Enable: 10/100 Mbps Full Duplex/Half Duplex
  Contact:
  Location:
```

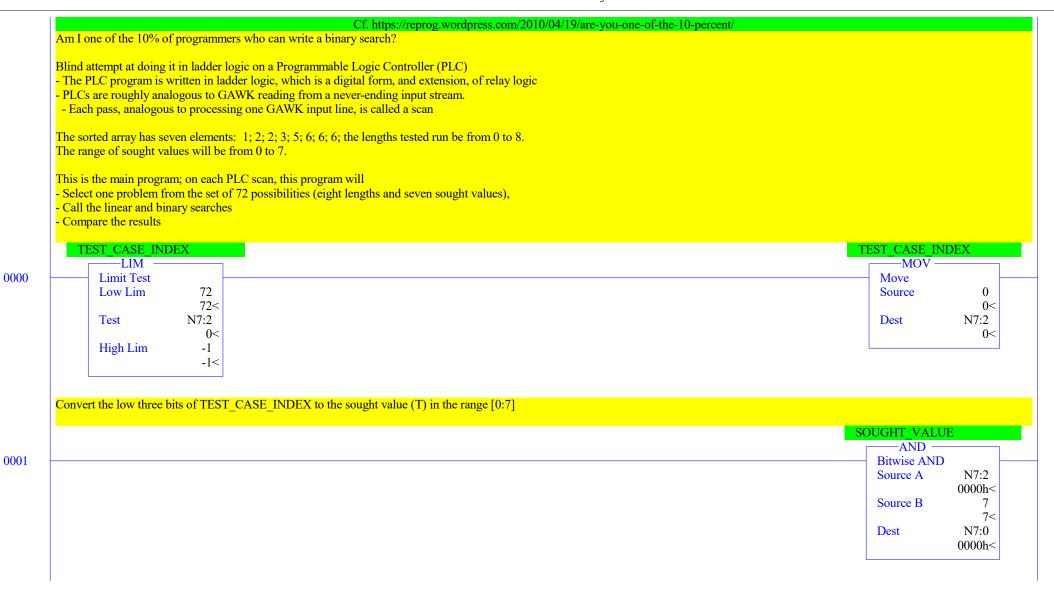
Program File List

Number	Type	Rungs	Debug	Bytes	
0	SYS	0	No	0	
1	SYS	0	No	0	
2	LADDER	7	No	150	
3	LADDER	9	No	362	
4	LADDER	9	No	602	
	Number 0 1 2 3 4	0 SYS 1 SYS 2 LADDER 3 LADDER	0 SYS 0 1 SYS 0 2 LADDER 7 3 LADDER 9	0 SYS 0 No 1 SYS 0 No 2 LADDER 7 No 3 LADDER 9 No	0 SYS 0 No 0 1 SYS 0 No 0 2 LADDER 7 No 150 3 LADDER 9 No 362

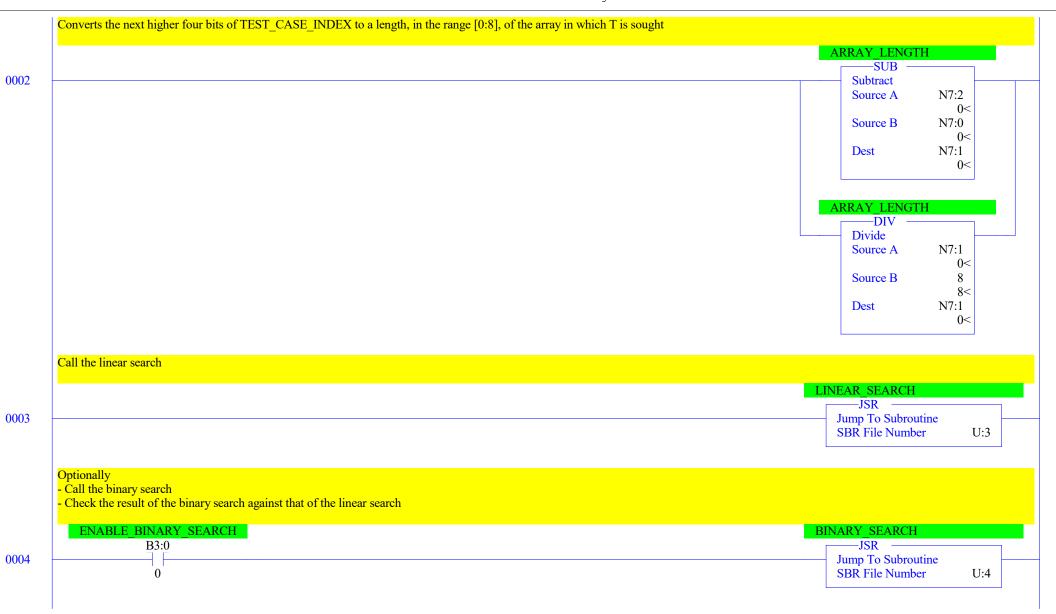
Data File List

Name	Number	Type	Scope	Debug	Words	Elements	Last
OUTPUT	0	0	Global	No	12	4	O:3
INPUT	1	I	Global	No	18	6	I:5
STATUS	2	S	Global	No	0	66	S:65
BINARY	3	В	Global	No	1	1	B3:0
TIMER	4	T	Global	No	3	1	T4:0
COUNTER	5	C	Global	No	6	2	C5:1
CONTROL	6	R	Global	No	3	1	R6:0
INTEGER	7	N	Global	No	7	7	N7:6
FLOAT	8	F	Global	No	2	1	F8:0
BNRYSEARCH	253	N	Global	No	72	72	N253:71
LINRSEARCH	254	N	Global	No	72	72	N254:71
SORTEDLIST	255	N	Global	No	8	8	N255:7

LAD 2 - CONTINUOUS --- Total Rungs in File = 7

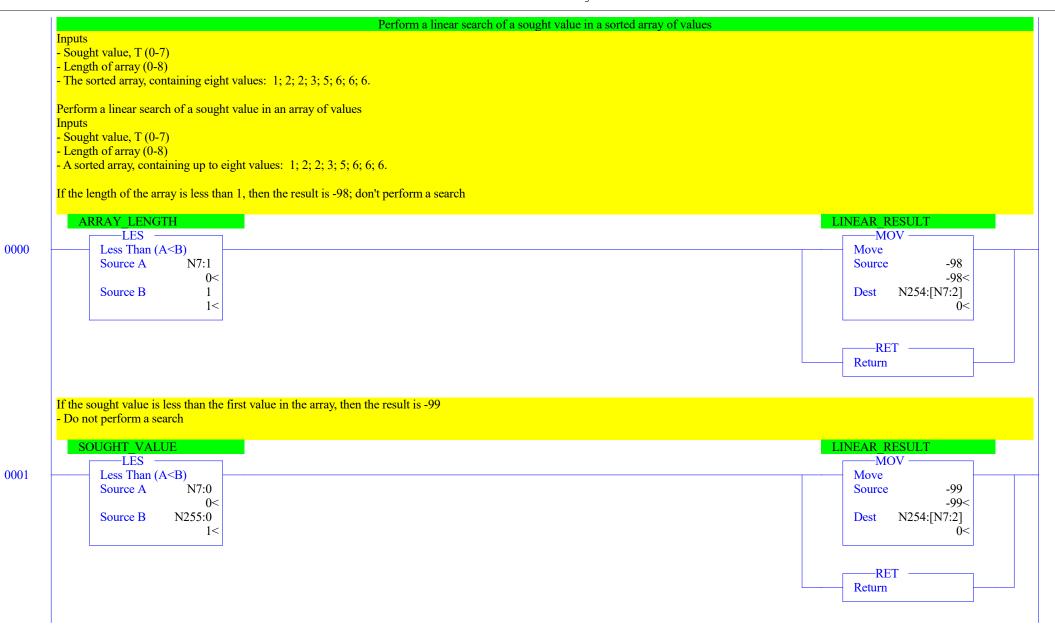


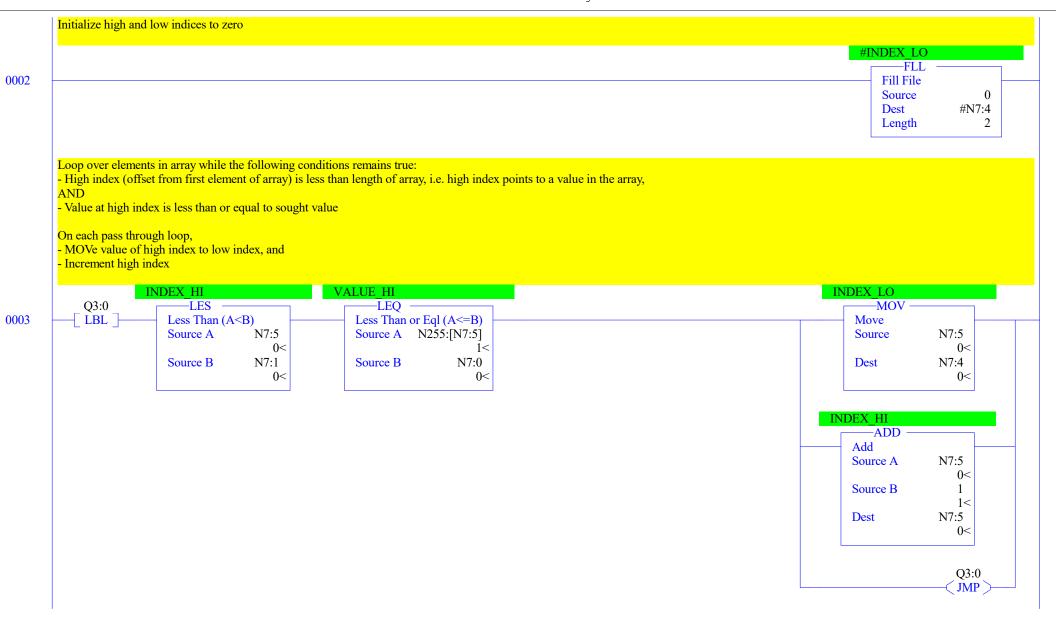
Page 1

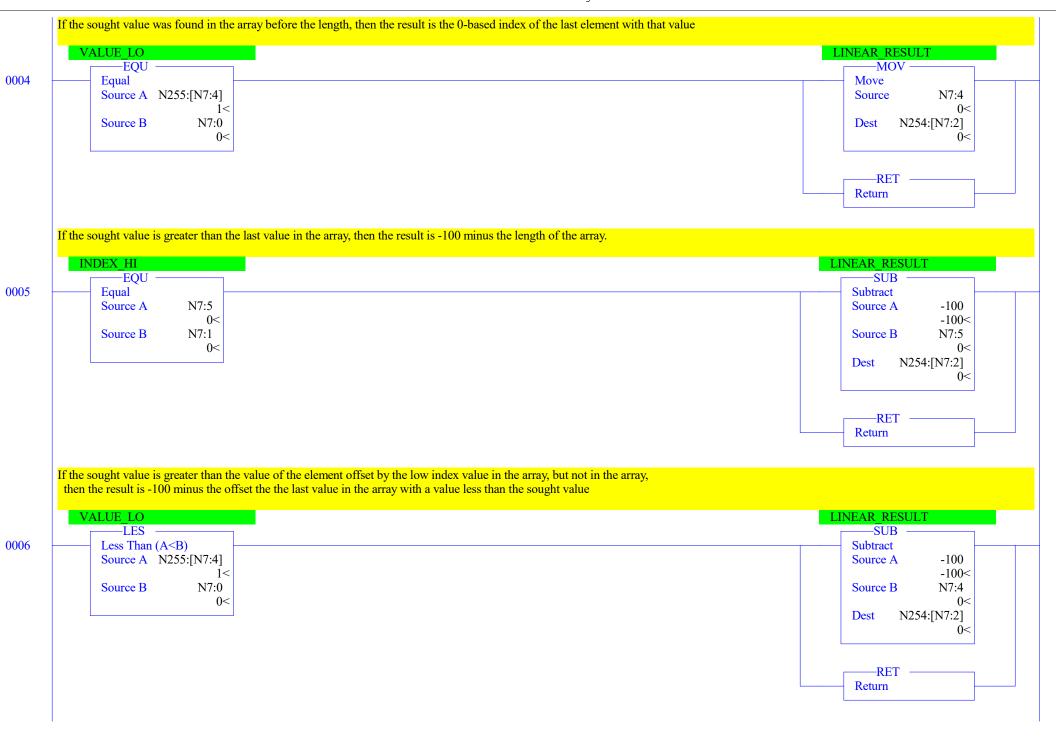


Increment the test case to prepare for the next scan	
	TEST_CASE_INDEX
	ADD ———————————————————————————————————
	Source A N7:2 0<
	Source B 1
	Dest N7:2
	(END)

LAD 3 - LINEAR --- Total Rungs in File = 9



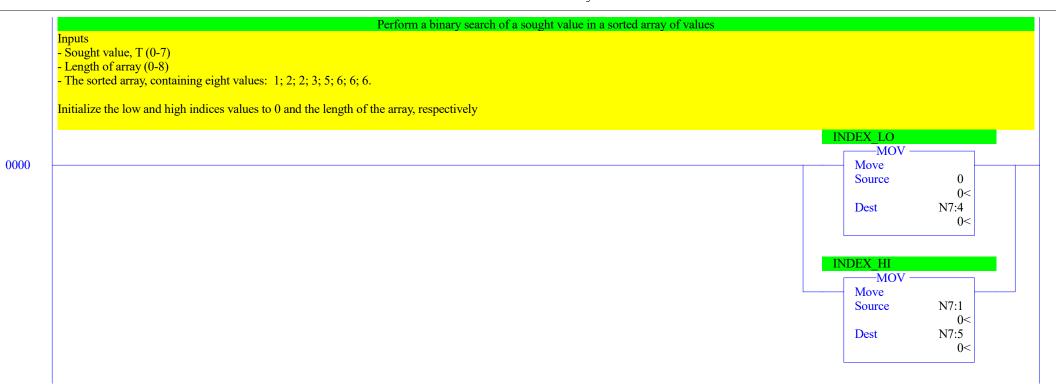




LAD 3 - LINEAR --- Total Rungs in File = 9



LAD 4 - BINARY --- Total Rungs in File = 9



Loop while the following condition remains true:

- High index value is at least two greater than low index value

Maintain the following invariants:

- Array value at low index is less than or equal to sought value

AND

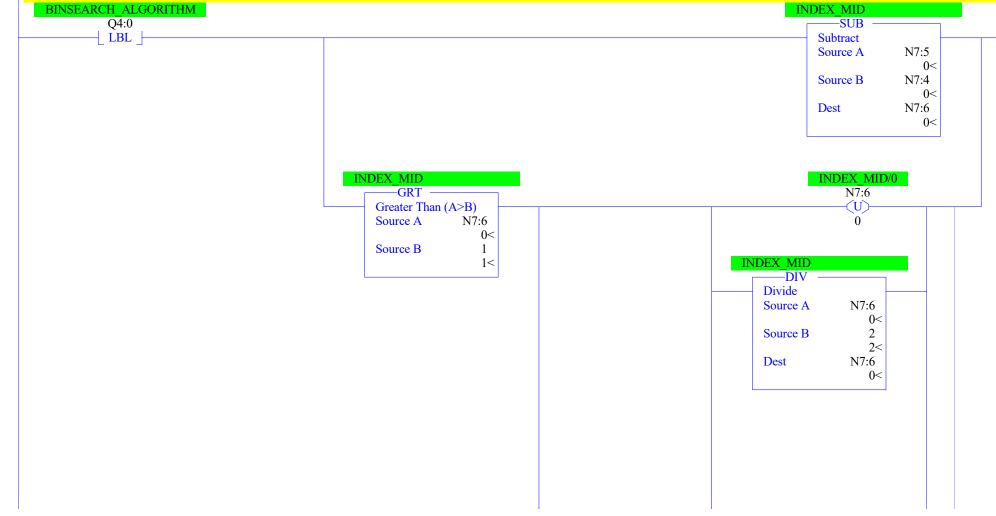
- EITHER high index is array length OR array value at high index is greater than or equal to sought value

N.B. Those invariants, if initially true, will eventually assign the low index to the offset of the last value in the array that is less than or equal to the sought value

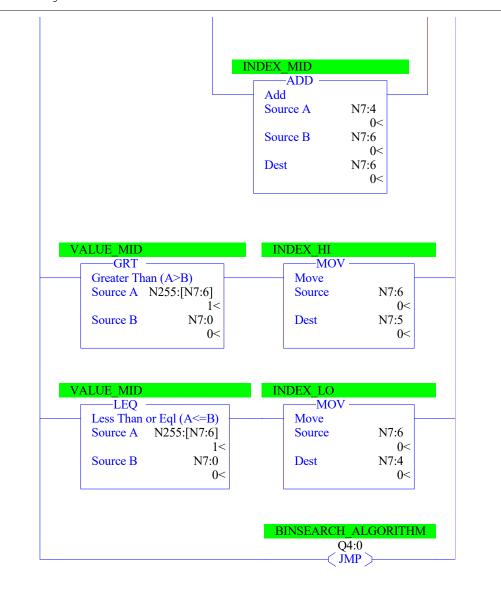
N.B. Those invariants will be violated when the sought value is less than value of the first element in the array, however in that case at the end the low and high index values will be 0 and 1, respectively.

On each pass through loop,

- Calculate mid index value
- Shift either low or high index to mid index value to maintain the invariants above

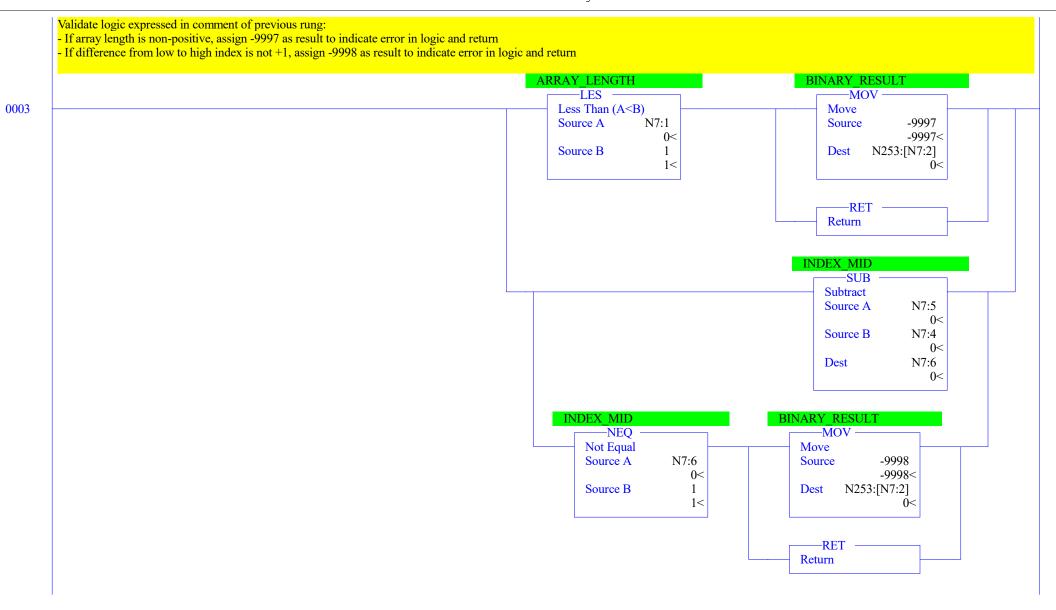


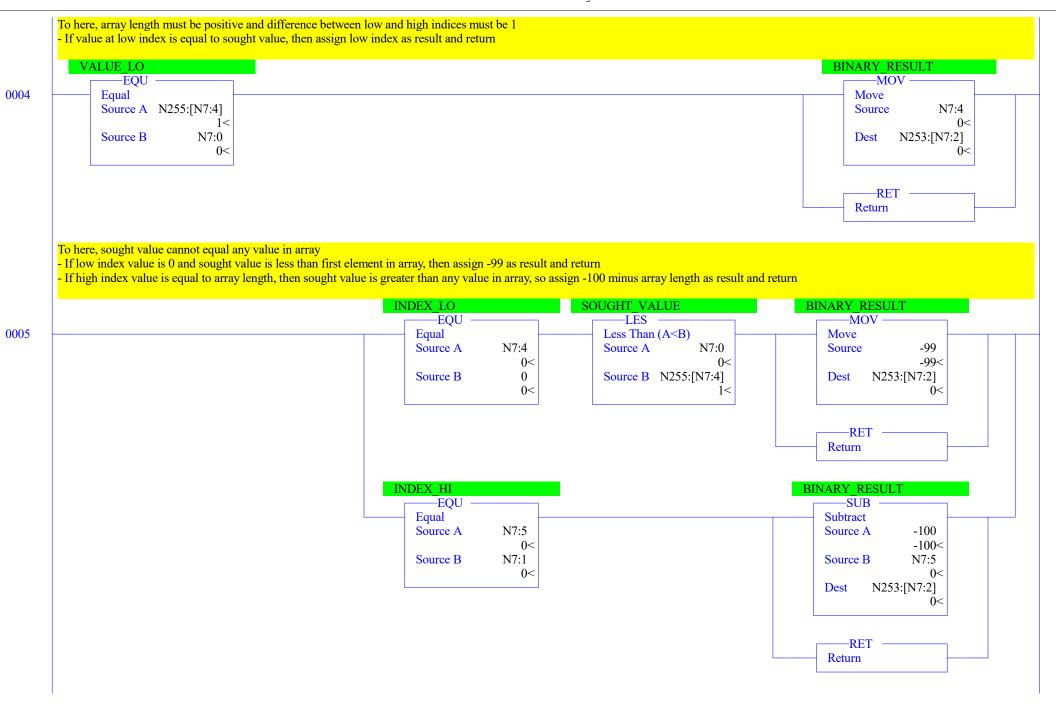
0001

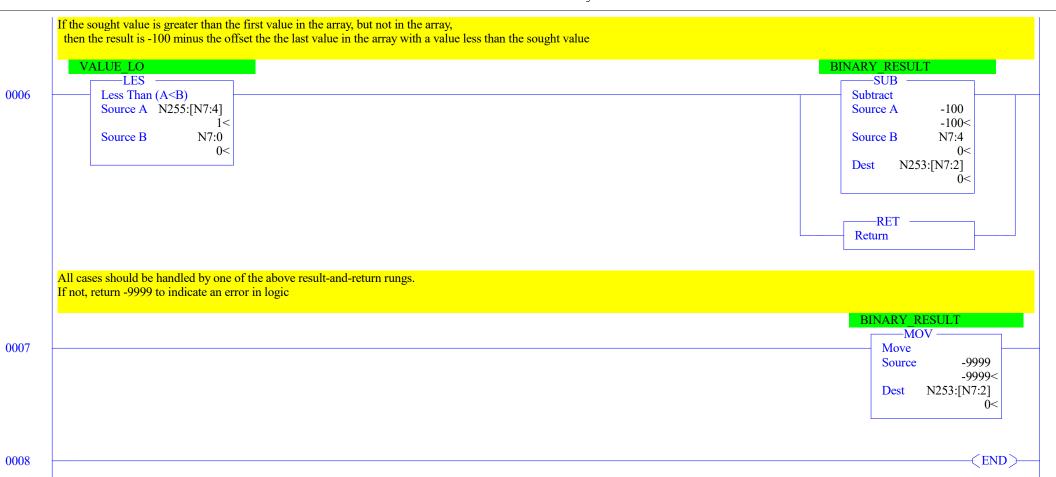


LAD 4 - BINARY --- Total Rungs in File = 9

If high index value is less than or equal to low index values are equal, then array length must be zero or less: assign -98 as result and return INDEX_HI BINARY_RESULT −MOV -—LEQ -0002 Less Than or Eql (A<=B) Move N7:5 Source A Source -98 0< -98< Source B N7:4 N253:[N7:2] Dest 0< 0< -RET Return







Data File OO (bin) -- OUTPUT

Offset	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0			
0:0.0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series	Α
0:0.1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series	Α
0:0.2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series	Α
0:0.3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series	Α

Data File I1 (bin) -- INPUT

Offset	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0		
I:0.0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series A
I:0.1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series A
I:0.2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series A
I:0.3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series A
I:0.4	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	Bul.1763	MicroLogix 1100 Series A-Analog Inp 0
I:0.5	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	Bul.1763	MicroLogix 1100 Series A-Analog Inp 1

Main

```
Processor Mode S:1/0 - S:1/4 = Remote Run
On Power up Go To Run (Mode Behavior) S:1/12 = 0
First Pass S:1/15 = No
Free Running Clock S:4 = 1101-0100-0011-0111

Proc

OS Catalog Number S:57 = 1100 User Pro
OS Series S:58 = B Compiler
OS FRS S:59 =
Processor Catalog Number S:60 =
Processor Series S:61 = A
Processor FRN S:62 =
```

User Program Type S:63 = 8001h Compiler Revision Number S:64 =

Scan Times

```
Maximum (x10 ms) S:22 = 25
Watchdog (x10 ms) S:3 (high byte) = 10
Last 100 uSec Scan Time S:35 = 6
Scan Toggle Bit S:33/9 = 0
```

Math

```
Math Overflow Selected S:2/14 = 1
Overflow Trap S:5/0 = 0
Carry S:0/0 = 0
Overflow S:0/1 = 0
Zero Bit S:0/2 = 0
Sign Bit S:0/3 = 1
```

Math Register (lo word) S:13=0Math Register (high word) S:14-S:13=0Math Register (32 Bit) S:14-S:13=0

Chan 0

Processor Mode S:1/0- S:1/4 = Remote Run Node Address S:15 (low byte) = 0
Baud Rate S:15 (high byte) = ?
Channel Mode S:33/3 = 0
Comms Active S:33/4 = 0
Incoming Cmd Pending S:33/0 = 0
Msg Reply Pending S:33/1 = 0

Outgoing Msg Cmd Pending S:33/2 = 0

Debug

Suspend Code S:7 = 0 Suspend File S:8 = 0

Data File S2 (hex) -- STATUS

Errors

```
Fault Override At Power Up S:1/8 = 0 Fault Routine S:29 = 0 Major Error S:6 = 0h Major Error Halt S:1/13 = 0 Error Description: Control Register Error S:5/2 = 0 Error Description: Major Error Executing User Fault Rtn. S:5/3 = 0 Battery Low S:5/11 = 0 Input Filter Selection Modified S:5/13 = 0 ASCII String Manipulation error S:5/15 = 0
```

Protection

Deny Future Access S:1/14 = No
Data File Overwrite Protection Lost S:36/10 = True

Mem Module

Memory Module Loaded On Boot S:5/8 = 0Password Mismatch S:5/9 = 0Load Memory Module On Memory Error S:1/10 = 0Load Memory Module Always S:1/11 = 0On Power up Go To Run (Mode Behavior) S:1/12 = 0Program Compare S:2/9 = 0Data File Overwrite Protection Lost S:36/10 = 1

Forces

Forces Enabled S:1/5 = Yes Forces Installed S:1/6 = No

Data File B3 (bin) -- BINARY

B3:0 0 0 0 0 0 0 0 0 0 0 0 1 1 0

Data File T4 -- TIMER

Offset EN TT DN BASE PRE ACC (Symbol) Description
T4:0 1 1 0 .001 sec 1000 777

Data File C5 -- COUNTER

Offset	CU	CD	DN	OV	UN	UA	PRE	ACC	(Symbol)	Description
C5:0 C5:1										

Data File R6 -- CONTROL

Offset EN EU DN EM ER UL IN FD LEN POS (Symbol) Description
R6:0 0 0 0 1 0 0 0 0 4 1

Data File N7 (dec) -- INTEGER

4 5 6 7 8 9

N7:0 0 0 0 0 0 0

3

1 2

Offset

Data File F8 -- FLOAT

Offset 0 1 2 3 4

F8:0

Data	File	N253	(dec)	 BNRYSEARCH

Offset	0	1	2	3	4	5	6	7	8	9
N253:0	0	0	0	0	0	0	0	0	0	0
N253:10	0	0	0	0	0	0	0	0	0	0
N253:20	0	0	0	0	0	0	0	0	0	0
N253:30	0	0	0	0	0	0	0	0	0	0
N253:40	0	0	0	0	0	0	0	0	0	0
N253:50	0	0	0	0	0	0	0	0	0	0
N253:60	0	0	0	0	0	0	0	0	0	0
N253:70	0	0								

Data	Fila	N 2 5 4	(dec)	 TITNESEARCH

Offset	0	1	2	3	4	5	6	7	8	9
N254:0	0	0	0	0	0	0	0	0	0	0
N254:10	0	0	0	0	0	0	0	0	0	0
N254:20	0	0	0	0	0	0	0	0	0	0
N254:30	0	0	0	0	0	0	0	0	0	0
N254:40	0	0	0	0	0	0	0	0	0	0
N254:50	0	0	0	0	0	0	0	0	0	0
N254:60	0	0	0	0	0	0	0	0	0	0
N254:70	0	0								

$D = \pm =$	Filo	M255	(dec)	 SORTEDITST

4 5 6 7 8 9

N255:0 1 2 2 3 5 6 6 6

3

2

Page 1 (Radix Decimal)

Offset

0

1

10PERCENT.RSS

Address/Symbol Database

Address	Symbol	Scope	Description	Sym Group	Dev. Code	ABV	BLW
B3:0/0 N7:0 N7:1 N7:2 N7:3 N7:4 N7:5	ENABLE_BINARY_SEARCH SOUGHT_VALUE ARRAY_LENGTH TEST_CASE_INDEX ERROR_COUNT INDEX_LO INDEX_HI	Global Global Global Global Global Global Global					
N7:6 N253:[N7:2] N254:[N7:2] N255:[N7:4] N255:[N7:5] N255:[N7:6] Q4:0	INDEX_MID BINARY_RESULT LINEAR_RESULT VALUE_LO VALUE_HI VALUE_MID BINSEARCH_ALGORITHM	Global Global Global Global Global Global Global					
S:0 S:0/0 S:0/1 S:0/2 S:0/3 S:1 S:1/0	51.65.1.01.200.1.1.1.1	010201	Arithmetic Flags Processor Arithmetic Carry Flag Processor Arithmetic Underflow/ Overflow Processor Arithmetic Zero Flag Processor Arithmetic Sign Flag Processor Mode Status/ Control Processor Mode Bit 0	Flag			
S:1/1 S:1/2 S:1/3 S:1/4 S:1/5 S:1/6 S:1/7			Processor Mode Bit 1 Processor Mode Bit 2 Processor Mode Bit 3 Processor Mode Bit 4 Forces Enabled Forces Present Comms Active				
S:1/8 S:1/9 S:1/10 S:1/11 S:1/12 S:1/13 S:1/14			Fault Override at Powerup Startup Protection Fault Load Memory Module on Memory Error Load Memory Module Always Load Memory Module and RUN Major Error Halted Access Denied				
S:1/15 S:2/0 S:2/1 S:2/2 S:2/3 S:2/4			First Pass STI Pending STI Enabled STI Executing Index Addressing File Range Saved with Debug Single Step				
S:2/5 S:2/6 S:2/7 S:2/15 S:3 S:4 S:5/0			DH-485 Incoming Command Pending DH-485 Message Reply Pending DH-485 Outgoing Message Command Pending Comms Servicing Selection Current Scan Time/ Watchdog Scan Time Time Base Overflow Trap				
S:5/0 S:5/2 S:5/3 S:5/4 S:5/8 S:5/9 S:5/10			Control Register Error Major Err Detected Executing UserFault Ro M0-M1 Referenced on Disabled Slot Memory Module Boot Memory Module Password Mismatch STI Overflow	putine			
s:5/11 s:6 s:7 s:8 s:9			Battery Low Major Error Fault Code Suspend Code Suspend File Active Nodes				
S:10 S:11 S:12 S:13			Active Nodes I/O Slot Enables I/O Slot Enables Math Register				

10PERCENT.RSS

Address/Symbol Database

Moth Mothers Boud Rate	Address	Symbol	Scope	Description	Sym Group	Dev. Code	ABV	BLW
Debug Single Step Flar	S:14			Math Register				
being Single Step File Debug Single Step Fareshpoint Rung Debug Faile Step Fareshpoint Rung Debug Faile Step File Debug Faile Forestpoint Rung Debug Faile Forestpoint File Size Maximum Observed Scan Time Maximum Observe	S:15			Node Address/ Baud Rate				
Debug Single Step Dreakpoint Rung Debug Single Single Dreakpoint Rung Debug Single Single Dreakpoint Rung Debug Single Singl	S:16			Debug Single Step Rung				
Debug Fingle Diebug Family Powercome Wong Debug Family Powercome Wong Debug Family Powercome Wong Debug Family Powercome Wong Debug Family Powercome Tile Debug Family Tile Debug Family Tile Debug Family Tile Debug Family Tile	S:17			Debug Single Step File				
Debug Familt, Powerdown Education Debug Familt Powerdown Education Debug Familton Debug Fam	S:18			Debug Single Step Breakpoint Rung				
Debug Foult/ Fowerdown File								
Debug Foult/ Fowerdown File	S:20			Debug Fault/ Powerdown Rung				
Maximum Observed Scan Time New York Ne								
Index Register								
1/0 Interrupt Emediang 1/0 Interrupt 1/0 Inte								
1/0 Interrupt Emailed 1/0 Interrupt								
1/0 Interrupt Enabled 1/2 1/								
1.0 1.0								
### Sepoint Surface Su								
STI Setpoint STI File Number STI Setpoint STI File Number STI File Number STI Setpoint STI File Number STI Setpoint STI SET								
S.31								
1/0 Interrupt Executing Status Control Word								
Extended Proc Status Control Word 3:33/1								
Signorman Sending Command Pending								
Signar								
Signaria Selection Status User/PFT								
Salay Selection Status User/DFT								
Signature Sign								
Signorman Signorman Servicing Selection								
Signature Sign								
### ### ### ### ### ### ### ### ### ##								
Signar S								
Scan Toggle Flag								
Discrete Input Interrupt Reconfigur Flag								
Signature Sign								
Signature Sign								
Scan Time Timebase Selection								
Simple								
S:33/15 DTR Force Bit								
S:34								
Simple S								
S:34/2 Floating Point Math Flag Disable,Fl S:34/2 Floating Point Math Flag Disable,Fl S:35 Last 1 ms Scan Time S:36 Extended Minor Error Bits S:36/8 DII Lost S:36/9 STI Lost S:36/10 Memory Module Data File Overwrite Protection S:37 Clock Calendar Year S:38 Clock Calendar Month S:39 Clock Calendar Day S:40 Clock Calendar Hours S:41 Clock Calendar Hours S:42 Clock Calendar Seconds S:43 STI Interrupt Time S:44 I/O Event Interrupt Time S:45 DII Interrupt Time S:46 DII Interrupt Time S:47 Discrete Input Interrupt—Slot Number S:48 Discrete Input Interrupt—Slot Number S:49 Discrete Input Interrupt—Compare Value S:50 Processor Catalog Number S:51 Discrete Input Interrupt—Accumulat								
### State								
Last 1 ms Scan Time								
Extended Minor Error Bits S:36/9 STI Lost S:36/10 Memory Module Data File Overwrite Protection S:37 Clock Calendar Year Clock Calendar Month S:39 Clock Calendar Boy S:40 Clock Calendar Hours Clock Calendar Minutes Clock Calendar								
S:36/8 S:36/9 STI Lost S:36/10 Memory Module Data File Overwrite Protection S:37 Clock Calendar Year S:38 Clock Calendar Month S:39 Clock Calendar Day S:40 Clock Calendar Hours S:41 Clock Calendar Hours S:42 Clock Calendar Seconds S:43 STI Interrupt Time S:44 I/O Event Interrupt Time S:45 DIII Interrupt Time S:46 Discrete Input Interrupt - File Number S:47 Discrete Input Interrupt - Slot Number S:48 S:49 Discrete Input Interrupt - Compare Value S:50 Processor Catalog Number S:51 Discrete Input Interrupt - Return Number S:52 Discrete Input Interrupt - Return Number								
S:36/9 S:36/10 Memory Module Data File Overwrite Protection S:37 Clock Calendar Year Clock Calendar Month S:39 Clock Calendar Day S:40 Clock Calendar Hours Clock Calendar Hours S:41 Clock Calendar Seconds S:42 Clock Calendar Seconds S:43 STI Interrupt Time S:44 I/O Event Interrupt Time S:45 DII Interrupt Time S:46 Discrete Input Interrupt File Number S:47 Discrete Input Interrupt Bit Mask S:49 Discrete Input Interrupt - Compare Value S:50 Processor Catalog Number S:51 Discrete Input Interrupt - Return Number S:52 Discrete Input Interrupt - Return Number Discrete Input Interrupt - Return Number								
S:36/10 S:37 Clock Calendar Year Clock Calendar Month S:39 Clock Calendar Day Clock Calendar Day Clock Calendar Hours S:40 Clock Calendar Hours Clock Calendar Hours S:42 Clock Calendar Minutes Clock Calendar Seconds S:43 STI Interrupt Time S:44 I/O Event Interrupt Time Discrete Input Interrupt File Number S:45 S:47 Discrete Input Interrupt Bit Mask S:49 Discrete Input Interrupt Slot Number S:50 Processor Catalog Number S:51 Discrete Input Interrupt Return Number S:52 Discrete Input Interrupt Return Number Siscrete Input Interrupt Return Number Discrete Input Interrupt Return Number								
S:37 S:38 Clock Calendar Year Clock Calendar Month S:39 Clock Calendar Day Clock Calendar Hours S:40 Clock Calendar Hours S:41 Clock Calendar Minutes S:42 Clock Calendar Seconds S:43 STI Interrupt Time I/O Event Interrupt Time S:44 I/O Event Interrupt File Number S:45 DII Interrupt Time S:46 Discrete Input Interrupt- Slot Number S:47 Discrete Input Interrupt- Bit Mask S:49 Discrete Input Interrupt- Compare Value S:50 Processor Catalog Number S:51 Discrete Input Interrupt- Return Number Discrete Input Interrupt- Return Number Discrete Input Interrupt- Accumulat								
S:38 Clock Calendar Month S:39 Clock Calendar Day S:40 Clock Calendar Hours S:41 Clock Calendar Minutes C:42 Clock Calendar Seconds S:43 STI Interrupt Time S:44 I/O Event Interrupt Time S:45 DII Interrupt Time Discrete Input Interrupt- File Number S:46 Discrete Input Interrupt- Slot Number S:48 S:49 Discrete Input Interrupt- Bit Mask S:49 Discrete Input Interrupt- Compare Value S:50 Processor Catalog Number S:51 Discrete Input Interrupt- Return Number Discrete Input Interrupt- Return Number Discrete Input Interrupt- Return Number								
S:39 Clock Calendar Day Clock Calendar Hours S:41 Clock Calendar Minutes C:42 Clock Calendar Seconds S:43 STI Interrupt Time S:44 I/O Event Interrupt Time S:45 DII Interrupt Time Discrete Input Interrupt- File Number S:47 Discrete Input Interrupt- Bit Mask S:48 Discrete Input Interrupt- Bit Mask S:49 Discrete Input Interrupt- Compare Value S:50 Processor Catalog Number S:51 Discrete Input Interrupt- Return Number Discrete Input Interrupt- Return Number Discrete Input Interrupt- Return Number								
Clock Calendar Hours S:41 Clock Calendar Minutes C:42 Clock Calendar Seconds S:43 STI Interrupt Time S:44 I/O Event Interrupt Time DII Interrupt Time S:45 DII Interrupt Time S:46 Discrete Input Interrupt- File Number S:47 Discrete Input Interrupt- Slot Number S:48 Discrete Input Interrupt- Bit Mask S:49 Discrete Input Interrupt- Compare Value S:50 Processor Catalog Number S:51 Discrete Input Interrupt- Return Number S:52 Discrete Input Interrupt- Accumulat								
Clock Calendar Minutes Clock Calendar Seconds Clock Calendar Minutes								
S:42 Clock Calendar Seconds S:43 STI Interrupt Time S:44 I/O Event Interrupt Time DII Interrupt Time S:45 Discrete Input Interrupt- File Number S:47 Discrete Input Interrupt- Slot Number S:48 Discrete Input Interrupt- Bit Mask S:49 Discrete Input Interrupt- Compare Value S:50 Processor Catalog Number S:51 Discrete Input Interrupt- Return Number S:52 Discrete Input Interrupt- Accumulat								
S:43 STI Interrupt Time S:44 I/O Event Interrupt Time S:45 DII Interrupt Time S:46 Discrete Input Interrupt- File Number S:47 Discrete Input Interrupt- Bit Number S:48 Discrete Input Interrupt- Bit Mask S:49 Discrete Input Interrupt- Compare Value S:50 Processor Catalog Number S:51 Discrete Input Interrupt- Return Number S:52 Discrete Input Interrupt- Accumulat								
S:44 I/O Event Interrupt Time								
S:45 DII Interrupt Time S:46 Discrete Input Interrupt- File Number S:47 Discrete Input Interrupt- Slot Number S:48 Discrete Input Interrupt- Bit Mask S:49 Discrete Input Interrupt- Compare Value Processor Catalog Number S:51 Discrete Input Interrupt- Return Number S:52 Discrete Input Interrupt- Accumulat								
S:46 Discrete Input Interrupt- File Number S:47 Discrete Input Interrupt- Slot Number S:48 Discrete Input Interrupt- Bit Mask S:49 Discrete Input Interrupt- Compare Value Processor Catalog Number S:50 Discrete Input Interrupt- Return Number S:51 Discrete Input Interrupt- Accumulat								
S:47 Discrete Input Interrupt- Slot Number S:48 Discrete Input Interrupt- Bit Mask S:49 Discrete Input Interrupt- Compare Value Processor Catalog Number S:50 Discrete Input Interrupt- Return Number S:51 Discrete Input Interrupt- Accumulat								
S:48 S:49 Discrete Input Interrupt- Bit Mask Discrete Input Interrupt- Compare Value Processor Catalog Number S:51 Discrete Input Interrupt- Return Number Discrete Input Interrupt- Accumulat								
S:49 S:50 Processor Catalog Number S:51 Discrete Input Interrupt- Compare Value Discrete Input Interrupt- Return Number Discrete Input Interrupt- Accumulat								
S:50 Processor Catalog Number S:51 Discrete Input Interrupt- Return Number S:52 Discrete Input Interrupt- Accumulat								
S:51 Discrete Input Interrupt- Return Number S:52 Discrete Input Interrupt- Accumulat								
S:52 Discrete Input Interrupt- Accumulat								
S.53 Reserved/ Clock Calendar Day of the Mook								
	S:53			Reserved/ Clock Calendar Day of the Week				
S:55 Last DII Scan Time	S:55			Last DII Scan Time				

10PERCENT.RSS

Address/Symbol Database

Address	Symbol	Scope	Description	Sym Group	Dev. Code	ABV	BLW
S:56			Maximum Observed DII Scan Time				
S:57			Operating System Catalog Number				
S:58			Operating System Series				
S:59			Operating System FRN				
S:61			Processor Series				
S:62			Processor Revision				
S:63			User Program Type				
S:64			User Program Functional Index				
S:65			User RAM Size				
S:66			Flash EEPROM Size				
S:67			Channel O Active Nodes				
S:68			Channel O Active Nodes				
S:69			Channel O Active Nodes				
S:70			Channel O Active Nodes				
S:71			Channel O Active Nodes				
S:72			Channel O Active Nodes				
S:73			Channel O Active Nodes				
S:74			Channel O Active Nodes				
S:75			Channel O Active Nodes				
S:76			Channel O Active Nodes				
S:77			Channel O Active Nodes				
S:78			Channel O Active Nodes				
S:79			Channel O Active Nodes				
S:80			Channel O Active Nodes				
S:81			Channel O Active Nodes				
S:82			Channel O Active Nodes				
S:83			DH+ Active Nodes				
S:84			DH+ Active Nodes				
S:85			DH+ Active Nodes				
S:86			DH+ Active Nodes				
U:3	LINEAR SEARCH	Global					
U:4	BINARY_SEARCH	Global					

Instruction Comment Database

Address Instruction Description

Group_Name Description