

RSLogix Micro Project Report



Processor Information

Processor Type: Bul.1763 MicroLogix 1100 Series B

Processor Name: UNTITLED

Total Memory Used: 228 Instruction Words Used - 313 Data Table Words Used

Total Memory Left: 6428 Instruction Words Left

Program Files: 3

Data Files: 12

Program ID: 562

I/O Configuration

0	Bul.1763	MicroLogix 1100 Series B
1		
2		
3		
4		

Channel Configuration

CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex

CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Edit Resource/Owner Timeout: 60
CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Passthru Link ID: 1
CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Write Protected: No
CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Comms Servicing Selection: Yes
CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Message Servicing Selection: Yes
CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex 1st AWA Append Character: \d
CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex 2nd AWA Append Character: \a

Source ID: 1 (decimal)
Baud: 19200
Parity: NONE
Control Line : No Handshaking
Error Detection: CRC
Embedded Responses: Auto Detect
Duplicate Packet Detect: Yes
ACK Timeout(x20 ms): 50
NAK Retries: 3
ENQ Retries: 3

CHANNEL 1 (SYSTEM) - Driver: Ethernet

CHANNEL 1 (SYSTEM) - Driver: Ethernet Edit Resource/Owner Timeout: 60
CHANNEL 1 (SYSTEM) - Driver: Ethernet Passthru Link ID: 1
CHANNEL 1 (SYSTEM) - Driver: Ethernet Write Protected: No
CHANNEL 1 (SYSTEM) - Driver: Ethernet Comms Servicing Selection: Yes
CHANNEL 1 (SYSTEM) - Driver: Ethernet Message Servicing Selection: Yes

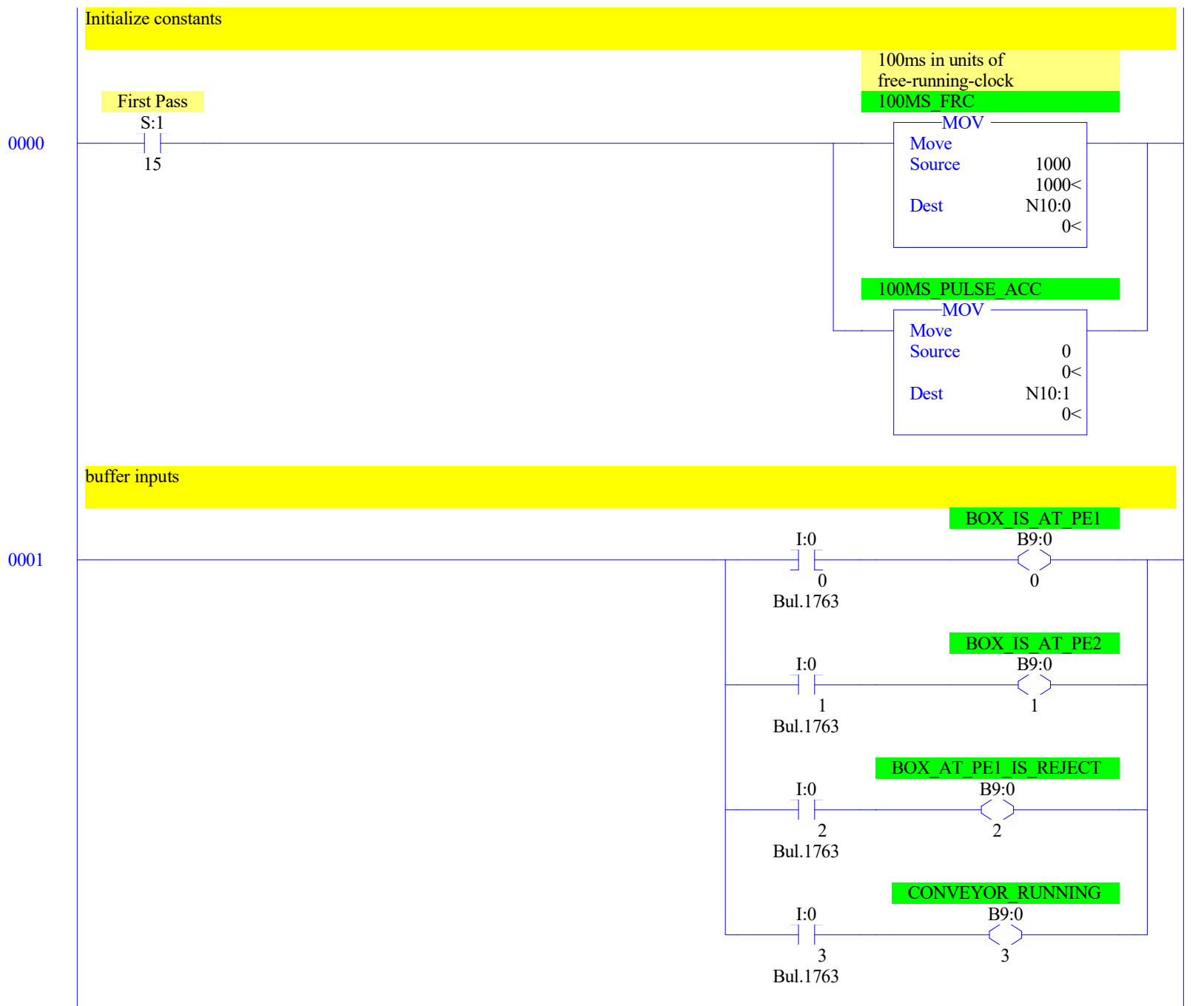
Hardware Address: 00:00:00:00:00:00
IP Address: 0.0.0.0
Subnet Mask: 0.0.0.0
Gateway Address: 0.0.0.0
Msg Connection Timeout (x 1mS): 15000
Msg Reply Timeout (x mS): 3000
Inactivity Timeout (x Min): 30
Bootp Enable: No
Dhcp Enable: Yes
SNMP Enable: No
HTTP Enable: Yes
Auto Negotiate Enable: Yes
Port Speed Enable: 10/100 Mbps Full Duplex/Half Duplex
Contact:
Location:

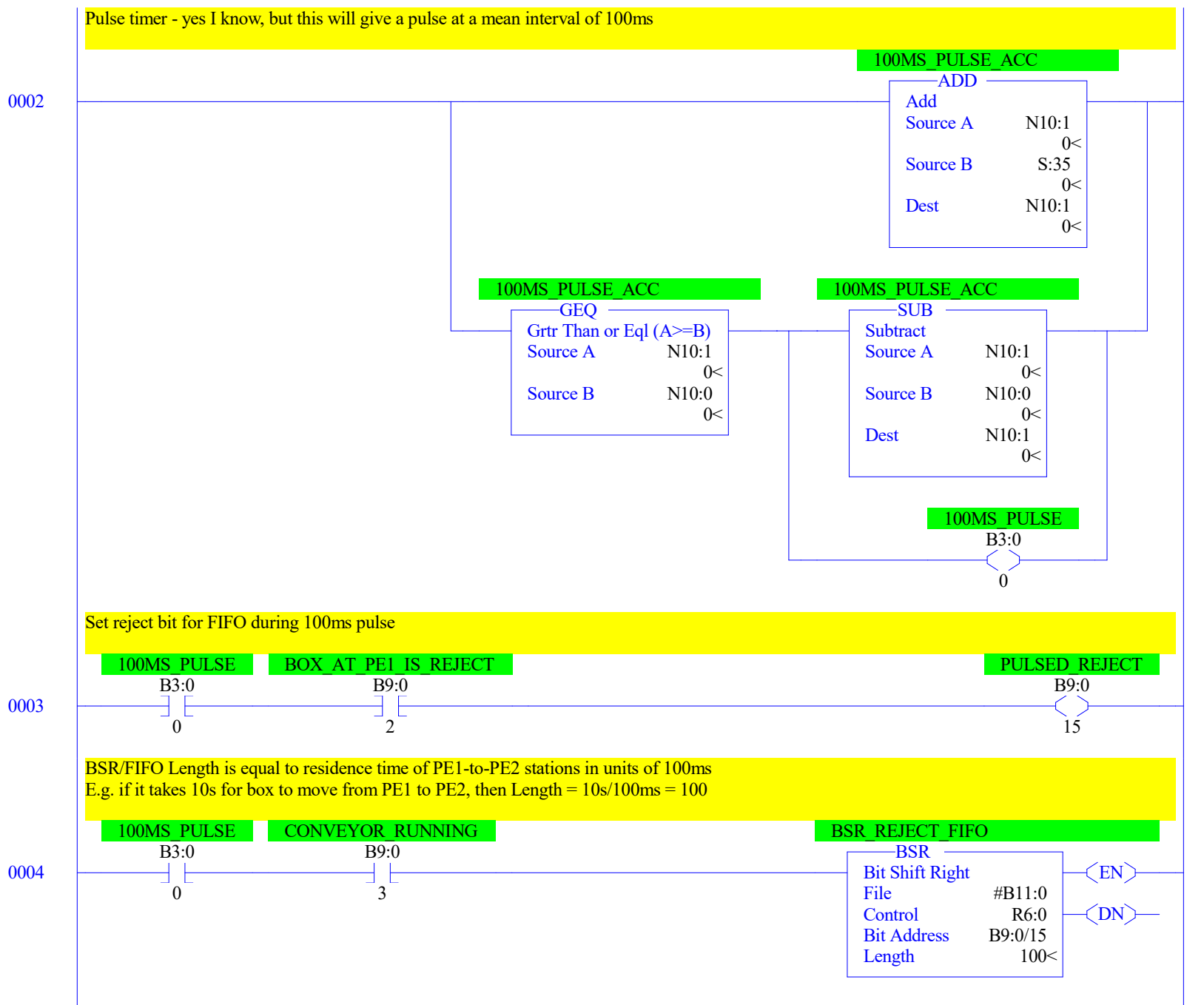
Program File List

Name	Number	Type	Rungs	Debug	Bytes
[SYSTEM]	0	SYS	0	No	0
	1	SYS	0	No	0
	2	LADDER	10	No	382

Data File List

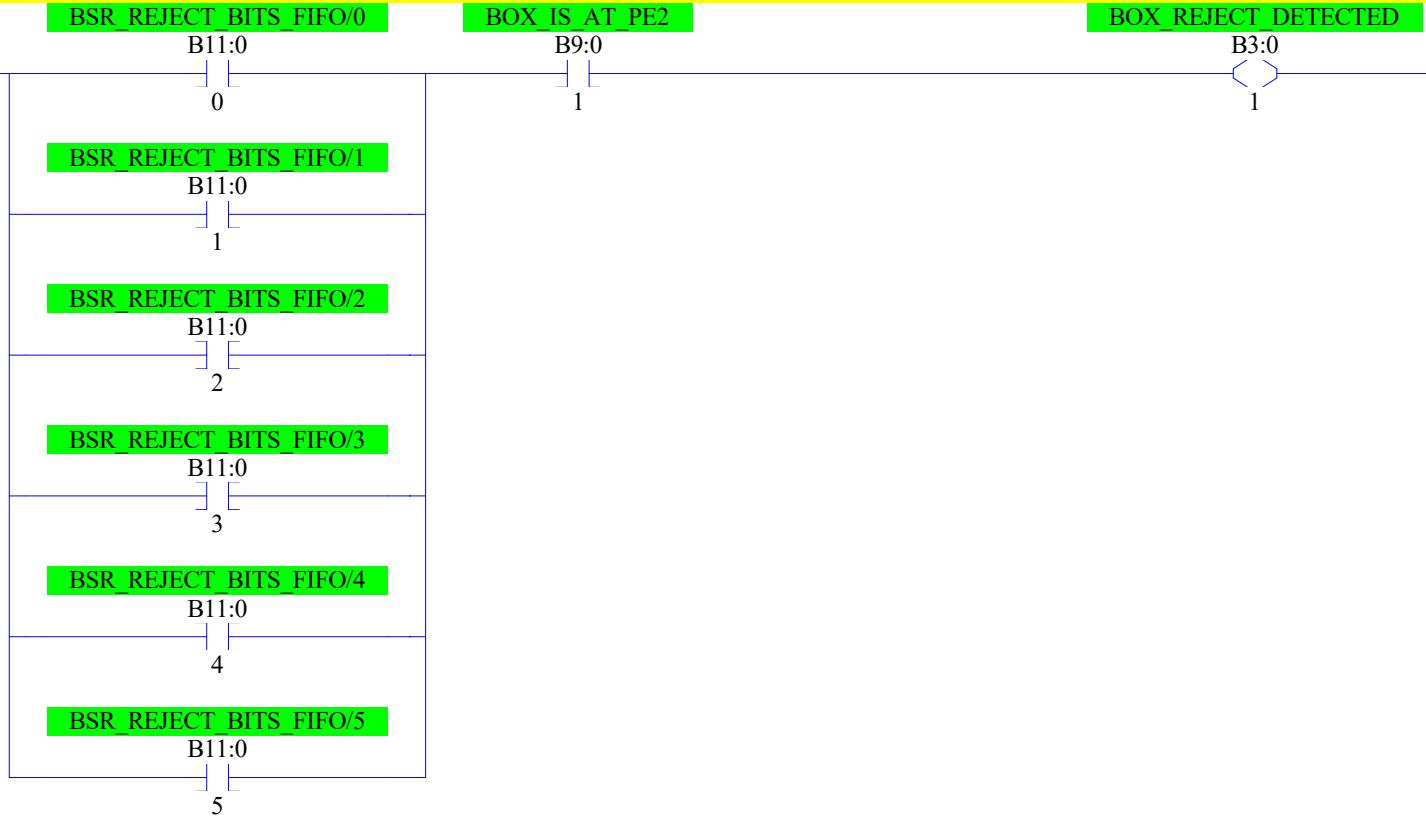
Name	Number	Type	Scope	Debug	Words	Elements	Last
OUTPUT	0	O	Global	No	12	4	O:3
INPUT	1	I	Global	No	18	6	I:5
STATUS	2	S	Global	No	0	66	S:65
BINARY	3	B	Global	No	1	1	B3:0
TIMER	4	T	Global	No	6	2	T4:1
COUNTER	5	C	Global	No	3	1	C5:0
CONTROL	6	R	Global	No	3	1	R6:0
INTEGER	7	N	Global	No	1	1	N7:0
FLOAT	8	F	Global	No	2	1	F8:0
INPUT_BFFR	9	B	Global	No	1	1	B9:0
CONSTANCE	10	N	Global	No	10	10	N10:9
RJECT_FIFO	11	B	Global	No	256	256	B11:255





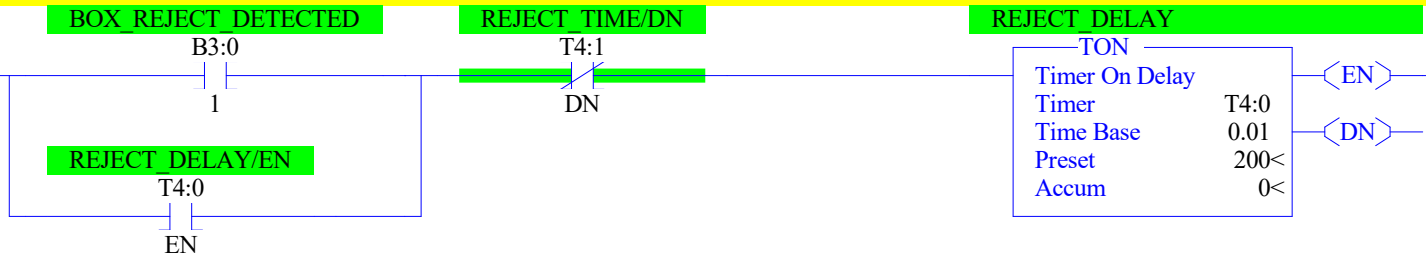
Implement reject window logic
- Is BOX_IS_AT_PE2 necessary here?

0005



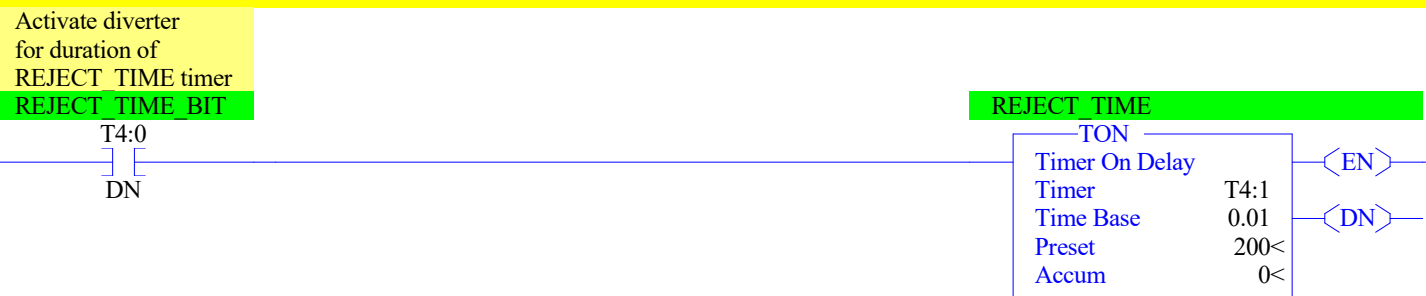
Delay before the diverter is activated (which executes a rejection)
- Done bit of REJECT_DELAY is REJECT_TIME_BIT i.e. activates diverter
- Seal-in REJECT_DELAY timer until REJECT_TIME timer is done
- Notes to parky
- This 2s delay seems long, requiring a 2m box at 1m/s to complete the delay
- Which is why I put in the seal-in
- Maybe this was a debounce, or phot-eye noise filter, and the Preset should be 20 (200ms)?

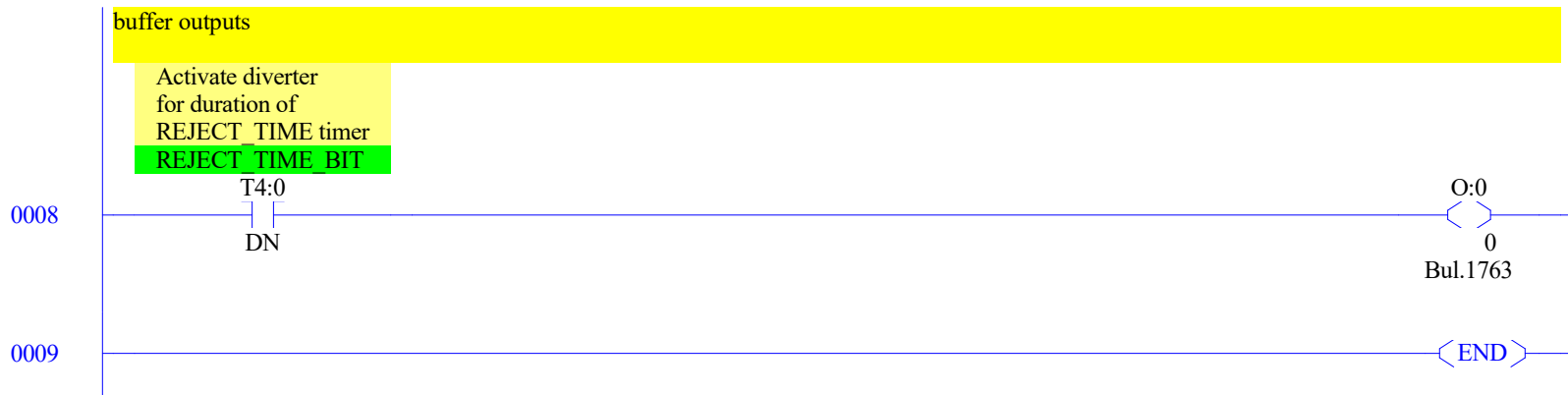
0006



Reject time for the diverter
- Rising edge of T4:1/DN will reset all bits

0007





Offset	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0				
O:0.0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix	1100	Series B
O:0.1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix	1100	Series B
O:0.2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix	1100	Series B
O:0.3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix	1100	Series B

Offset	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0				
I:0.0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix	1100	Series B
I:0.1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix	1100	Series B
I:0.2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix	1100	Series B
I:0.3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix	1100	Series B
I:0.4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix	1100	Series B-Analog
I:0.5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix	1100	Series B-Analog

Main

Processor Mode S:1/0 - S:1/4 = Remote Program Mode
On Power up Go To Run (Mode Behavior) S:1/12 = 0
First Pass S:1/15 = No
Free Running Clock S:4 = 0000-0000-0000-0000

Proc

OS Catalog Number S:57 = 1100 User Program Type S:63 = 8001h
OS Series S:58 = A Compiler Revision Number S:64 =
OS FRS S:59 =
Processor Catalog Number S:60 =
Processor Series S:61 = A
Processor FRN S:62 =

Scan Times

Maximum (x10 ms) S:22 = 0
Watchdog (x10 ms) S:3 (high byte) = 10
Last 100 uSec Scan Time S:35 = 0
Scan Toggle Bit S:33/9 = 0

Math

Math Overflow Selected S:2/14 = 0 Math Register (lo word) S:13 = 0
Overflow Trap S:5/0 = 0 Math Register (high word) S:14-S:13 = 0
Carry S:0/0 = 0 Math Register (32 Bit) S:14-S:13 = 0
Overflow S:0/1 = 0
Zero Bit S:0/2 = 0
Sign Bit S:0/3 = 0

Chan 0

Processor Mode S:1/0- S:1/4 = Remote Program Mode
Node Address S:15 (low byte) = 0 Outgoing Msg Cmd Pending S:33/2 = 0
Baud Rate S:15 (high byte) = ?
Channel Mode S:33/3 = 0
Comms Active S:33/4 = 0
Incoming Cmd Pending S:33/0 = 0
Msg Reply Pending S:33/1 = 0

Debug

Suspend Code S:7 = 0
Suspend File S:8 = 0

Errors

Fault Override At Power Up S:1/8 = 0 Fault Routine S:29 = 0
Startup Protection Fault S:1/9 = 0 Major Error S:6 = 0h
Major Error Halt S:1/13 = 0
Overflow Trap S:5/0 = 0 Error Description:
Control Register Error S:5/2 = 0
Major Error Executing User Fault Rtn. S:5/3 = 0
Battery Low S:5/11 = 0
Input Filter Selection Modified S:5/13 = 0
ASCII String Manipulation error S:5/15 = 0

Protection

Deny Future Access S:1/14 = No
Data File Overwrite Protection Lost S:36/10 = False

Mem Module

Memory Module Loaded On Boot S:5/8 = 0
Password Mismatch S:5/9 = 0
Load Memory Module On Memory Error S:1/10 = 0
Load Memory Module Always S:1/11 = 0
On Power up Go To Run (Mode Behavior) S:1/12 = 0
Program Compare S:2/9 = 0
Data File Overwrite Protection Lost S:36/10 = 0

Forces

Forces Enabled S:1/5 = Yes
Forces Installed S:1/6 = No

Offset	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	(Symbol)	Description
B3:0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		

Offset	EN	TT	DN	BASE	PRE	ACC	(Symbol)	Description
T4:0	0	0	0	.01 sec	200	0	(REJECT DELAY)	
T4:1	0	0	0	.01 sec	200	0	(REJECT_TIME)	

Offset	CU	CD	DN	OV	UN	UA	PRE	ACC	(Symbol)	Description
C5:0	0	0	0	0	0	0	0	0		

Offset	EN	EU	DN	EM	ER	UL	IN	FD	LEN	POS	(Symbol)	Description
R6:0	0	0	0	0	0	0	0	0	100	0	(BSR_REJECT_FIFO)	

Offset	0	1	2	3	4	5	6	7	8	9
N7:0	0									

Offset	0	1	2	3	4
F8:0	0				

[illegible]

Offset	0	1	2	3	4	5	6	7	8	9
N10:0	0	0	0	0	0	0	0	0	0	0

Data File B11 (bin) -- RJECT_FIFO

Offset	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	(Symbol) Description
B11:0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	(BSR_REJECT_BITS_FIFO)
B11:1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:7	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:8	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:9	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:11	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:12	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:13	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:14	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:15	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:16	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:17	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:18	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:19	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:20	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:21	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:22	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:23	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:24	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:25	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:26	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:27	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:28	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:29	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:30	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:31	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:32	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:33	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:34	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:35	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:36	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:37	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:38	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:39	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:41	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:42	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:43	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:44	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:45	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:46	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:47	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:48	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:49	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:50	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:51	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:52	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:53	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:54	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:55	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:56	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:57	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:58	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:59	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:60	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:61	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:62	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:63	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:64	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:65	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:66	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Data File B11 (bin) -- RJECT_FIFO																	
Offset	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	(Symbol) Description
B11:67	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:68	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:69	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:70	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:71	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:72	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:73	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:74	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:75	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:76	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:77	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:78	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:79	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:80	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:81	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:82	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:83	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:84	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:85	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:86	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:87	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:88	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:89	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:90	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:91	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:92	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:93	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:94	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:95	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:96	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:97	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:98	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:99	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:100	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:101	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:102	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:103	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:104	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:105	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:106	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:107	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:108	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:109	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:110	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:111	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:112	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:113	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:114	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:115	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:116	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:117	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:118	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:119	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:120	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:121	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:122	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:123	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:124	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:125	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:126	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:127	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:128	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:129	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:130	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:131	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:132	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
B11:133	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Offset	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	(Symbol)	Description
B11:134	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:135	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:136	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:137	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:138	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:139	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:140	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:141	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:142	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:143	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:144	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:145	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:146	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:147	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:148	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:149	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:150	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:151	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:152	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:153	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:154	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:155	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:156	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:157	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:158	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:159	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:160	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:161	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:162	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:163	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:164	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:165	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:166	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:167	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:168	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:169	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:170	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:171	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:172	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:173	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:174	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:175	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:176	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:177	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:178	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:179	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:180	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:181	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:182	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:183	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:184	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:185	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:186	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:187	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:188	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:189	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:190	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:191	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:192	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:193	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:194	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:195	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:196	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:197	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:198	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:199	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:200	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		

Offset	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	(Symbol)	Description
B11:201	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:202	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:203	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:204	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:205	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:206	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:207	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:208	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:209	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:210	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:211	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:212	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:213	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:214	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:215	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:216	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:217	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:218	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:219	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:220	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:221	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:222	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:223	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:224	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:225	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:226	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:227	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:228	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:229	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:230	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:231	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:232	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:233	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:234	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:235	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:236	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:237	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:238	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:239	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:240	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:241	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:242	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:243	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:244	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:245	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:246	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:247	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:248	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:249	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:250	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:251	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:252	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:253	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:254	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
B11:255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		

Address/Symbol Database

Address	Symbol	Scope	Description	Sym Group	Dev. Code	Al
B3:0/0	100MS_PULSE	Global				
B3:0/1	BOX_REJECT_DETECTED	Global				
B3:0/2						
B9:0/0	BOX_IS_AT_PE1	Global				
B9:0/1	BOX_IS_AT_PE2	Global				
B9:0/2	BOX_AT_PE1_IS_REJECT	Global				
B9:0/3	CONVEYOR_RUNNING	Global				
B9:0/15	PULSED_REJECT	Global				
B11:0	BSR_REJECT_BITS_FIFO	Global				
B11:0/4						
N10:0	100MS_FRC	Global	100ms in units of free-running-clock			
N10:1	100MS_PULSE_ACC	Global				
R6:0	BSR_REJECT_FIFO	Global				
S:0			Arithmetic Flags			
S:0/0			Processor Arithmetic Carry Flag			
S:0/1			Processor Arithmetic Underflow/ Overflow Flag			
S:0/2			Processor Arithmetic Zero Flag			
S:0/3			Processor Arithmetic Sign Flag			
S:1			Processor Mode Status/ Control			
S:1/0			Processor Mode Bit 0			
S:1/1			Processor Mode Bit 1			
S:1/2			Processor Mode Bit 2			
S:1/3			Processor Mode Bit 3			
S:1/4			Processor Mode Bit 4			
S:1/5			Forces Enabled			
S:1/6			Forces Present			
S:1/7			Comms Active			
S:1/8			Fault Override at Powerup			
S:1/9			Startup Protection Fault			
S:1/10			Load Memory Module on Memory Error			
S:1/11			Load Memory Module Always			
S:1/12			Load Memory Module and RUN			
S:1/13			Major Error Halted			
S:1/14			Access Denied			
S:1/15			First Pass			
S:2/0			STI Pending			
S:2/1			STI Enabled			
S:2/2			STI Executing			
S:2/3			Index Addressing File Range			
S:2/4			Saved with Debug Single Step			
S:2/5			DH-485 Incoming Command Pending			
S:2/6			DH-485 Message Reply Pending			
S:2/7			DH-485 Outgoing Message Command Pending			
S:2/15			Comms Servicing Selection			
S:3			Current Scan Time/ Watchdog Scan Time			
S:4			Time Base			
S:5/0			Overflow Trap			
S:5/2			Control Register Error			
S:5/3			Major Err Detected Executing UserFault Routine			
S:5/4			M0-M1 Referenced on Disabled Slot			
S:5/8			Memory Module Boot			
S:5/9			Memory Module Password Mismatch			
S:5/10			STI Overflow			
S:5/11			Battery Low			
S:6			Major Error Fault Code			
S:7			Suspend Code			
S:8			Suspend File			
S:9			Active Nodes			
S:10			Active Nodes			
S:11			I/O Slot Enables			
S:12			I/O Slot Enables			
S:13			Math Register			
S:14			Math Register			
S:15			Node Address/ Baud Rate			
S:16			Debug Single Step Rung			
S:17			Debug Single Step File			
S:18			Debug Single Step Breakpoint Rung			
S:19			Debug Single Step Breakpoint File			
S:20			Debug Fault/ Powerdown Rung			
S:21			Debug Fault/ Powerdown File			
S:22			Maximum Observed Scan Time			
S:23			Average Scan Time			
S:24			Index Register			
S:25			I/O Interrupt Pending			
S:26			I/O Interrupt Pending			
S:27			I/O Interrupt Enabled			
S:28			I/O Interrupt Enabled			
S:29			User Fault Routine File Number			
S:30			STI Setpoint			
S:31			STI File Number			
S:32			I/O Interrupt Executing			
S:33			Extended Proc Status Control Word			
S:33/0			Incoming Command Pending			
S:33/1			Message Reply Pending			

Address/Symbol Database

Address	Symbol	Scope	Description	Sym Group	Dev. Code	Al
S:33/2			Outgoing Message Command Pending			
S:33/3			Selection Status User/DF1			
S:33/4			Communicat Active			
S:33/5			Communicat Servicing Selection			
S:33/6			Message Servicing Selection Channel 0			
S:33/7			Message Servicing Selection Channel 1			
S:33/8			Interrupt Latency Control Flag			
S:33/9			Scan Toggle Flag			
S:33/10			Discrete Input Interrupt Reconfigur Flag			
S:33/11			Online Edit Status			
S:33/12			Online Edit Status			
S:33/13			Scan Time Timebase Selection			
S:33/14			DTR Control Bit			
S:33/15			DTR Force Bit			
S:34			Pass-thru Disabled			
S:34/0			Pass-Thru Disabled Flag			
S:34/1			DH+ Active Node Table Enable Flag			
S:34/2			Floating Point Math Flag Disable,Fl			
S:35			Last 1 ms Scan Time			
S:36			Extended Minor Error Bits			
S:36/8			DII Lost			
S:36/9			STI Lost			
S:36/10			Memory Module Data File Overwrite Protection			
S:37			Clock Calendar Year			
S:38			Clock Calendar Month			
S:39			Clock Calendar Day			
S:40			Clock Calendar Hours			
S:41			Clock Calendar Minutes			
S:42			Clock Calendar Seconds			
S:43			STI Interrupt Time			
S:44			I/O Event Interrupt Time			
S:45			DII Interrupt Time			
S:46			Discrete Input Interrupt- File Number			
S:47			Discrete Input Interrupt- Slot Number			
S:48			Discrete Input Interrupt- Bit Mask			
S:49			Discrete Input Interrupt- Compare Value			
S:50			Processor Catalog Number			
S:51			Discrete Input Interrupt- Return Number			
S:52			Discrete Input Interrupt- Accumulat			
S:53			Reserved/ Clock Calendar Day of the Week			
S:55			Last DII Scan Time			
S:56			Maximum Observed DII Scan Time			
S:57			Operating System Catalog Number			
S:58			Operating System Series			
S:59			Operating System FRN			
S:61			Processor Series			
S:62			Processor Revision			
S:63			User Program Type			
S:64			User Program Functional Index			
S:65			User RAM Size			
S:66			Flash EEPROM Size			
S:67			Channel 0 Active Nodes			
S:68			Channel 0 Active Nodes			
S:69			Channel 0 Active Nodes			
S:70			Channel 0 Active Nodes			
S:71			Channel 0 Active Nodes			
S:72			Channel 0 Active Nodes			
S:73			Channel 0 Active Nodes			
S:74			Channel 0 Active Nodes			
S:75			Channel 0 Active Nodes			
S:76			Channel 0 Active Nodes			
S:77			Channel 0 Active Nodes			
S:78			Channel 0 Active Nodes			
S:79			Channel 0 Active Nodes			
S:80			Channel 0 Active Nodes			
S:81			Channel 0 Active Nodes			
S:82			Channel 0 Active Nodes			
S:83			DH+ Active Nodes			
S:84			DH+ Active Nodes			
S:85			DH+ Active Nodes			
S:86			DH+ Active Nodes			
T4:0	REJECT_DELAY	Global				
T4:0/DN	REJECT_TIME_BIT	Global	Activate diverter for duration of REJECT_TIME timer			
T4:1	REJECT_TIME	Global				

Address	Instruction	Description
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Symbol Group Database

Group_Name	Description
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