RSLogix Micro Project Report



Processor Information

Processor Type: Bul.1763 MicroLogix 1100 Series B

Processor Name: UNTITLED

Total Memory Used: *

Total Memory Left: *

Program Files: 3

Data Files: 9

Program ID: 0

I/O Configuration

)		
L		
2		
3		
1		

Bul.1763

MicroLogix 1100 Series B

Channel Configuration

```
CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Edit Resource/Owner Timeout: 60 CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Passthru Link ID: 1
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Write Protected: No
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Comms Servicing Selection: Yes
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Message Servicing Selection: Yes
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex 1st AWA Append Character: \d
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex 2nd AWA Append Character: \a
  Source ID: 1 (decimal)
  Baud: 19200
  Parity: NONE
  Control Line : No Handshaking
  Error Detection: CRC
  Embedded Responses: Auto Detect
  Duplicate Packet Detect: Yes
  ACK Timeout (x20 ms): 50
  NAK Retries: 3
  ENQ Retries: 3
CHANNEL 1 (SYSTEM) - Driver: Ethernet
  CHANNEL 1 (SYSTEM) - Driver: Ethernet Edit Resource/Owner Timeout: 60
  CHANNEL 1 (SYSTEM) - Driver: Ethernet Passthru Link ID: 1
  CHANNEL 1 (SYSTEM) - Driver: Ethernet Write Protected: No
  CHANNEL 1 (SYSTEM) - Driver: Ethernet Comms Servicing Selection: Yes
  CHANNEL 1 (SYSTEM) - Driver: Ethernet Message Servicing Selection: Yes
  Hardware Address: 00:00:00:00:00:00
  IP Address: 0.0.0.0
  Subnet Mask: 0.0.0.0
  Gateway Address: 0.0.0.0
  Msg Connection Timeout (x 1mS):
  Msg Reply Timeout (x mS): 3000
  Inactivity Timeout (x Min): 30
  Bootp Enable: Yes
  Dhcp Enable No
  SNMP Enable: No
  HTTP Enable: Yes
  Auto Negotiate Enable: Yes
  Port Speed Enable: 10/100 Mbps Full Duplex/Half Duplex
  Contact:
  Location:
```

Program File List

Name	Number	Type	Rungs	Debug	Bytes
[SYSTEM]	0	SYS	0	No	0
	1	SYS	0	No	0
	2	LADDER	11	No	469

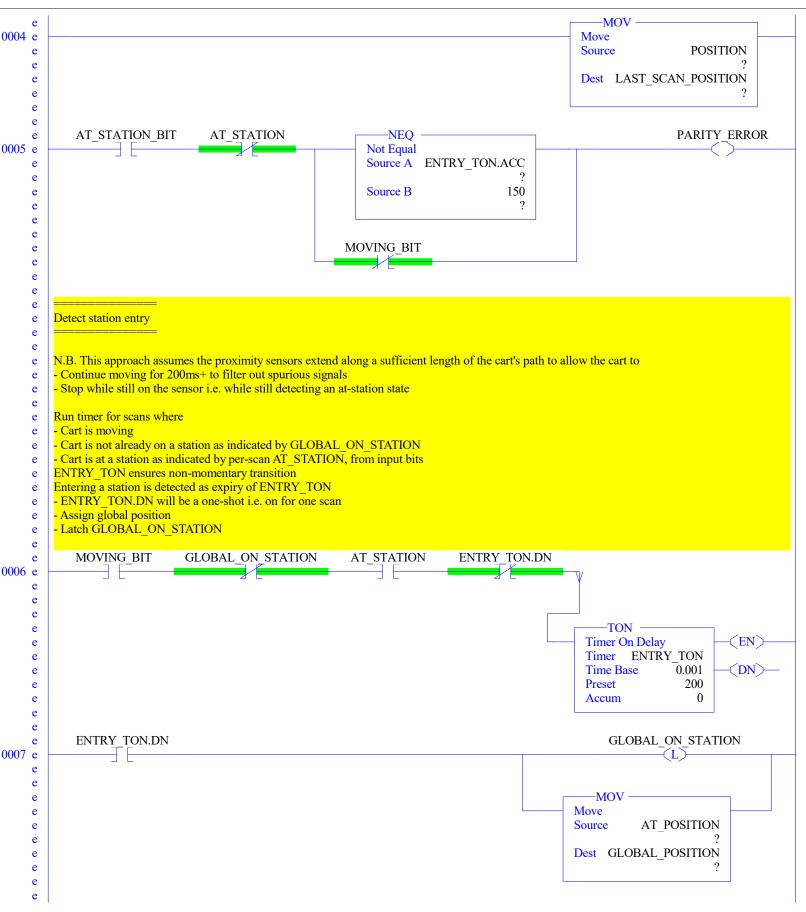
Data File List

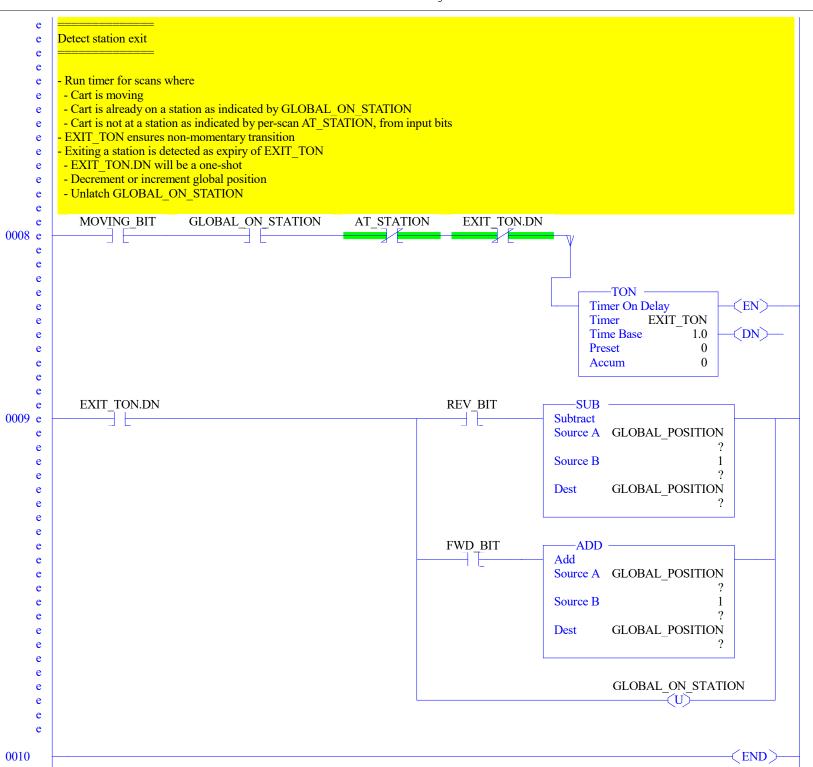
Name	Number	Type	Scope	Debug	Words	Elements	Last			
OUTPUT	0	0	Global	No	12	4	O:3			
INPUT	1	I	Global	No	18	6	I:5			
STATUS	2	S	Global	No	0	66	S:65			
BINARY	3	В	Global	No	1	1	B3:0			
TIMER	4	T	Global	No	3	1	T4:0			
COUNTER	5	C	Global	No	3	1	C5:0			
CONTROL	6	R	Global	No	3	1	R6:0			
INTEGER	7	N	Global	No	1	1	N7:0			
FLOAT	8	F	Global	No	2	1	F8:0			



e

```
e
         At-station check
     e
     e
     e
         Valid Station: TWO BIT ONE BIT PARITY BIT AT STATION BIT = STATION INT; STATION INT&12 (12 = binary 1100)
     e
     e
         N.B. those four bits are the low bits of INTeger STATION INT
     e
         S: T|O|P|A
     e
         1: 0|0|1|1 = 3; 0
     e
         2: 0|1|0|1 = 5; 4
     e
         3: \ 1|0|0|1 = 9; 8
     e
         4: 1|1|1|1=15; 12
     e
     e
                          -EQU
                                                                                                                     AT STATION BIT
     e
0002 e
                      Source A STATION INT
     e
     e
                                              3
                      Source B
                                                                                                               -AND
                                                                                                           Bitwise AND
                                                                                                           Source A STATION INT
     e
                          -EQU
                                                                                                                                  12
                                                                                                           Source B
     e
                      Equal
     e
                      Source A STATION INT
                                                                                                           Dest
                                                                                                                      AT_POSITION
     e
     e
                      Source B
                                              5
     e
     e
     e
     e
                          -EQU -
     e
     e
                      Source A STATION INT
     e
     e
                                              9
                      Source B
     e
     e
     e
     e
     e
                          -EOU
     e
                      Source A STATION INT
     e
     e
                      Source B
                                             15
     e
     e
     e
     e
     e
         Housekeeping
     e
     e
     e
     e
         Assign moving bit if cart is moving in a reverse or forward direction
         Save last scan position
     e
         Odd parity check for three bits: TWO BIT; ONE BIT; PARITY BIT.
     e
         - Only after 150ms (75%) of entry timer, or if cart is not moving
     e
         - AT STATION BIT is excluded from parity check
     e
         - Nothing is done with parity at this point, it is only here to show it can be done
     e
     e
                  REV BIT
                                                                                                                               MOVING BIT
     e
0003 e
                    3 6
     e
                  FWD BIT
     e
     e
     e
```





Data File OO (bin) -- OUTPUT

Offset	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0		
0:0.0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B
0:0.1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B
0:0.2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B
0:0.3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B

Data File I1 (bin) -- INPUT

Offset	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0		
I:0.0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B
I:0.1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B
I:0.2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B
I:0.3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B
I:0.4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B-Analog
I:0.5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B-Analog

```
Data File S2 (hex) -- STATUS
```

```
Main
```

```
Processor Mode S:1/0 - S:1/4 = Remote Program Mode
On Power up Go To Run (Mode Behavior) S:1/12 = 0
First Pass S:1/15 = No
Free Running Clock S:4 = 0000-0000-0000-0000
Proc
OS Catalog Number S:57 = 1100
                                        User Program Type S:63 = 8001h
OS Series S:58 = A
                                        Compiler Revision Number S:64 =
OS FRS S:59 =
Processor Catalog Number S:60 =
Processor Series S:61 = A
Processor FRN S:62 =
Scan Times
Maximum (x10 ms) S:22 = 0
Watchdog (x10 ms) S:3 (high byte) = 10
Last 100 uSec Scan Time S:35 = 0
Scan Toggle Bit S:33/9 = 0
Math
Math Overflow Selected S:2/14 = 0
                                            Math Register (lo word) S:13 = 0
Overflow Trap S:5/0 = 0
                                             Math Register (high word) S:14-S:13 = 0
Carry S:0/0 = 0
                                             Math Register (32 Bit) S:14-S:13 = 0
Overflow S:0/1 = 0
Zero Bit S:0/2 = 0
Sign Bit S:0/3 = 0
Chan 0
Processor Mode S:1/0- S:1/4 = Remote Program Mode
Node Address S:15 (low byte) = 0
                                 Outgoing Msg Cmd Pending S:33/2 = 0
Baud Rate S:15 (high byte) = ?
Channel Mode S:33/3 = 0
Comms Active S:33/4 = 0
Incoming Cmd Pending S:33/0 = 0
Msg Reply Pending S:33/1 = 0
Debug
Suspend Code S:7 = 0
Suspend File S:8 = 0
Errors
Fault Override At Power Up S:1/8 = 0
                                             Fault Routine S:29 = 0
Startup Protection Fault S:1/9 = 0
                                             Major Error S:6 = 0h
Major Error Halt S:1/13 = 0
Overflow Trap S:5/0 = 0
                                             Error Description:
Control Register Error S:5/2 = 0
Major Error Executing User Fault Rtn. S:5/3 = 0
Battery Low S:5/11 = 0
Input Filter Selection Modified S:5/13 = 0
ASCII String Manipulation error S:5/15 = 0
Protection
Deny Future Access S:1/14 = No
Data File Overwrite Protection Lost S:36/10 = False
```

Mem Module

```
Memory Module Loaded On Boot S:5/8 = 0
Password Mismatch S:5/9 = 0
Load Memory Module On Memory Error S:1/10 = 0
Load Memory Module Always S:1/11 = 0
On Power up Go To Run (Mode Behavior) S:1/12 = 0
Program Compare S:2/9 = 0
Data File Overwrite Protection Lost S:36/10 = 0
```

Data File S2 (hex) -- STATUS

Forces

Forces Enabled S:1/5 = Yes Forces Installed S:1/6 = No Data File B3 (bin) -- BINARY

Offset 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0 (Symbol) Description

B3:0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

Data File T4 -- TIMER

Offset EN TT DN BASE PRE ACC (Symbol) Description
T4:0 0 0 0 .01 sec 0 0

Data File C5 -- COUNTER

Offset CU CD DN OV UN UA PRE ACC (Symbol) Description
C5:0 0 0 0 0 0 0 0

Data File R6 -- CONTROL

Offset EN EU DN EM ER UL IN FD LEN POS (Symbol) Description R6:0 0 0 0 0 0 0 0 0 0

Data File N7 (dec) -- INTEGER

Offset 0 1 2 3 4 5 6 7 8 9

N7:0 0

Data File F8 -- FLOAT

Offset 0 1 2 3 4

F8:0 0

Address/Symbol Database

Address	Symbol	Scope	Description	Sym Group	Dev. Code	ABV
I:0.0	INP_AT_STATION_BIT GLOBAL_POSITION AT_STATION ENTRY_TON POSITION ON_STATION MOVING_BIT REV_BIT LAST_SCAN_POSITION OLD_POSITION TWO_BIT PARITY_ERROR FWD_BIT INP_FWD_BIT AT_TWO_BIT AT_TWO_BIT AT_STATION_BIT	Global Global Global Global Global Global Global				
N7:0 s:0	XXX	Global	Arithmetic Flags			
S:0/0 S:0/1 S:0/2 S:0/2 S:0/3 S:1/1 S:1/0 S:1/1 S:1/2 S:1/3 S:1/4 S:1/5 S:1/6 S:1/7 S:1/8 S:1/9 S:1/10 S:1/11 S:1/12 S:1/13 S:1/14 S:1/15 S:2/0 S:2/1 S:2/2 S:2/3 S:2/6 S:2/7 S:2/5 S:2/6 S:2/7 S:2/5 S:3 S:4 S:5/0 S:5/2 S:5/3 S:5/8 S:5/9 S:5/10 S:5/2 S:5/1 S:2/2 S:5/1 S:2/2 S:5/1 S:5/2 S:5/2 S:5/3 S:5/3 S:5/4 S:5/5 S:5/8 S:5/9 S:5/10 S:5/1 S:5/2 S:5/1 S:5/2 S:5/3 S:5/8 S:5/9 S:5/10 S:5/2 S:5/1 S:5/2 S:5/3 S:5/8 S:5/9 S:5/10 S:5/11 S:6/5 S:7 S:8/5 S:5/9 S:5/10 S:5/2 S:5/8 S:5/9 S:5/8 S:5/9 S:5/10 S:5/2 S:5/8 S:5/9 S:5/8 S:5			Processor Arithmetic Carry Flag Processor Arithmetic Underflow/ Overflow Flag Processor Arithmetic Zero Flag Processor Arithmetic Sign Flag Processor Mode Status/ Control Processor Mode Bit 1 Processor Mode Bit 1 Processor Mode Bit 2 Processor Mode Bit 3 Processor Mode Bit 3 Processor Mode Bit 4 Forces Enabled Forces Present Comms Active Fault Override at Powerup Startup Protection Fault Load Memory Module and Memory Error Load Memory Module and RUN Major Error Halted Access Denied First Pass STI Fending STI Enabled STI Executing Index Addressing File Range Saved with Debug Single Step DH-485 Incoming Command Pending DH-485 Outgoing Message Command Pending Comms Servicing Selection Current Scan Time/ Watchdog Scan Time Time Base Overflow Trap Control Register Error Major Err Detected Executing UserFault Routine MO-MI Referenced on Disabled Slot Memory Module Boot Memory Module Password Mismatch STI Overflow Battery Low Major Error Fault Code Suspend Code Suspend Code Suspend Code Suspend File Active Nodes I/O Slot Enables I/O Slot Enables Math Register Node Address/ Baud Rate Debug Single Step Fung Debug Single Step Freakpoint Rung Debug Single Step Freakpoint Rung Debug Fault/ Powerdown Rung Debug Fault/ Powerdown File Maximum Observed Scan Time Index Register I/O Interrupt Pending I/O Interrupt Pending I/O Interrupt Pending I/O Interrupt Enabled User Fault Routine File Number STI Setpoint			
5:30			SII SETPOINT			

Address/Symbol Database

Address	Symbol	Scope	Description	Sym Group	Dev. Code ABV
S:31			STI File Number		
S:32			I/O Interrupt Executing		
S:33			Extended Proc Status Control Word		
s:33/0			Incoming Command Pending		
S:33/1			Message Reply Pending		
S:33/2 S:33/3			Outgoing Message Command Pending Selection Status User/DF1		
S:33/4			Communicat Active		
S:33/5			Communicat Servicing Selection		
s:33/6			Message Servicing Selection Channel 0		
S:33/7			Message Servicing Selection Channel 1		
S:33/8			Interrupt Latency Control Flag		
S:33/9 S:33/10			Scan Toggle Flag Discrete Input Interrupt Reconfigur Flag		
S:33/10 S:33/11			Online Edit Status		
S:33/12			Online Edit Status		
s:33/13			Scan Time Timebase Selection		
S:33/14			DTR Control Bit		
S:33/15			DTR Force Bit		
S:34 S:34/0			Pass-thru Disabled Pass-Thru Disabled Flag		
S:34/1			DH+ Active Node Table Enable Flag		
S:34/2			Floating Point Math Flag Disable, Fl		
S:35			Last 1 ms Scan Time		
S:36			Extended Minor Error Bits		
S:36/8 S:36/9			DII Lost		
S:36/10			STI Lost Memory Module Data File Overwrite Protection		
S:37			Clock Calendar Year		
s:38			Clock Calendar Month		
S:39			Clock Calendar Day		
S:40			Clock Calendar Hours		
S:41 S:42			Clock Calendar Minutes Clock Calendar Seconds		
S:43			STI Interrupt Time		
S:44			I/O Event Interrupt Time		
S:45			DII Interrupt Time		
S:46			Discrete Input Interrupt- File Number		
S:47 S:48			Discrete Input Interrupt- Slot Number Discrete Input Interrupt- Bit Mask		
S:49			Discrete Input Interrupt- Compare Value		
S:50			Processor Catalog Number		
S:51			Discrete Input Interrupt- Return Number		
S:52			Discrete Input Interrupt- Accumulat		
S:53			Reserved/ Clock Calendar Day of the Week		
S:55 S:56			Last DII Scan Time Maximum Observed DII Scan Time		
S:57			Operating System Catalog Number		
S:58			Operating System Series		
S:59			Operating System FRN		
S:61			Processor Series		
S:62 S:63			Processor Revision		
S:64			User Program Type User Program Functional Index		
S:65			User RAM Size		
S:66			Flash EEPROM Size		
S:67			Channel O Active Nodes		
S:68 S:69			Channel O Active Nodes Channel O Active Nodes		
S:09 S:70			Channel O Active Nodes		
S:71			Channel O Active Nodes		
S:72			Channel O Active Nodes		
S:73			Channel O Active Nodes		
S:74			Channel O Active Nodes		
S:75 S:76			Channel 0 Active Nodes Channel 0 Active Nodes		
S:77			Channel O Active Nodes		
S:78			Channel O Active Nodes		
s:79			Channel O Active Nodes		
S:80			Channel O Active Nodes		
S:81			Channel O Active Nodes		
S:82 S:83			Channel 0 Active Nodes DH+ Active Nodes		
S:84			DH+ Active Nodes DH+ Active Nodes		
S:85			DH+ Active Nodes		
S:86			DH+ Active Nodes		

Instruction Comment Database

Address Instruction Description

Group_Name Description