The Wayback Machine - https://web.archive.org/web/20060926002712/http://tmb.elitedvb.net: 80/dvd-game/index.php/PORTS wayback Machine - https://web/archive.org/web/20060926002712/http://tmb.elitedvb.net: 80/dvd-game/index.php/PORTS wayback Machine - https://web/archive.org/web/20060926002712/http://tmb.elitedvb.net: 80/dvd-game/index.php/PORTS wayback Machine - https://web/archive.org/we

## **PORTS**

## From Dvd-game

The used CPU has some registers which are ports.

P8 is the serial port and brought to CN302.

P4 is used for interfacing the Cover Detection, Cover Output, Error Output pins....

- P4[5] = "Cover In" (connect to cover input switch)
  P4[6] = "Cover Out" (connect to DI cover line)
  P4[7] = "error" (connect to DI error line)

P5: unkown. [5] and [6] seem to be used [80B89,80B89,80B89]

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