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## **Serial Commands**

## From Dvd-game

The drive accepts 8 debug commmands via the serial interface found on CN302. It reads a single bit from SBI3 when SBT3 goes high.

For every byte(?) a T9 capture B interrupt (0x4C) is generated and handled at [81119,81118,81119] Incoming data goes into a 10 byte ringbuffer at [40EB48,40EB48,40EB48]. [40EB42,40EB42,40EB42] is its write offset.

```
SERIAL_CMD_FF:

Read memory, does memcopy to 0x40eb54. max 0x20 bytes. Then
transmits over serial if.
Never actually used it, should go over SB03.
        00
scr addr M
scr addr L
         scr addr H
0
0
            nr of bytes to write 0
SERIAL_CMD_FE:
Writes one or two bytes to memory
           00
ADDR_M
ADDR_L
BYTE_1
ADDR_H
            BYTE_2
0
            0
1-2
0
                         nr of bytes to write
SERIAL_CMD_FD:
Dumps some interesting memory locations to [40D600,,]
    .....
R[9] z

memcpy[(400600,], [8136,], 0x48);

"(u24 *) [400648,,] = [8136,,];

"(u8 *) [400648,,] = "(u24 *)[817E,,];

"(u8 *) [40064F,,] = "(u2 *)[817E,,];

"(u8 *) [400656,,] = "(u2 *)[8182,,];

"(u16 *) [400653,,] = "(u16 *)[8182,,];

"(u16 *) [400653,] = "(u16 *)[8120,];

memcpy[(400658,],] [819E,,], 0x10);

memcpy[(400683,],] [818E,,], 0x10);

memcpy[(400683,],] [818E,,], 0x10);

memcpy[(400683,],] [818E,,], 0x10);

memcpy[(400683,],] [819E,,], 0x10);

memcpy[(400683,], [819E,,], 0x10);

memcpy[(400684,],] [8008,], 0x01);

memcpy[(400685,],] [8008,], 0x04);

memcpy[(400685,],] [808F,], 0x10);

"(u16 *) [400684,], [40687E,], 0x10);

"(u16 *) [400684,], [40687E,], 0x10);
SERIAL_CMD_FC:
if R4 |= 0, drive does not reply anymore (to be more exact it replies zeroes on dma tranfers) You can bring it back to life with R4 = 0. Also used on init with R4 = 0. Probably something like "Enable DI" *confirm*
            0-1
FF
0
0
0
SERIAL_CMD_FB:
Writes val to [81E5,,] and [40000B,,] ???
            val
SERIAL_CMD_F9:
                         -----
Same as DbgSetReadOptions
```

SERIAL.	
Allows some limited HLECommand execution	
R[0] R[1] R[2] R[3] R[4] R[5]	F8
R[1]	99
R[2]	A5
R[3]	5A
R[4]	0-1
R[5]	FF Control of the Con
R[6] R[7] R[8]	$\delta$
R[7]	$\delta$
R[8]	$\delta$
R[9]	$\delta$
<b>.</b>	

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■ This page was last modified 14:22, 21 October 2007.