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Memory

From Dvd-game

All addresses are given in the format [4,6,8]. See DriveVersions

```
.....
[804C,804C,804C] u24
[805B,805B,805B] u8
[8084,8078,8080] u16
[8090,8084,808C] u8
[8096,8084,808D] u8
[8085,8049,8081] u8
[8085,8049,8081] u8
[8085,8044,8082] u8
                                                                                                                      irq vector (set to address of irq handler)
irq depth (>0 if in interrupt)
                                                                                                                    ind depth (% ir in interrupt)
ticks
media flag, see DBG_StartStopDisc
dicmdbuf0+0 (contains most significant byte)
dicmdbuf0+1
dicmdbuf0+2
[80B7,80AB,80B3] u8
                                                                                                                      dicmdbuf0+3 (contains least significant byte)
                                                                                                                     alcombourters (contains least significant byte) dicomboufi+0 (contains most significant byte) dicomboufi+1 dicomboufi+1 dicomboufi+2 (contains least significant byte) dicomboufi+2 (contains most significant byte) dicomboufi+0 (contains most significant byte)
[80B8,80AC,80B4] u8
[80B9,80AD,80B5] u8
[80B4,80AE,80B6] u8
[80BB,80AF,80B7] u8
[80BC.80B0.80B8] u8
                                                                                                                    dicmdbuf2+0 (contains most significant byte)
dicmdbuf2+1
dicmdbuf2+2
dicmdbuf2+3 (contains least significant byte)
lowest 16 bits of n part of drive error code (see yagcd)
upper 8 bits of n part of drive error code (see yagcd)
a part of drive error code (see yagcd)
Debug state (ff: off, 0: half way unlocked, 1: unlocked)
drive status 2 (a better name should go here...)
last ticks of some event? (set to ticks to prevent idle bug)
[80BD,80B1,80B9] u8
[80BE,80B2,80BA] u8
[80BE,80B3,80BB] u8
[80DC,80D0,80D8] u16
[80DE,80DA,80D2] u8
[80E4,80D8,80E0] u8
[8192,8186,818E] u8
[8194,8188,8190] u16
[8196,8190,8198] u16
                                                                                                                    drive status
IRQ Handler
memcpy
memset
DI Command Decode
[81B0,81A4,81AC] u16
[80A74,80A74,80A74]
[82F27,82F2E,82F27]
[82F49,82F50,82F40]
[83EF7.83EEF.83EF7]
                                                                                                                     DI_IO
Disable interrupts
[8AE09.8B126.8AE56]
[8D1C4,8D164,8D21F]
                                                                                                                    XXX continuously set to 0x0C by patched firmare, purpose?
XXX kind of error state?
XXX related to error counters?
XXX ??? related to error recovery?
XXX set to 0x13 by firmware, xeno sets it to zero when disabled?
XXX ??? related to error recovery?
[819A,818E,8196]
[49EC09,40EC09,40EC00] u16
[40EC09,40EC09,40EC02] u8 array
[40EC14,40EC14,40EC14] u8
[40EC60,40EC60,40EC60] u8
[40ED01,40ED01,40ED01] u8
(oops, i probably should convert my IDA symbols into a table.)
(yes, probably :p)
```

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