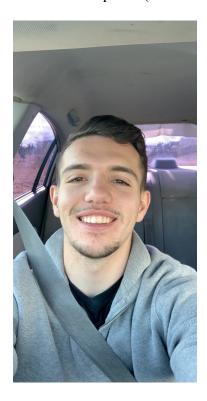# CSE 140 HW#0 – Due 2/1 11:59PM

In this homework, you will introduce yourself and your background knowledge about computer architecture. Fill the answers, convert it to a pdf file, and submit it to CatCourse HW0 module.

1. Your name and student ID

    Andre Martin (100296766)

2. Your headshot photo (that shows your face clearly)

3. Mark (circle the corresponding alphabet) the topics that you've learned (or have knowledge/ experiences). If you know the topic, please provide brief explanation where and how you learned the topic (e.g., from CSE31)

    a. **Performance metrics** (e.g., Cycles per instruction)

    b. Instruction set architecture (MIPS assembly language)

    c. MARS tool (MIPS simulator)

    d. **Single-cycle CPU architecture**

e. **Pipelined CPU architecture**

f. Memory Hierarchy (e.g., operations and performance pros/cons of DRAM, SRAM, and caches)

g. Hyperthreading

h. Multi-core architecture

i. Multi-processor architecture

j. GPU