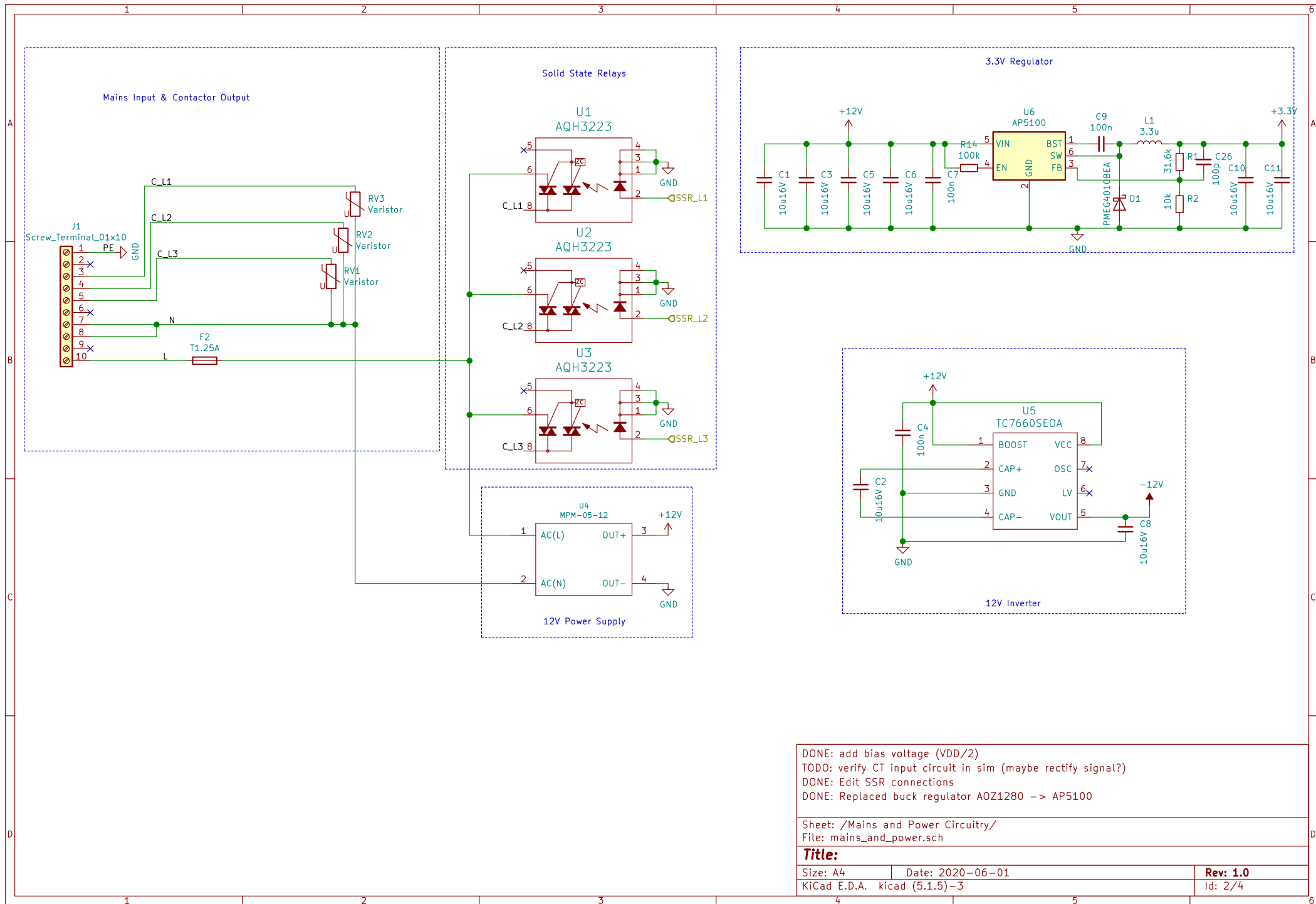


TODO: finish button/LED connections

Sheet: /
File: atmevse.sch

Title: **ATmega4808 EVSE**

Size: A4	Date: 2020-06-01	Rev: 1.0
KiCad E.D.A. kicad (5.1.5)-3		Id: 1/4



DONE: add bias voltage (VDD/2) TODO: verify CT input circuit in sim (maybe rectify signal?) DONE: Edit SSR connections DONE: Replaced buck regulator AOZ1280 -> AP5100		
Sheet: /Mains and Power Circuitry/ File: mains_and_power.sch		
Title:		
Size: A4	Date: 2020-06-01	Rev: 1.0
KiCad E.D.A. kicad (5.1.5)-3		Id: 2/4

