

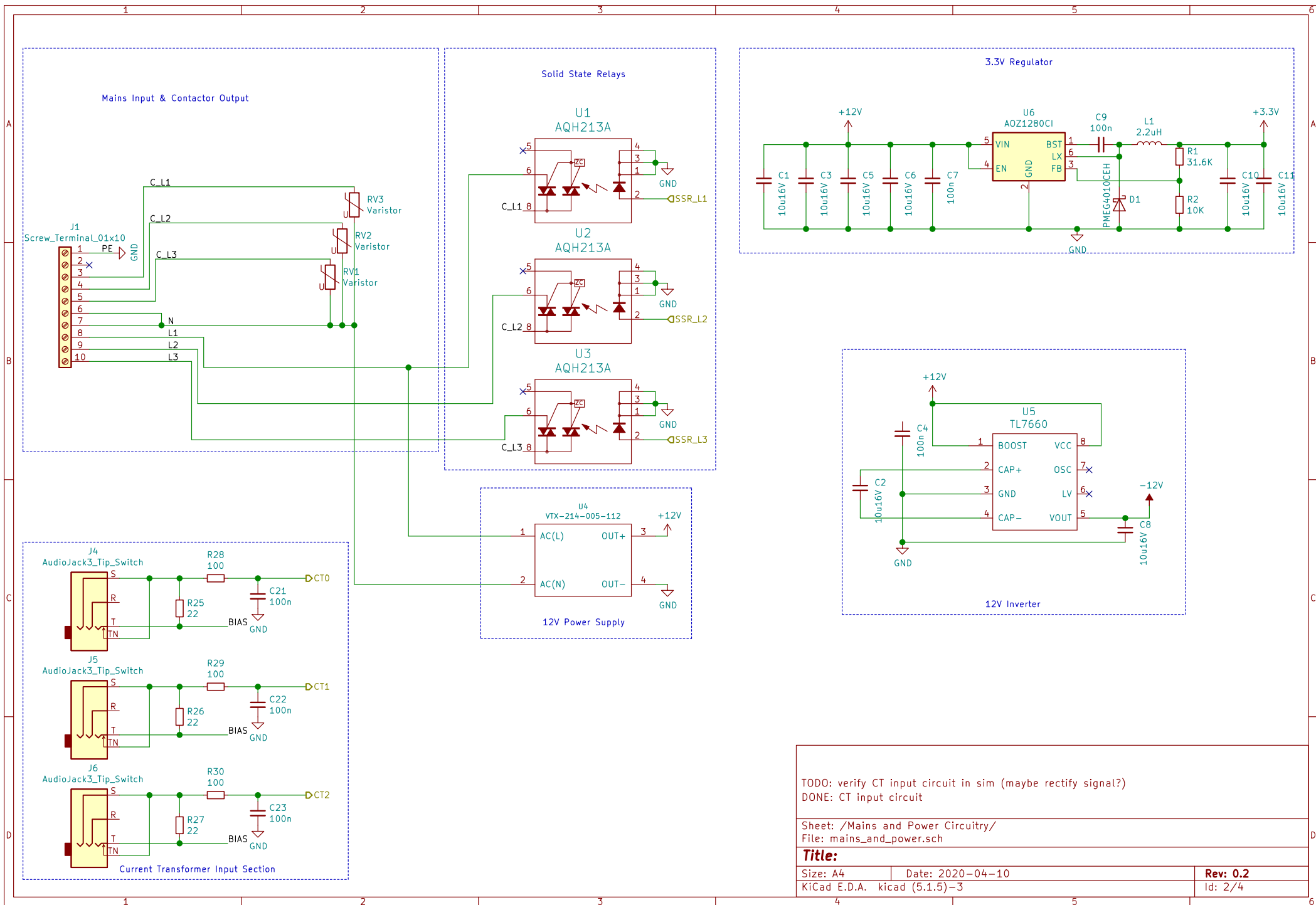
Sheet: /  
File: atmevse.sch

Title: ATmega4808 EVSE

Size: A4  
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Date: 2020-04-10  
Id: 1/4

Rev: 0.2



TODO: verify CT input circuit in sim (maybe rectify signal?)  
 DONE: CT input circuit

Sheet: /Mains and Power Circuitry/  
 File: mains\_and\_power.sch

<b>Title:</b>		
Size: A4	Date: 2020-04-10	Rev: 0.2
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Id: 4/4