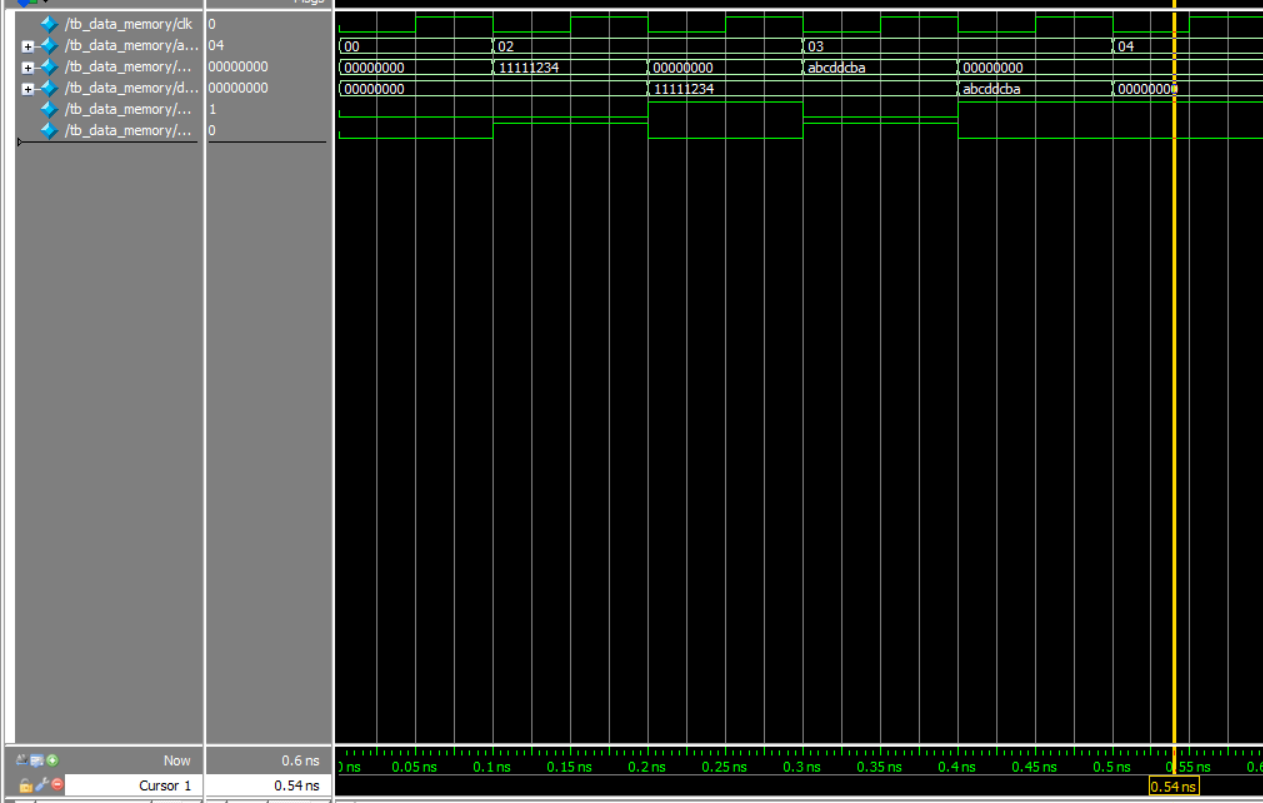
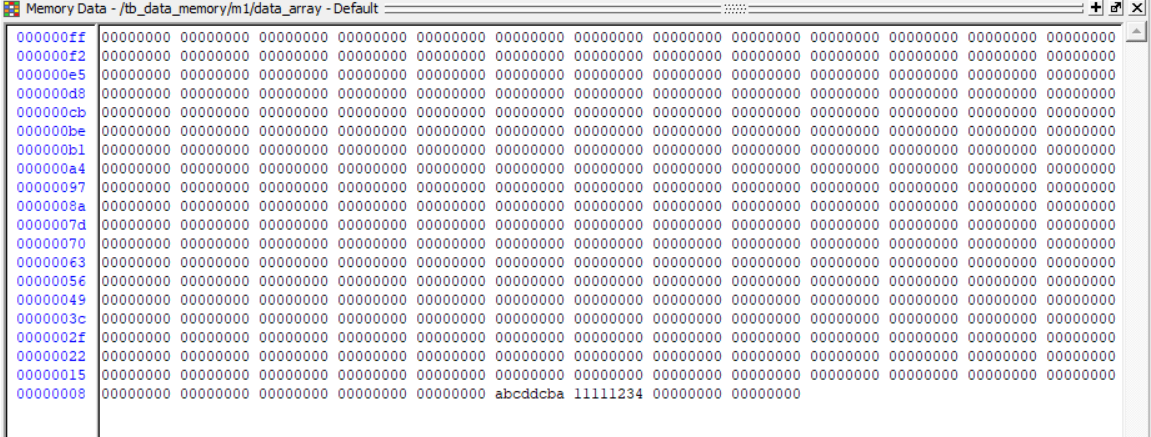
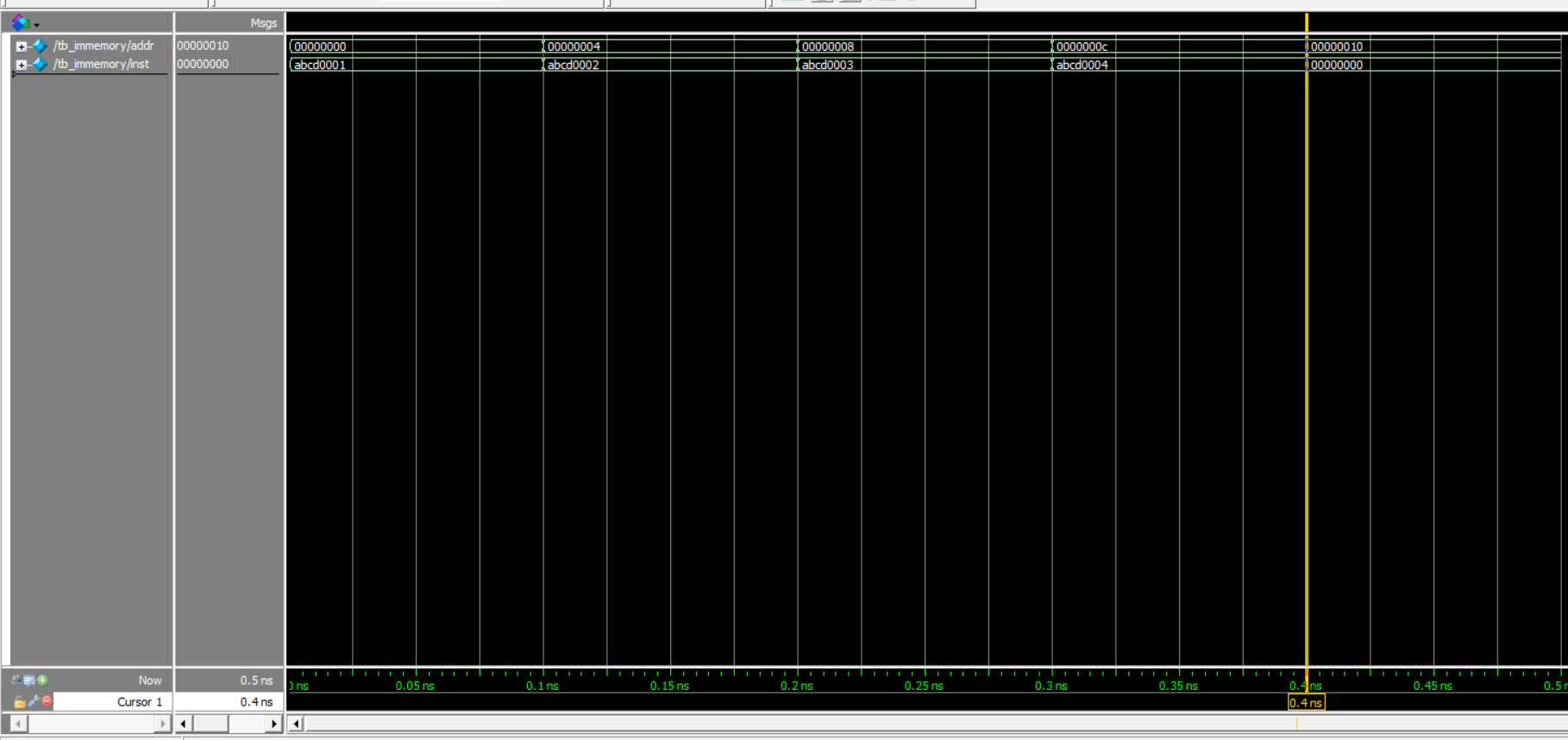
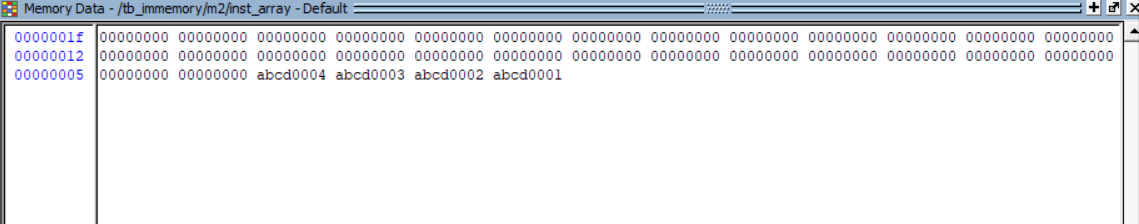
Data\_mem\_testbench:



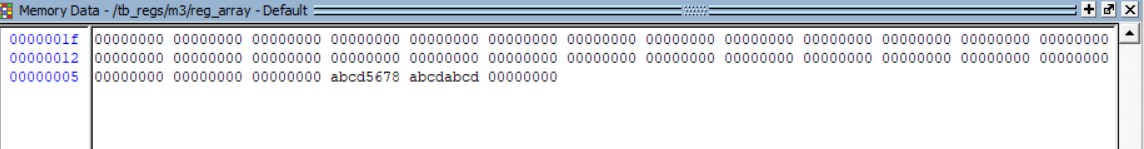
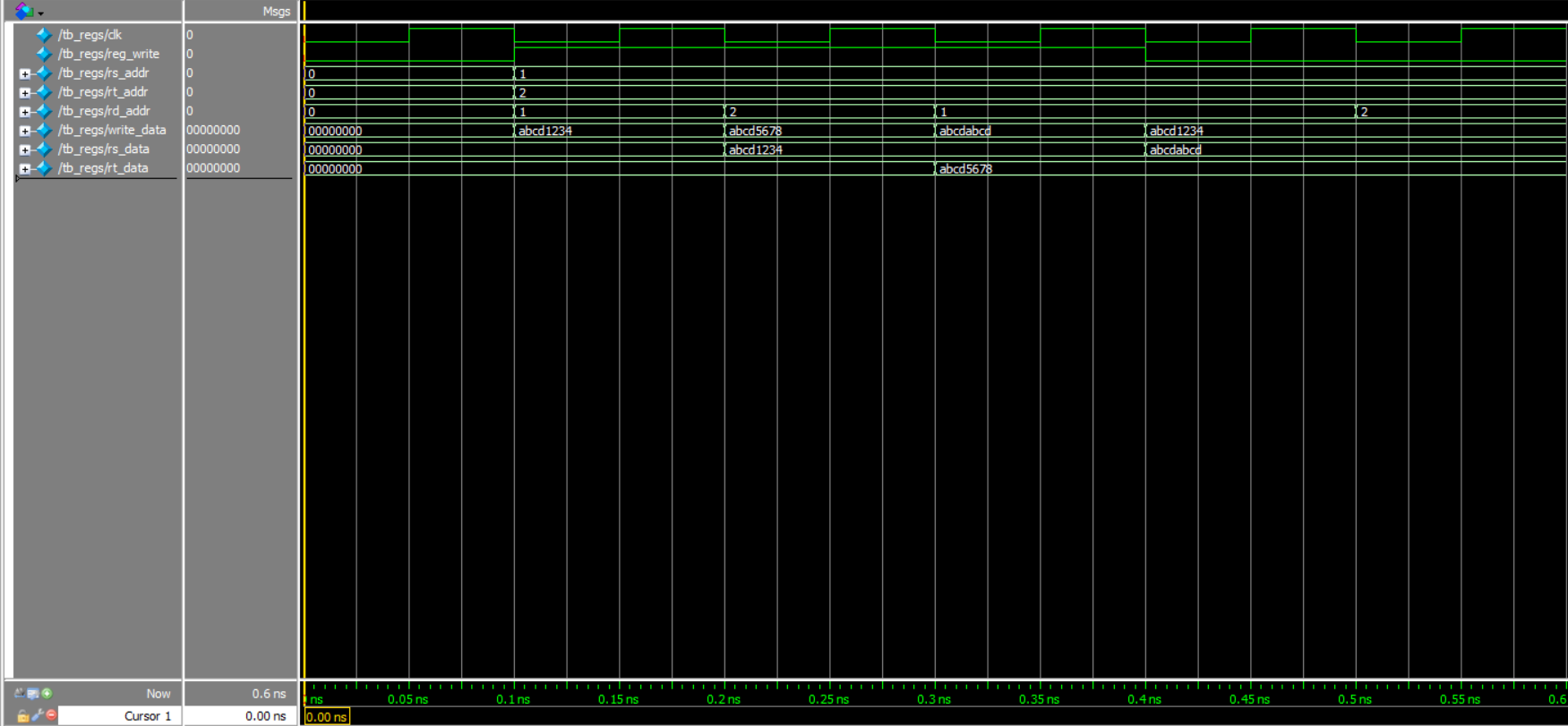


Immemory\_testbench:

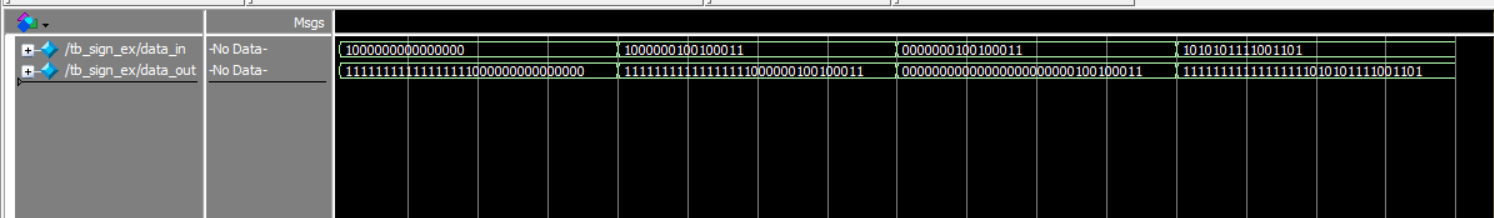




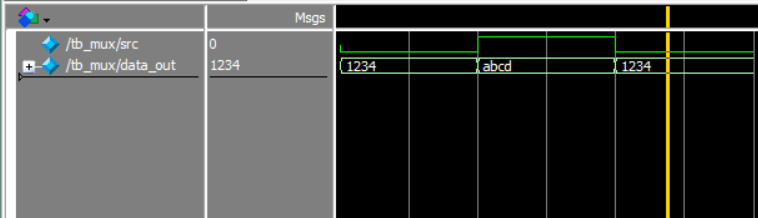
Tb\_regfiles



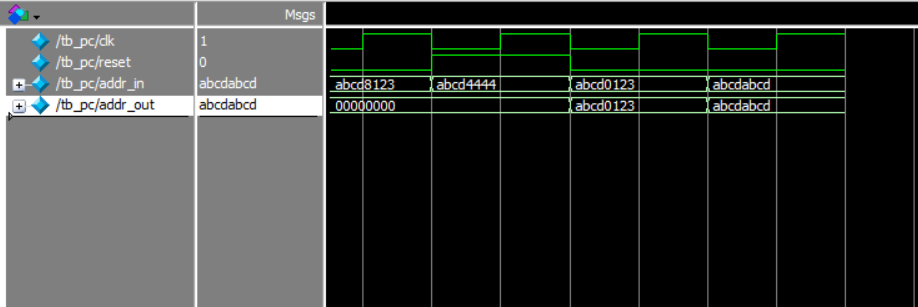
Tb\_sign\_ex



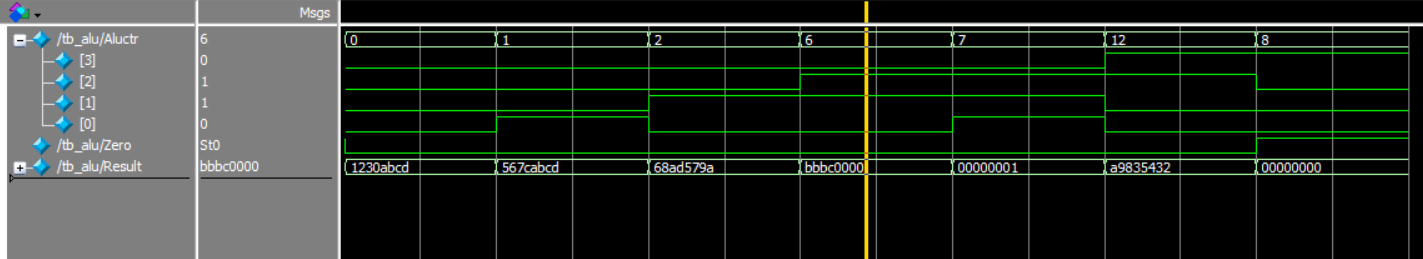
Tb\_mux



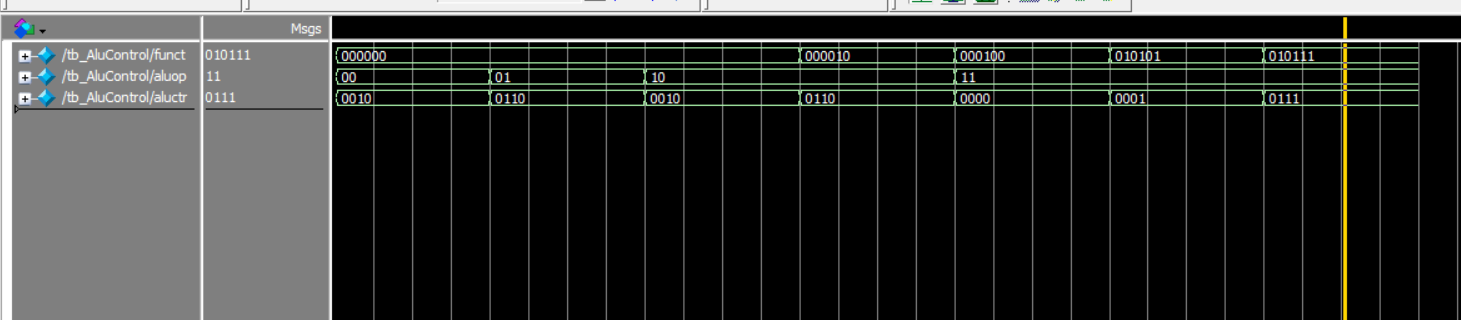
Tb\_pc\_reg



Tb\_alu



Tb\_alu\_control:



Tb\_address\_alu(for branch):

