

CERTIFICATE OF WORKSHOP COMPLETION

This certifies that

DHARAVATH RENU

has successfully completed the workshop in

VLSI Physical Design Using Synopsys Tools



Period of Workshop From:

<u>17-02-2025</u>

To:

21-02-2025

During this period she has gained expertise on the following:

- Synthesizing RTL with constraints
- · Design setup, floor planning and power planning
- Placement and CTS
- Routing and fixing DRC and LVS after routing
- Fixing timing issues after routing

25-02-2025

Issue Date

Signature

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