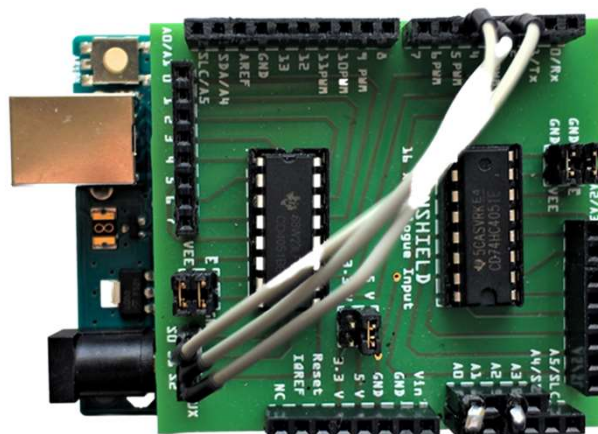


It looks simple, it is simple...

Sieht einfach aus, ist es auch...



more Inputs

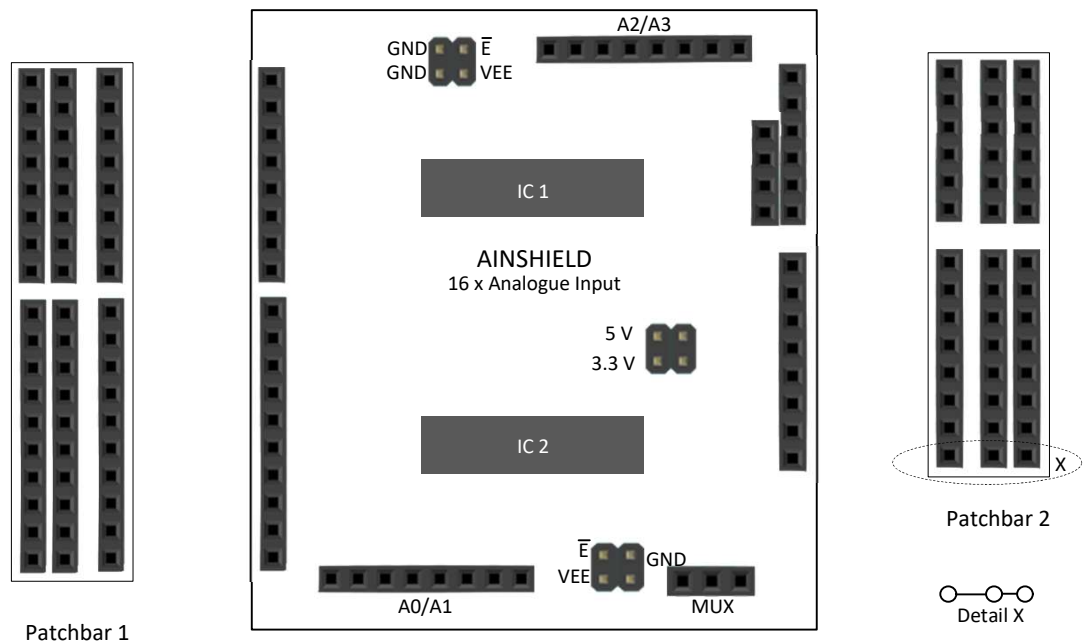


Fig.7 Example  
Stacking with Patchbar

Fig.7 Beispiel  
Stapeln mit Patchbar

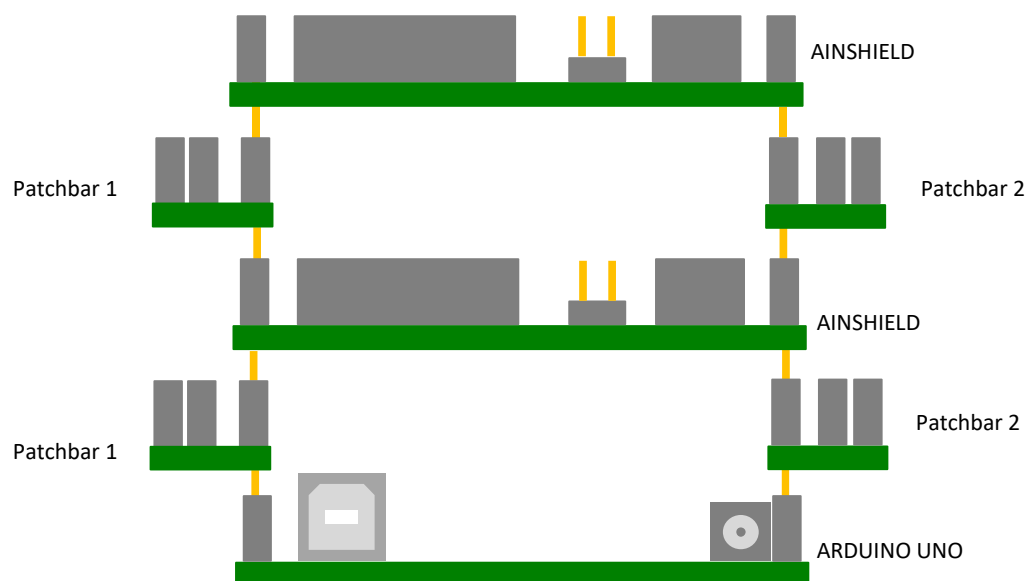
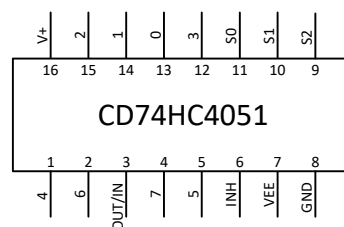
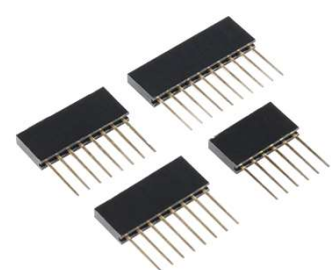
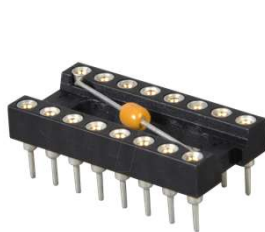
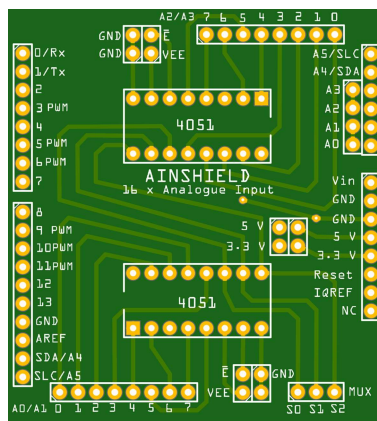


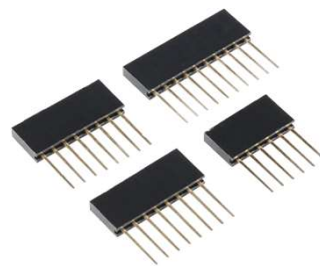
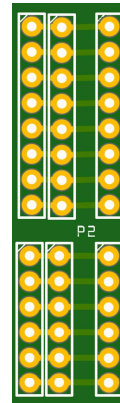
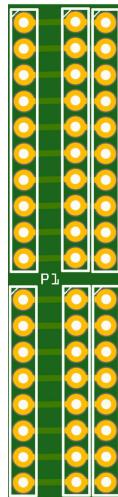
Fig.7

- 1 x PCB
- 2 x IC DIP SOCKET 16 LEGS (100pF inside)
- 6 x HEADER MALE 2 LEGS
- 2 x STACKING HEADER SET ARDUINO SHIELD
- 2 x CD74HC4051



2 x PCB

3 x STACKING HEADER SET ARDUINO SHIELD



IC1 and IC2 (HC4051) are 8 channel bidirectional multiplexers in high speed technology (Typ.  $t_{PD} = 15 \text{ ns}$ ). The analogue operating range is  $V_{SS} - V_{EE}$ . For operation as a digital multiplexer also, VEE is connected to ground (GND). High Signal on  $\bar{E}$  disable the switches.

IC1 und IC2 (HC4051) sind schnelle, achtkanalige bidirektionale Multiplexer (Typ.  $t_{PD} = 15 \text{ ns}$ ). Der Messbereich ist  $V_{SS} - V_{EE}$ . Zur Nutzung von binären Signalen wird VEE auf Masse verdrahtet (GND). Mit High Signal auf  $\bar{E}$  kann die Messung verhindert werden.

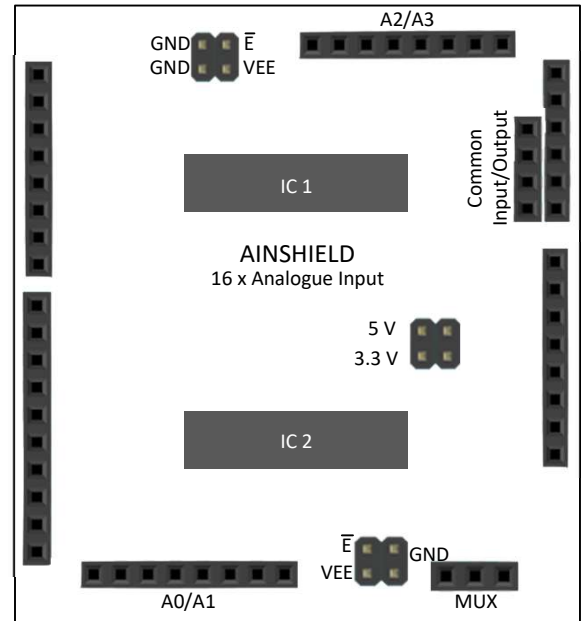


Fig.1

Fig.2 shows simplified how it works.

Die Arbeitsweise ist vereinfacht in Fig.2 dargestellt.

MUX (Selector)			
S <sub>2</sub>	S <sub>1</sub>	S <sub>0</sub>	Input Output
0	0	0	0
0	0	1	1
0	1	0	2
0	1	1	3
1	0	0	4
1	0	1	5
1	1	0	6
1	1	1	7

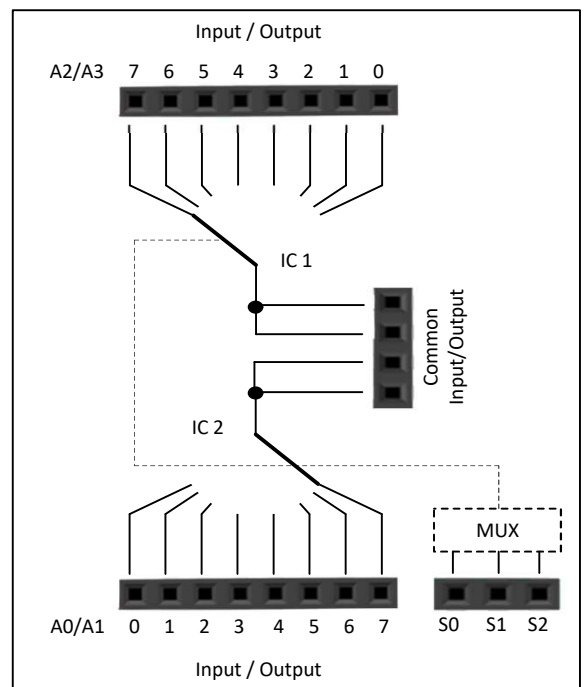


Fig.2

Fig.3 shows the input network.

Fig.3 zeigt die Eingangsbeschaltung.

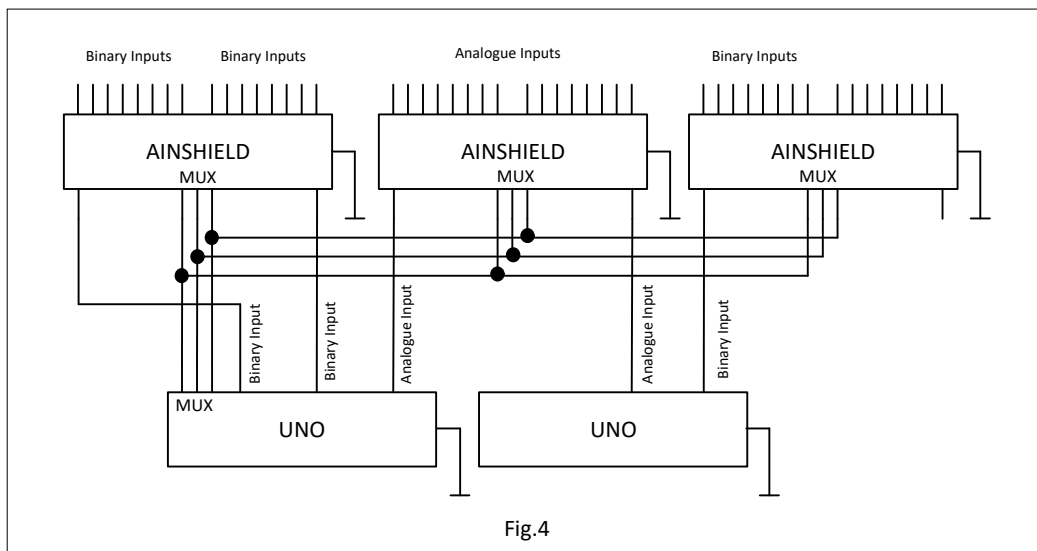
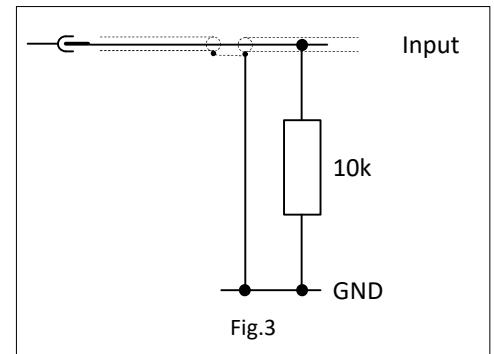


Fig.4 shows an example for interconnection between Arduinos and AINSHIELDS.

Fig.4 zeigt ein Beispiel wie Arduinos und AINSHIELDS miteinander verbunden werden können.