## Classwork

## **CACHE MEMORY**

Most PCs are held back not by the speed of their main processor, but by the time it takes to move data in and out of memory. One of the most important techniques for getting around this

- 5 bottleneck is the memory cache.
  - The idea is to use a small number of very fast memory chips as a buffer or cache between main memory and the processor. Whenever the processor needs to read data it looks in this cache
- area first. If it finds the data in the cache then this counts as a 'cache hit' and the processor need not go through the more laborious process of reading data from the main memory. Only if the data is not in the cache does it need to access main
- 15 memory, but in the process it copies whatever it finds into the cache so that it is there ready for the next time it is needed. The whole process is controlled by a group of logic circuits called the cache controller.
- 20 One of the cache controller's main jobs is to look after 'cache coherency' which means ensuring that any changes written to main memory are reflected within the cache and vice versa. There are several techniques for achieving this, the most obvious

- 25 being for the processor to write directly to both the cache and main memory at the same time. This is known as a 'write-through' cache and is the safest solution, but also the slowest.
- The main alternative is the 'write-back' cache
  which allows the processor to write changes only
  to the cache and not to main memory. Cache
  entries that have changed are flagged as 'dirty',
  telling the cache controller to write their contents
  back to main memory before using the space to
- 35 cache new data. A write-back cache speeds up the write process, but does require a more intelligent cache controller.
  - Most cache controllers move a 'line' of data rather than just a single item each time they need to
- 40 transfer data between main memory and the cache. This tends to improve the chance of a cache hit as most programs spend their time stepping through instructions stored sequentially in memory, rather than jumping about from one
- 45 area to another. The amount of data transferred each time is known as the 'line size'.

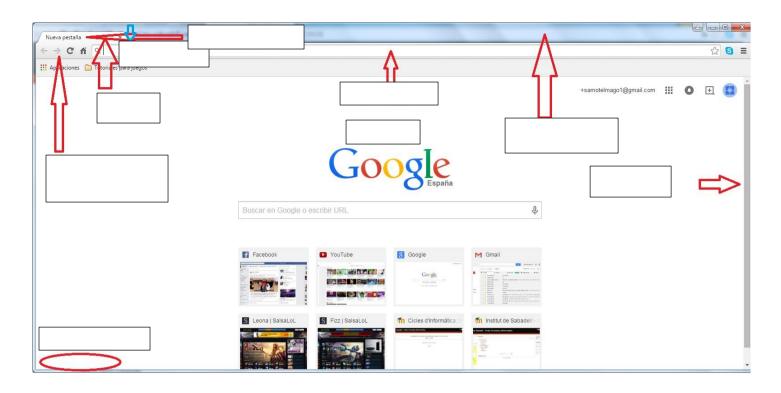
Word	Translation	Example in Spanish	Example in English

•	Find synonyms in the tex	t for these words				
1.	Essential (line 4)		6. Enable (line 30)			
2.	Quick (line 6)		7. Need (36)			
3.	Chief (line 7)		8. Information (45)	8. Information (45)		
4.	Enter (line 14)					
5.	Evident (line 24)					

## PRESENT PERFECT WITH FOR AND SINCE

period of t We use SII the start o  2005 20  2005	ime. NCE where the per 1006 2:	we are talking about an we are talking about iod.  2007 2008  London FOR three year London SINCE 2005	> yesterday > two months > a week > June > Wednesday > six hours > last month > five days > 1992
for or sin	has I	<i>ived</i> (live) in Lo	Perfect (remember to use the 3 <sup>rd</sup> column of the verbs) Then, choose ondon for / since five years.  (know) Tom for / since he moved to London.
			(be) a teacher for / since twelve years.
			(live) in Paris for / since 1998.
4)	You	doesn't played	(not/ play) the piano for / since several weeks.
5)	1	don't saw	(not/ see) Emma for / since last week.
6)	They _	studied	(study) English for / since three months.
7)	Sarah _	doesn't spoke	(not/speak) to Eva for / since five days.
8)	We	were there	(be) there / since three hours.
9)	Не	looked	(look) for his keys for / since a long time.
Nrite exam	ples us	sing the present	perfect tense

2.



BACK AND FORWARD BUTTONS - TITLE BAR - STATUS BAR - TOOLBAR ICONS - ADDRESS BAR

BROWSER - SCROLL BARS - TAB BUTTON - DISPLAY WINDOW

SOFTWARE, BACKUP, SERVER, BOOT, BROWSE, CACHE, CODE, CRASH, CURSOR, DASHBOARD, DATABASE, DELETE, DESKTOP, FIRMWARE, FOLDER, FREEWARE, INSTALL, VIRUS 1. SCRAH 2. HRDBDSOAA 3. OOTB Ordená para 4. OWSREB formar las 5. WMERIARF palabras de la caja y buscá su 6. FEAREWER traducción si es 7. CSORRU necesario. 8. FEAOWSTR 9. OFLRED 10. TEDSKPO 11. LDTEEE 12. SUIRV 13. TLLISAN 14. RRESVE 15. ACHEC 16. ECDO 17. DBSTAAAE 18. ABPCUK