

## 1. Description

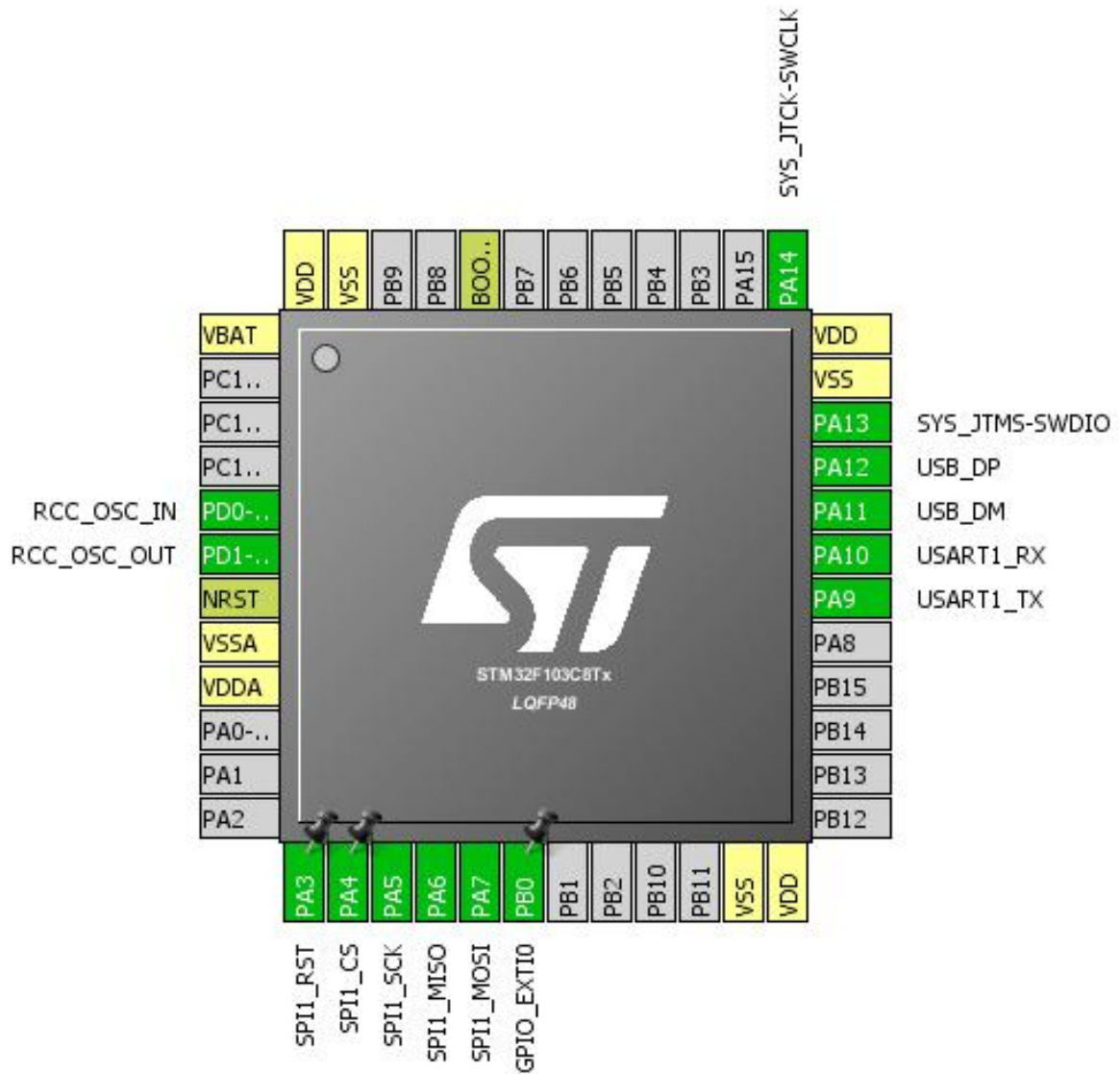
### 1.1. Project

Project Name	stm32f103_usb_host
Board Name	custom
Generated with:	STM32CubeMX 4.26.1
Date	06/30/2019

### 1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103C8Tx
MCU Package	LQFP48
MCU Pin number	48

## 2. Pinout Configuration

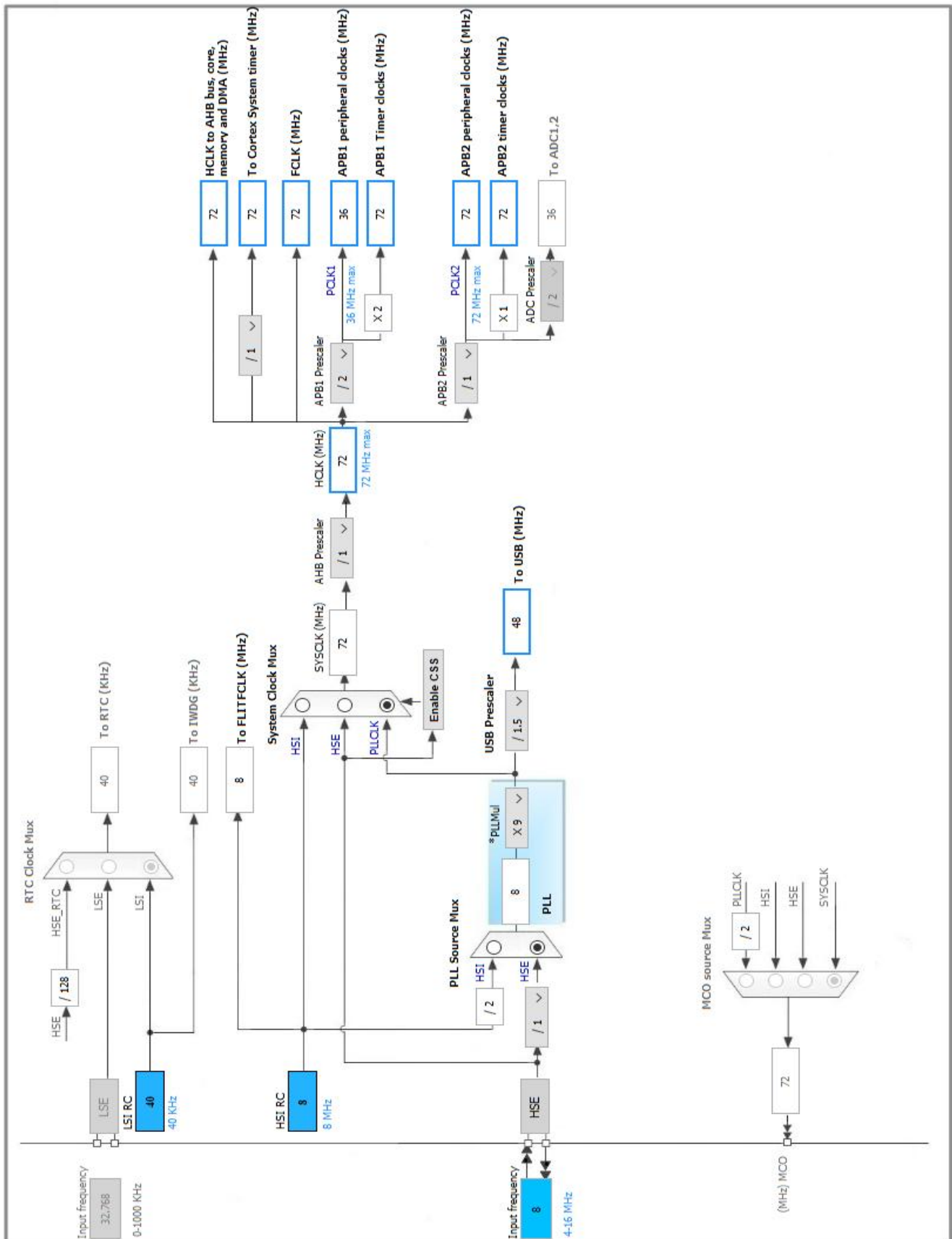


### 3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
5	PD0-OSC_IN	I/O	RCC_OSC_IN	
6	PD1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
13	PA3 *	I/O	GPIO_Output	SPI1_RST
14	PA4 *	I/O	GPIO_Output	SPI1_CS
15	PA5	I/O	SPI1_SCK	
16	PA6	I/O	SPI1_MISO	
17	PA7	I/O	SPI1_MOSI	
18	PB0	I/O	GPIO_EXTI0	
23	VSS	Power		
24	VDD	Power		
30	PA9	I/O	USART1_TX	
31	PA10	I/O	USART1_RX	
32	PA11	I/O	USB_DM	
33	PA12	I/O	USB_DP	
34	PA13	I/O	SYS_JTMS-SWDIO	
35	VSS	Power		
36	VDD	Power		
37	PA14	I/O	SYS_JTCK-SWCLK	
44	BOOT0	Boot		
47	VSS	Power		
48	VDD	Power		

\* The pin is affected with an I/O function

## 4. Clock Tree Configuration



## 5. IPs and Middleware Configuration

### 5.1. RCC

#### High Speed Clock (HSE): Crystal/Ceramic Resonator

##### 5.1.1. Parameter Settings:

###### System Parameters:

VDD voltage (V)	3.3
Prefetch Buffer	Enabled
Flash Latency(WS)	2 WS (3 CPU cycle)

###### RCC Parameters:

HSI Calibration Value	16
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

### 5.2. SPI1

#### Mode: Full-Duplex Master

##### 5.2.1. Parameter Settings:

###### Basic Parameters:

Frame Format	Motorola
Data Size	8 Bits
First Bit	MSB First

###### Clock Parameters:

Prescaler (for Baud Rate)	<b>4 *</b>
Baud Rate	<b>18.0 MBits/s *</b>
Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

###### Advanced Parameters:

CRC Calculation	Disabled
NSS Signal Type	Software

### 5.3. SYS

#### Debug: Serial Wire

#### Timebase Source: SysTick

## 5.4. USART1

**Mode: Asynchronous**

### 5.4.1. Parameter Settings:

#### Basic Parameters:

Baud Rate	115200
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

#### Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples

## 5.5. USB

**mode: Device (FS)**

### 5.5.1. Parameter Settings:

#### Basic Parameters:

Speed	Full Speed 12MBit/s
Endpoint 0 Max Packet size	8 Bytes

#### Power Parameters:

Low Power	Disabled
Link Power Management	Disabled
Battery Charging	Disabled

## 5.6. USB\_DEVICE

**Class For FS IP: Mass Storage Class**

### 5.6.1. Parameter Settings:

#### Basic Parameters:

USBD_MAX_NUM_INTERFACES (Maximum number of supported interfaces)	1
USBD_MAX_NUM_CONFIGURATION (Maximum number of supported configuration)	1
USBD_MAX_STR_DESC_SIZ (Maximum size for the string descriptors)	512
USBD_SUPPORT_USER_STRING (Enable user string descriptor)	Disabled
USBD_SELF_POWERED (Enabled self power)	Enabled

USBD\_DEBUG\_LEVEL (USBD Debug Level)

0: No debug message

**Class Parameters:**

MSC\_MEDIA\_PACKET (Media I/O buffer Size)

512

## 5.6.2. Device Descriptor:

**Device Descriptor:**

VID (Vendor Identifier)

1155

LANGID\_STRING (Language Identifier)

English(United States)

MANUFACTURER\_STRING (Manufacturer Identifier)

STMicroelectronics

**Device Descriptor FS:**

PID (Product Identifier)

22314

PRODUCT\_STRING (Product Identifier)

STM32 Mass Storage

SERIALNUMBER\_STRING (Serial number)

00000000001A

CONFIGURATION\_STRING (Configuration Identifier)

MSC Config

INTERFACE\_STRING (Interface Identifier)

MSC Interface

\* User modified value

## 6. System Configuration

### 6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	PD0-OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PD1-OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SPI1	PA5	SPI1_SCK	Alternate Function Push Pull	n/a	<b>High *</b>	
	PA6	SPI1_MISO	Input mode	No pull-up and no pull-down	<b>n/a</b>	
	PA7	SPI1_MOSI	Alternate Function Push Pull	n/a	<b>High *</b>	
SYS	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	
USART1	PA9	USART1_TX	Alternate Function Push Pull	n/a	<b>High *</b>	
	PA10	USART1_RX	Input mode	No pull-up and no pull-down	<b>n/a</b>	
USB	PA11	USB_DM	n/a	n/a	n/a	
	PA12	USB_DP	n/a	n/a	n/a	
GPIO	PA3	GPIO_Output	Output Push Pull	<b>Pull-up *</b>	Low	SPI1_RST
	PA4	GPIO_Output	Output Push Pull	<b>Pull-up *</b>	Low	SPI1_CS
	PB0	GPIO_EXTI0	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	

### 6.2. DMA configuration

nothing configured in DMA service



### 6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
EXTI line0 interrupt	true	0	0
USB low priority or CAN RX0 interrupts	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
USB high priority or CAN TX interrupts	unused		
SPI1 global interrupt	unused		
USART1 global interrupt	unused		

\* User modified value

## **7. Power Consumption Calculator report**

### 7.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
MCU	STM32F103C8Tx
Datasheet	13587_Rev17

### 7.2. Parameter Selection

Temperature	25
Vdd	3.3

## 8. Software Project

### 8.1. Project Settings

Name	Value
Project Name	stm32f103_usb_host
Project Folder	C:\Users\Drew\Source\Repos\stm32f103_usb_host
Toolchain / IDE	Other Toolchains (GPDSC)
Firmware Package Name and Version	STM32Cube FW_F1 V1.6.1

### 8.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No

## ***9. Software Pack Report***