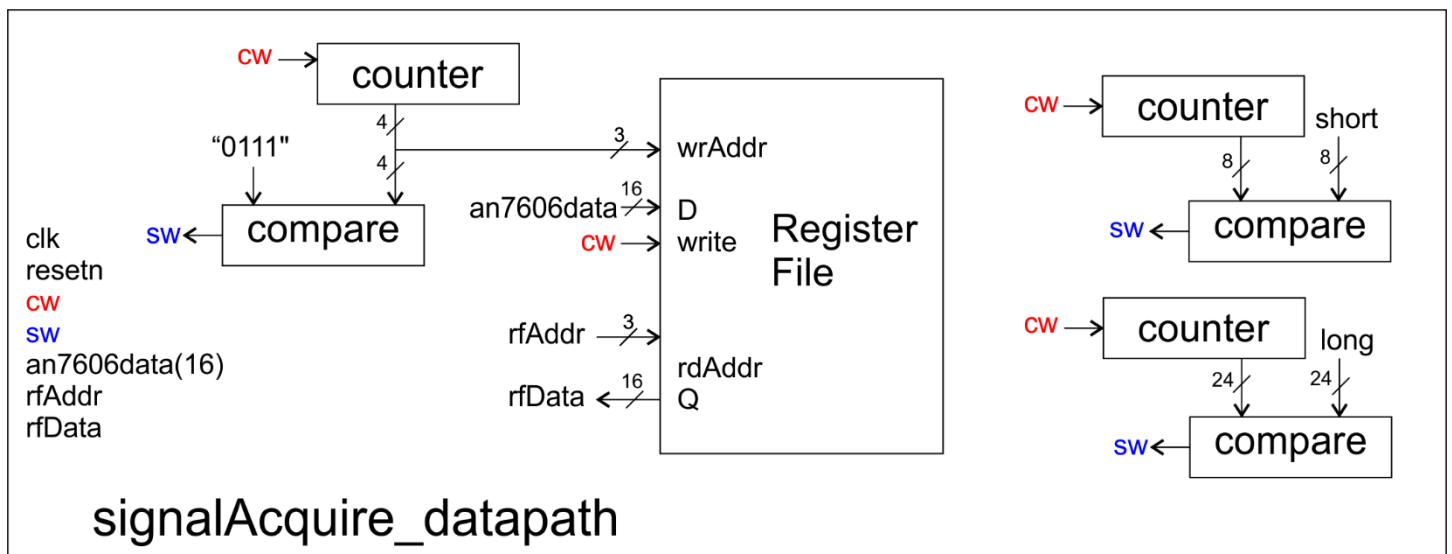
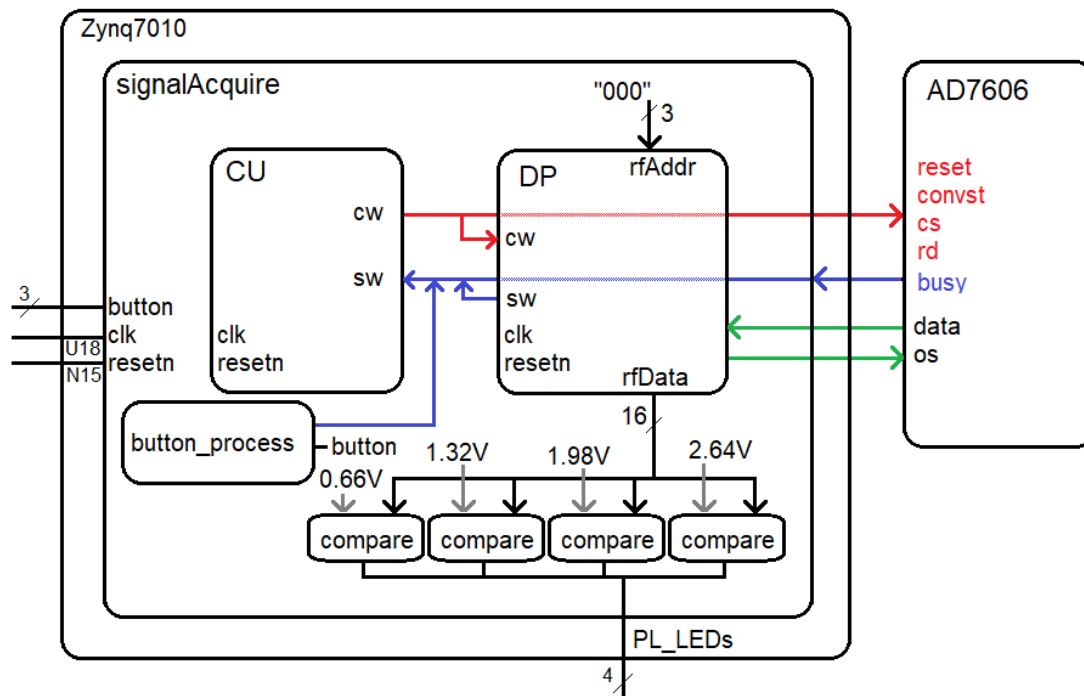
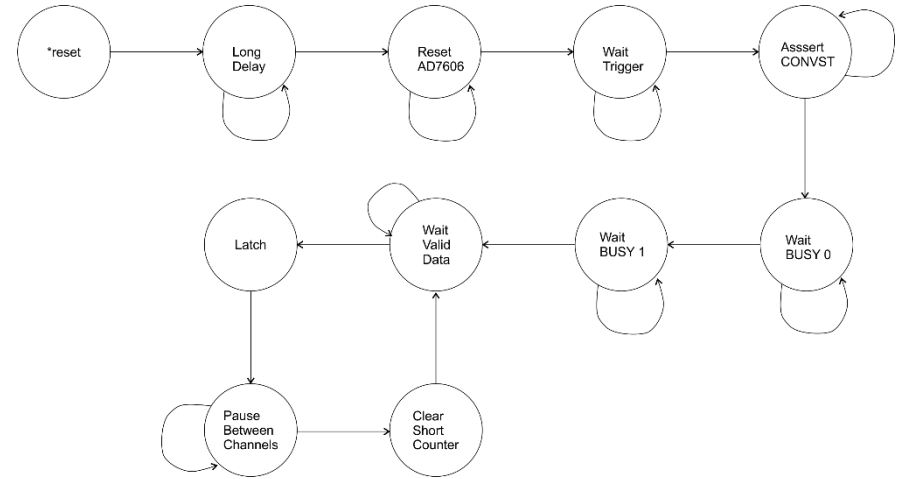
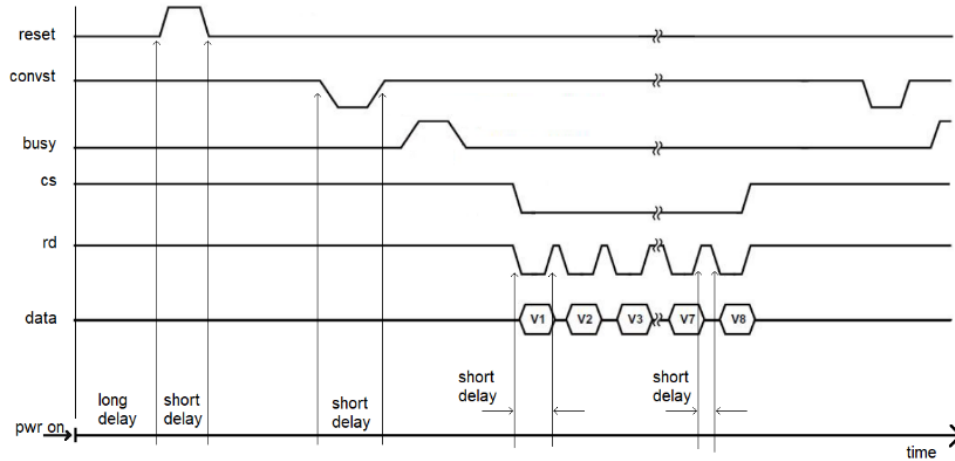




EENG 484 – Advanced Digital Design

Acquire





State	AN CONVST	AN RD	AN CS	AN RESET	Long Delay	Short Delay	RF Counter	RF Write	Hex
RESET									
Long Delay									
RESET AD7606									
Wait Trigger									
Assert CONV									
Wait Busy 0									
Wait Busy 1									
Wait Valid									
Latch									
Pause									
Clear									