

# Using the Stellaris® Ethernet Controller with Micro IP (uIP)

## Application Note



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## Introduction

Many of the devices in the Stellaris® family of microcontrollers include an Ethernet controller. The Ethernet controller consists of a fully integrated media access controller (MAC) and a network physical (PHY) interface device. The controller conforms to IEEE 802.3 specifications and fully supports 10BASE-T and 100BASE-TX standards.

The Micro IP (uIP) stack is an open-source implementation of the TCP/IP stack developed specifically for embedded microcontrollers. For embedded systems, uIP makes it possible to connect the system to a local intranet or the Internet. The uIP stack has been ported to the Stellaris® family of microcontrollers. This application note gives an overview of the uIP TCP/IP stack followed by a discussion of uIP application examples provided for Stellaris evaluation boards with Ethernet controllers.

## uIP TCP/IP Stack

The uIP stack was developed by Adam Dunkels of the Networked Embedded Systems group at the Swedish Institute of Computer Science. uIP, written in the C programming language, is an open-source TCP/IP stack developed for embedded microcontrollers by minimizing the required code and data memory footprints. Typical code size is on the order of a few kilobytes while RAM usage can be as low as a few hundred bytes. A basic overview of the uIP stack is provided below. For more information, visit the uIP web page at <http://www.sics.se/~adam/uip>.

## Features

The uIP stack implementation is designed to have only the minimal set of features needed for a TCP/IP stack. The TCP/IP stack is a suite of protocols including the following which are supported by uIP:

- Transmission Control Protocol (TCP)
- User Datagram Protocol (UDP)
- Internet Protocol (IP)
- Internet Control Message Protocol (ICMP)
- Address Resolution Protocol (ARP)

The uIP stack can handle multiple concurrent TCP and UDP connections. The maximum number of connections allowed is configurable at compile time.

## Implementation

The uIP stack is mainly concerned with the TCP/IP protocols. The upper layer protocols are considered to be part of the application. The lower level protocols are handled by the networking hardware's device driver.

The TCP/IP suite of protocols are defined in a layered fashion where each layer has a specific function. The implementations of the protocols in uIP are not layered but instead are tightly coupled in order to save code space.

The requirements for the protocols in the TCP/IP stack are specified in a number of Request For Comments (RFC) documents published by the Internet Engineering Task Force (IETF). Each of the protocols is defined in one or more RFC documents. RFC1122 incorporates, amends, corrects, and supplements these RFC documents to define the requirements for the communication layers for internet hosts.

The RFC1122 requirements can be divided into two categories: those that affect host-to-host communication and those that affect communication between the TCP/IP stack and the application. All of the requirements for host-to-host communication are implemented in uIP. However, some of the requirements for stack-to-application communication, such as the soft error reporting mechanism and dynamically configurable type-of-service bits for TCP connections, were not implemented in order to reduce code size.

## Application Program Interface (API)

uIP provides two application program interfaces (API) which define how the application program interacts with the TCP/IP stack. The two APIs include protocols and a raw API. The protocols API is similar to a Berkeley (BSD) socket API without the full multi-threading overhead. Protocols only work with TCP connections. The raw API is an event-based driven interface where the application is invoked in response to certain events. The raw API is more of a low-level interface compared to protocols but uses less memory.

## uIP Application Examples

There are several uIP application examples available for the Stellaris LM3S6965 evaluation board. The example applications available include the example provided with the Stellaris driver library (DriverLib), a FreeRTOS demo, and the application examples provided with the uIP distribution package. These examples are supported by the following tool sets:

- Keil™ RealView® Microcontroller Development Kit
- IAR Embedded Workbench
- CodeSourcery Sourcery G++ for Stellaris EABI
- Code Red Technologies Red Suite
- Texas Instruments' Code Composer Studio™

## DriverLib Example

The Stellaris Peripheral Driver Library is a set of drivers for accessing the peripherals found on the Stellaris family of microcontrollers. The peripheral driver library is available in both source code and binary formats. The peripheral driver library is shipped as part of the Stellaris evaluation and development kits and is available as a free download from the <http://www.ti.com/stellaris> web site.

In addition to the peripheral driver library, the Stellaris® Firmware Development Package includes an example application using the uIP TCP/IP stack that is targeted for all Stellaris evaluation kits with Ethernet including the LM3S9B92, LM3S9B90, LM3S8962, and LM3S6965 evaluation boards. This example application demonstrates an HTTP server running on the uIP stack that serves up a web page, which displays a few lines of text and a counter that increments each time the page is sent

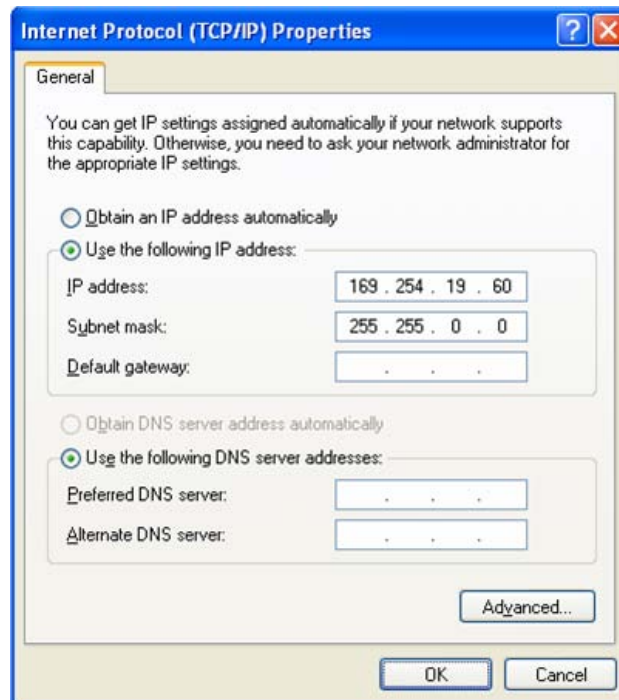
from the board. The example first configures the LM3S6965 microcontroller and then initializes the uIP TCP/IP stack and HTTP server. The IP address of the LM3S6965 evaluation board is displayed using the OLED display on the board. The main control loop looks for incoming packets, processes them using the uIP stack and HTTP server, and transmits packets back onto the network as required. The example also implements a TCP/IP periodic timer and transmits packets onto the network as required by uIP. The Ethernet controller device drivers provided within the peripheral driver library are used by this example. The source code and project files for the uIP example are provided in the StellarisWare® software in their respective StellarisWare\boards directories.

In this uIP example, the TCP/IP stack is configured so that it requires approximately 4.5 Kbytes of flash memory for code and approximately 2.5 Kbytes of SRAM for data and stack.

An Ethernet cable must be connected between a PC and the evaluation board. The integrated PHY on the LM3S9B92, LM3S9B90, LM3S8962, and LM3S6965 microcontrollers has an Auto-MDIX feature that allows the use of a straight-through or cross-over Ethernet cable. Use a web browser to view the web page served up by the microcontroller.

The default static IP address for the board is 169.254.19.63, but is configurable in the `enet_uip.c` file if a different IP address is desired. The PC must be configured to be on the same subnet as the board. In most cases, the PC detects the correct IP address and subnet settings automatically after several seconds. In some cases, the PC's IP address and subnet mask must be configured manually. To do this, disable the PC's wireless network connection and any other internet connections that could interfere with the network being created. Select the Internet Protocol (TCP/IP) connection within the Local Area Connection Properties and click on Properties. Next, manually configure the PC's IP address as 169.254.19.60 and subnet mask to 255.255.0.0, as shown in Figure 1.

After completing the above configuration and programming the microcontroller with the application, open a web browser and type the IP address into the address bar to view the web page being served up by the microcontroller. For example, if the default static IP address is used, type `http://169.254.19.63` in the address bar.

**Figure 1. TCP/IP Properties**

## FreeRTOS.org™ Embedded Ethernet Demo

FreeRTOS provides an embedded Ethernet demo using the uIP stack that supports the LM3S6965 evaluation board. The demo with source code can be downloaded from the LM3S6965 evaluation kit web page at [www.luminarymicro.com](http://www.luminarymicro.com).

The FreeRTOS demo creates a simple web server that serves up a few web pages using the LM3S6965 microcontroller. One of the web pages allows the user to write data to the OLED display and control the status LED on the LM3S6965 evaluation board. The other web pages display task, TCP, and network connection statistics. These web pages can be viewed by using a web browser.

Just as with the uIP example provided with StellarisWare®, an Ethernet cable should be connected between a PC and the LM3S6965 evaluation board. Either a straight-through or cross-over Ethernet cable can be used. By default, the IP address of the evaluation board is configured to be 172.25.218.9. The IP address is configurable at compile time by the constants `uipIP_ADDR0` through `uipIP_ADDR3` within the `uIP_Task.c` file.

If the PC does not detect the correct IP address and subnet mask settings, then they must be manually configured. To manually configure these settings, select the Internet Protocol (TCP/IP) connection within the Local Area Connection Properties and click on Properties. Assuming that the IP address for the board uses the default setting of 172.25.218.9, then the IP address for the PC can be configured as 172.25.218.10 and the subnet mask to 255.255.0.0. This configuration is the same as in Figure 1 except with a different IP address.

After completing the above configuration and programming the LM3S6965 microcontroller with the application, open a web browser and type the IP address into the address bar to view the web pages

being served up by the LM3S6965 microcontroller. For example, if the default static IP address is used, type <http://172.25.218.9> in the address bar.

Visit <http://www.freertos.org> for more information on FreeRTOS.

## Examples Provided with the uIP Package

The uIP TCP/IP stack package comes with a few example applications which can be used to run on top of the uIP stack. These example applications include dhcpc, hello-world, httpd, resolv, smtp, telnetd, webclient, and webserver. In addition, the uIP reference manual shows a few simple uIP applications. These examples as well as the uIP reference manual are installed with the DriverLib package for the Stellaris family of microcontrollers. For more information, visit the uIP webpage at <http://www.sics.se/~adam/uip>

## Conclusion

Many of the devices in the Stellaris family of microcontrollers include an Ethernet controller consisting of an integrated MAC and PHY. By using the open-source uIP stack which has been ported to the Stellaris family of microcontrollers, you can connect the system to a local intranet or the Internet. uIP application examples that support the Ethernet-enabled microcontrollers are provided and make it easy to get started writing network-enabled applications using the evaluation board.

## References

The following documents and source code are available for download at [www.ti.com/stellaris](http://www.ti.com/stellaris):

- *Stellaris® LM3S6965 microcontroller data sheet*
- *Stellaris® LM3S8962 microcontroller data sheet*
- *Stellaris® LM3S9B90 microcontroller data sheet*
- *Stellaris® LM3S9B92 microcontroller data sheet*
- Stellaris Peripheral Driver Library, order number SW-DRL
- *Stellaris Peripheral Driver Library User's Manual*, publication number SW-DRL-UG



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