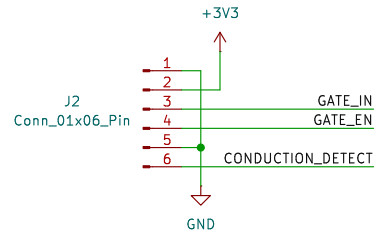
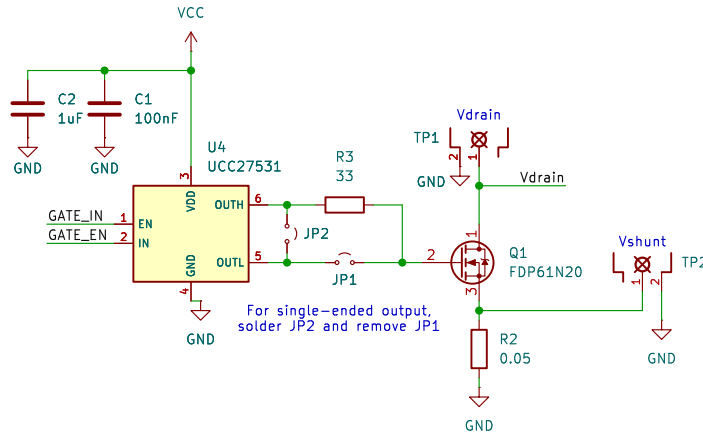


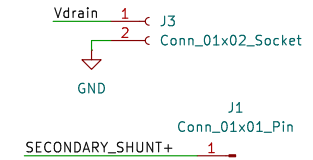
## TO FPGA



## DIGITAL GATE DRIVER

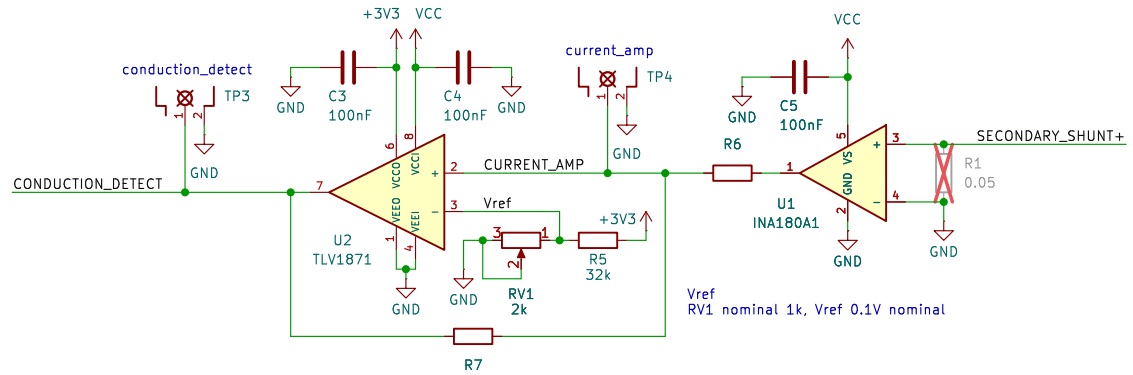
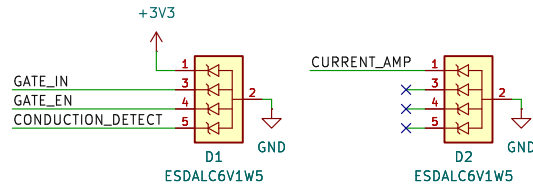


## TO FLYBACK CONVERTER



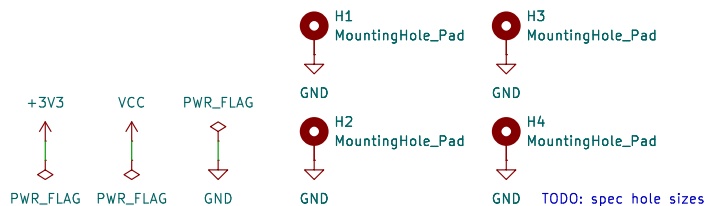
## SECONDARY COIL CONDUCTION DETECT

### 3V3 TVS



TODO: Spec R6 and R7

Decoupling capacitors: C1-C5  
place close to respective pins



Sheet: /  
File: digital-gate-driver.kicad\_sch

Title:

Size: A4

Date:

KiCad E.D.A. 8.0.9

Rev:

Id: 1/1