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AC\_Input.sch

AC Current Sensor  
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AC Output  
AC\_Output.sch

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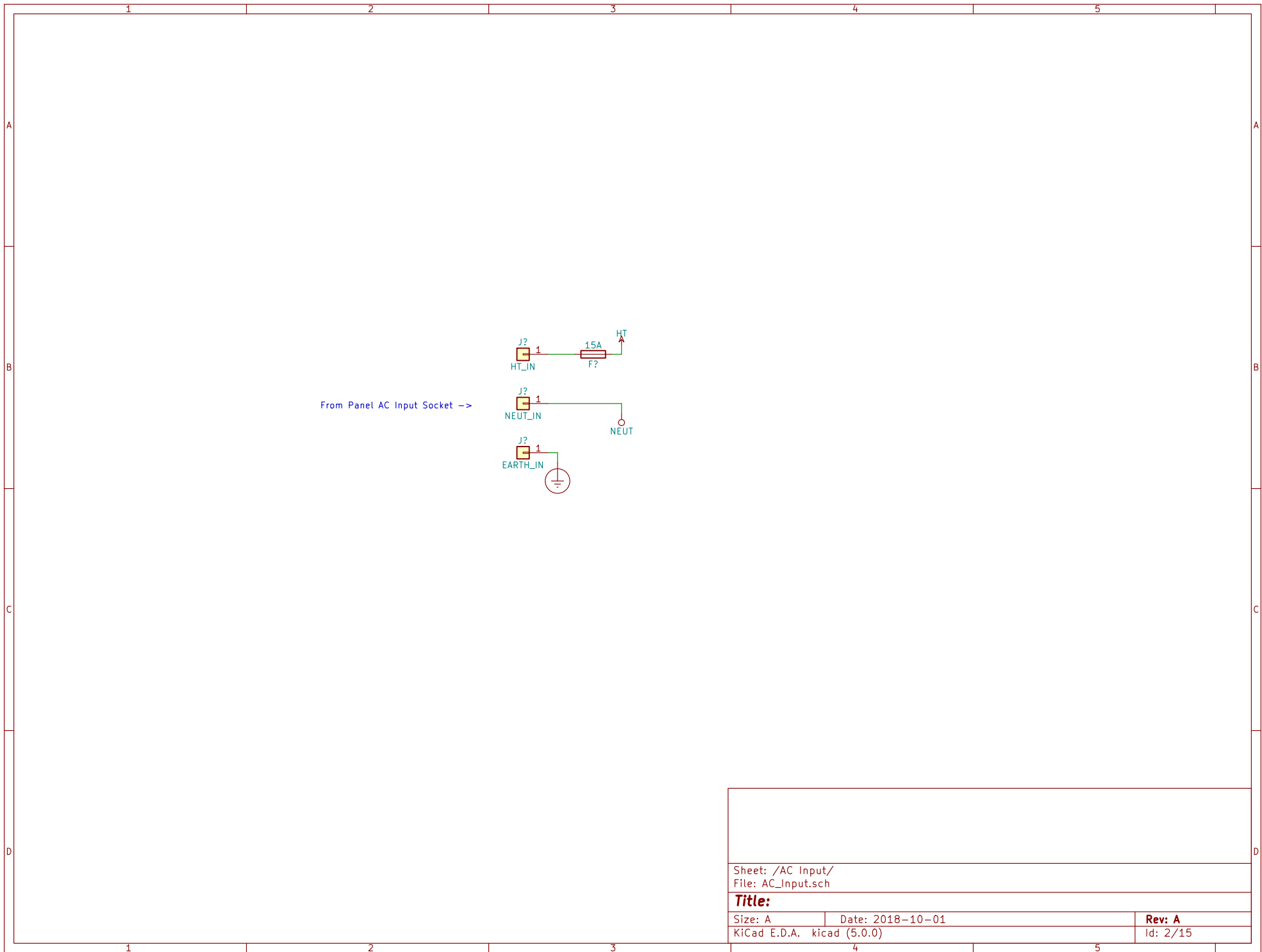
Output Switch  
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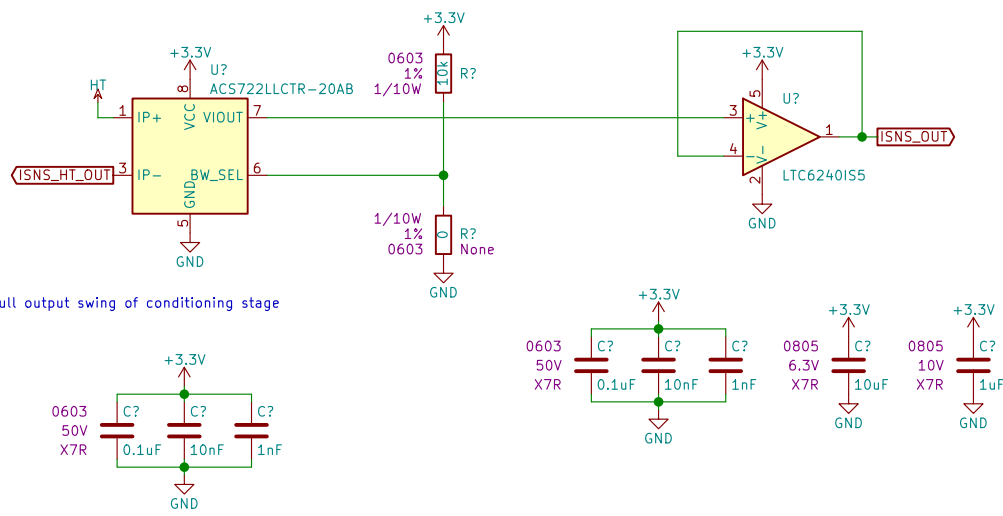
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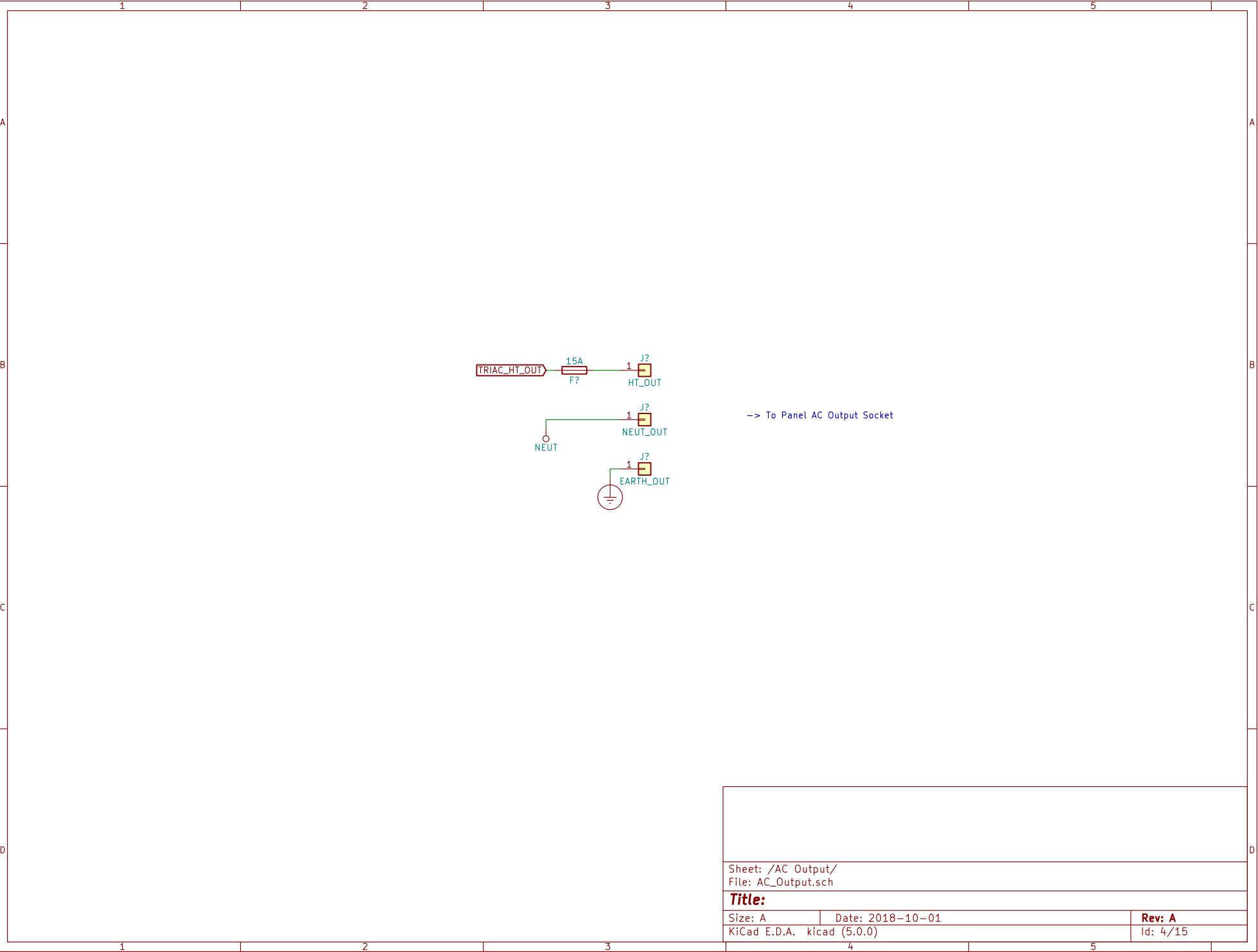
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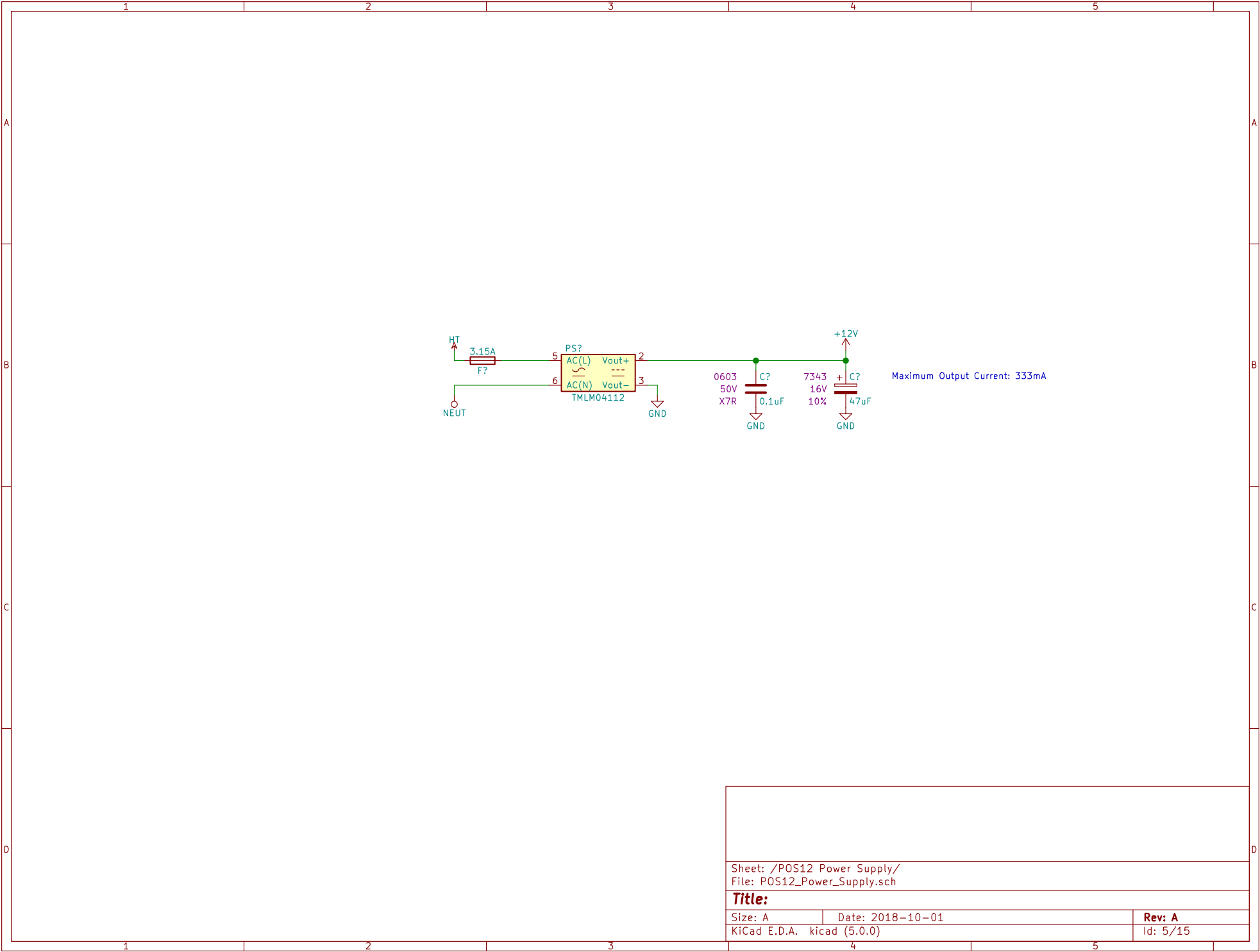
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Date: 2018-10-01

Rev: A

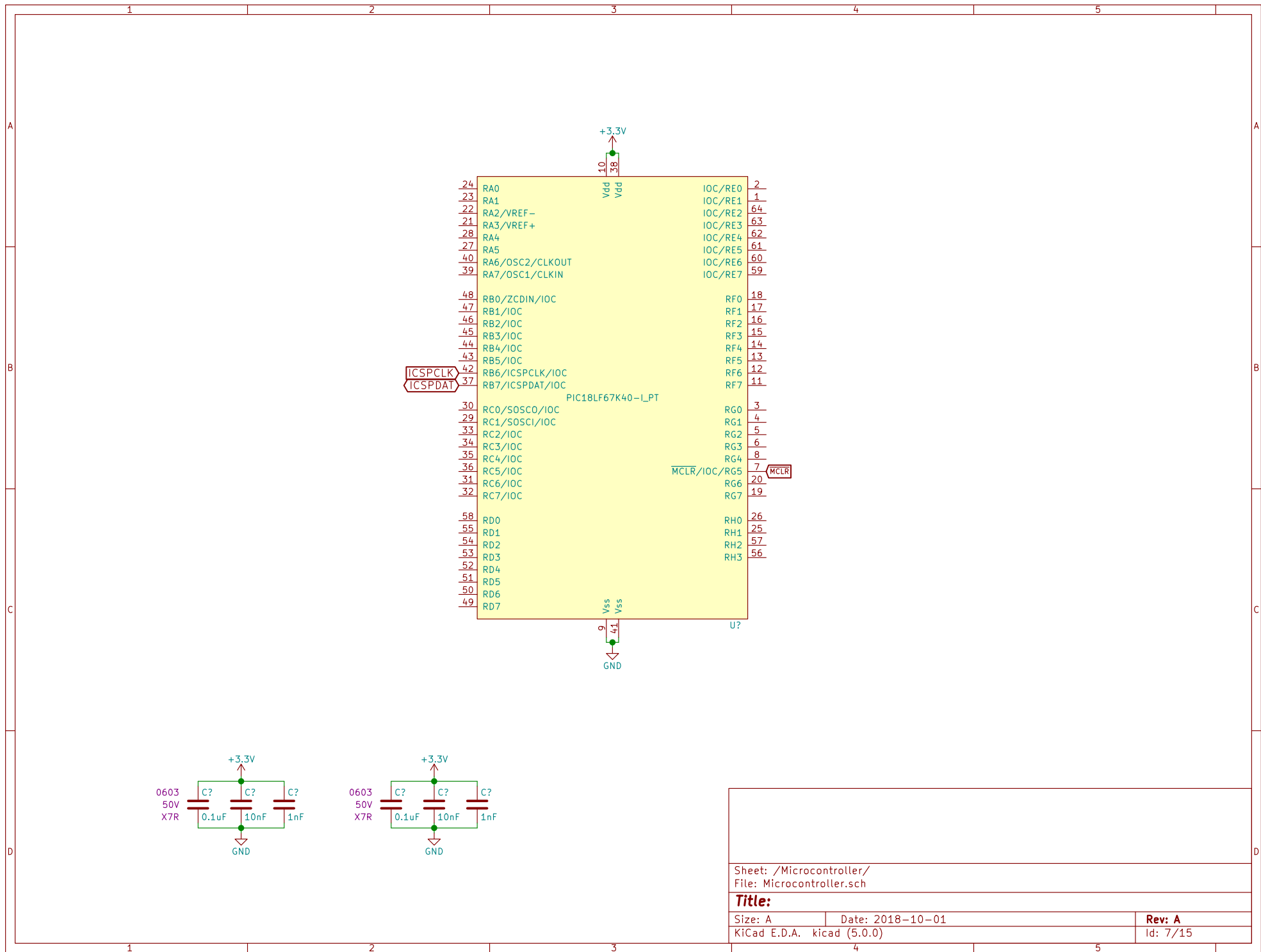
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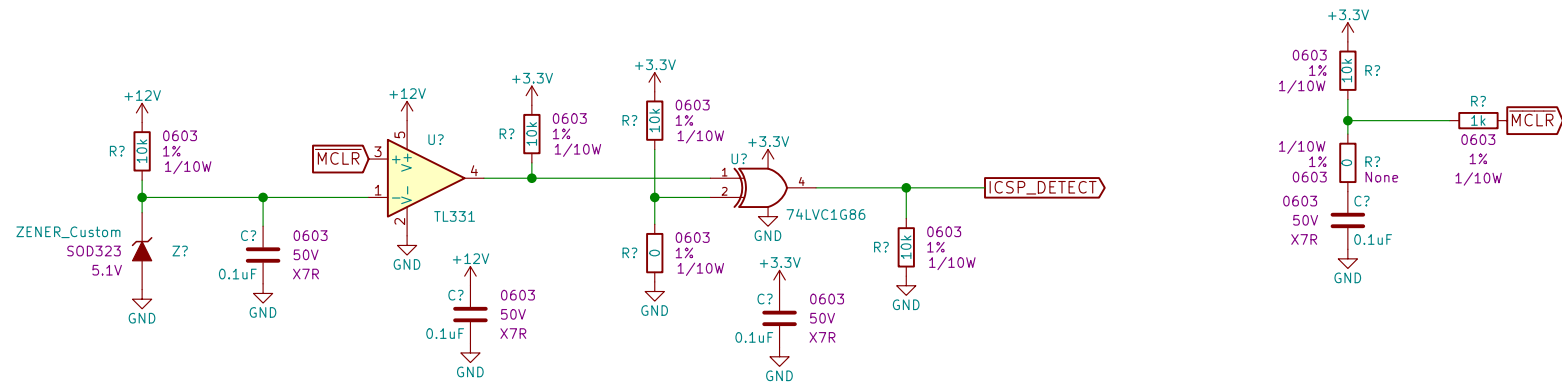
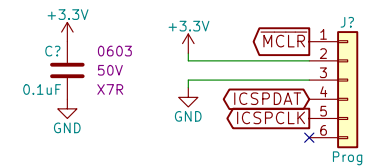


**Programming Header/ICSP Detection**

The schematic shows a circuit for detecting a programming header. It includes a TL331 op-amp configured as a voltage follower, a 74LVC1G86 NAND gate, and various passive components like resistors, capacitors, and a zener diode. The circuit is powered by a 3.3V supply and has a 6-pin programming header (J?) for ICSPDAT and ICSPCLK. The output of the NAND gate is labeled ICSP\_DETECT.

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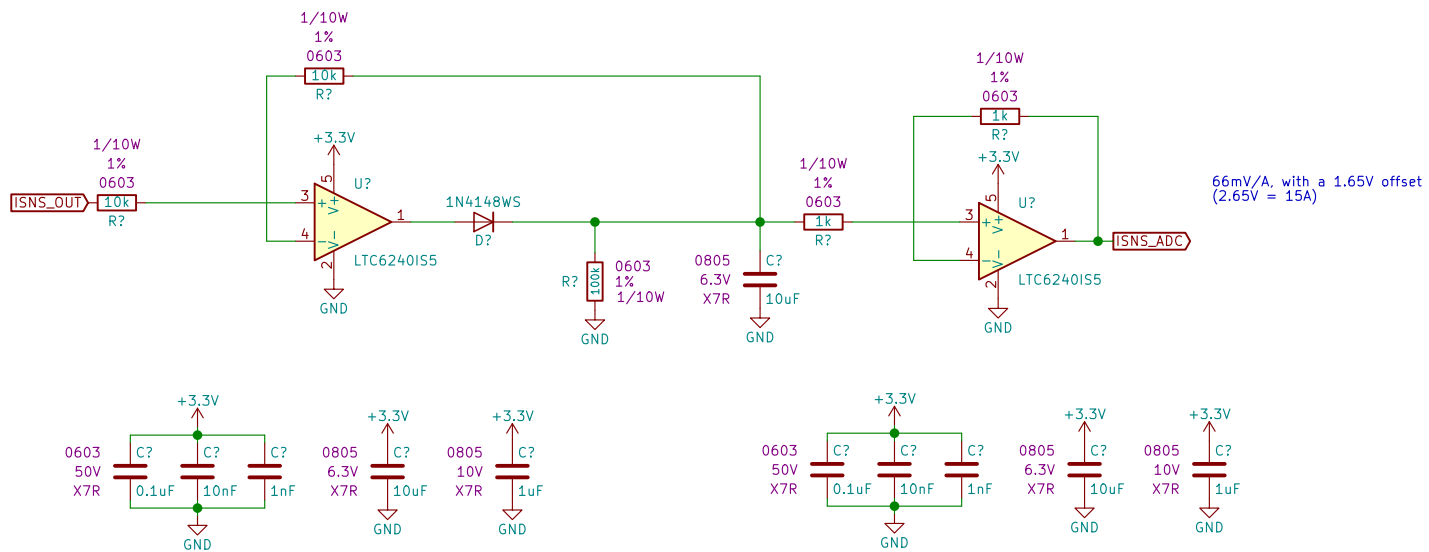
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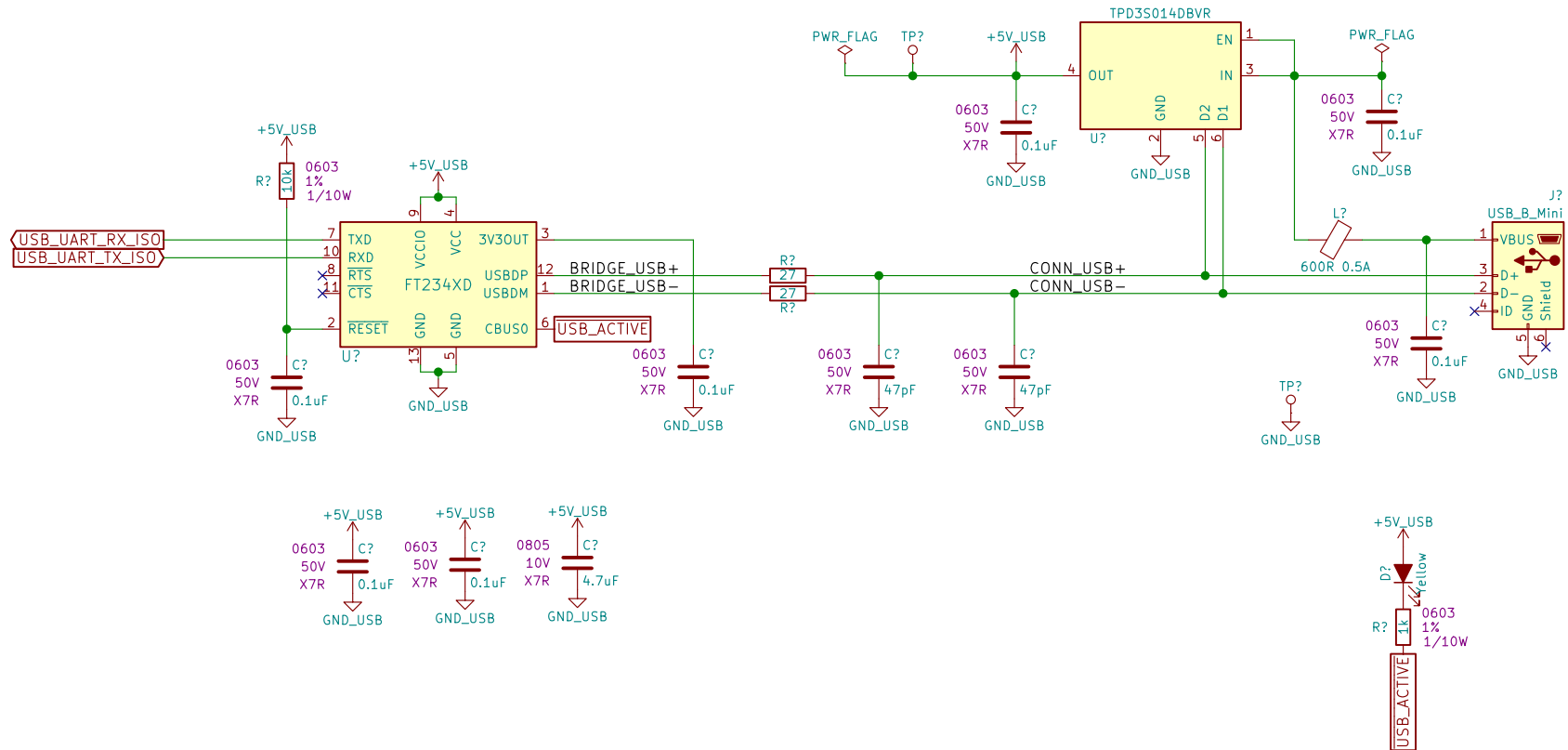
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# UART to USB Bridge



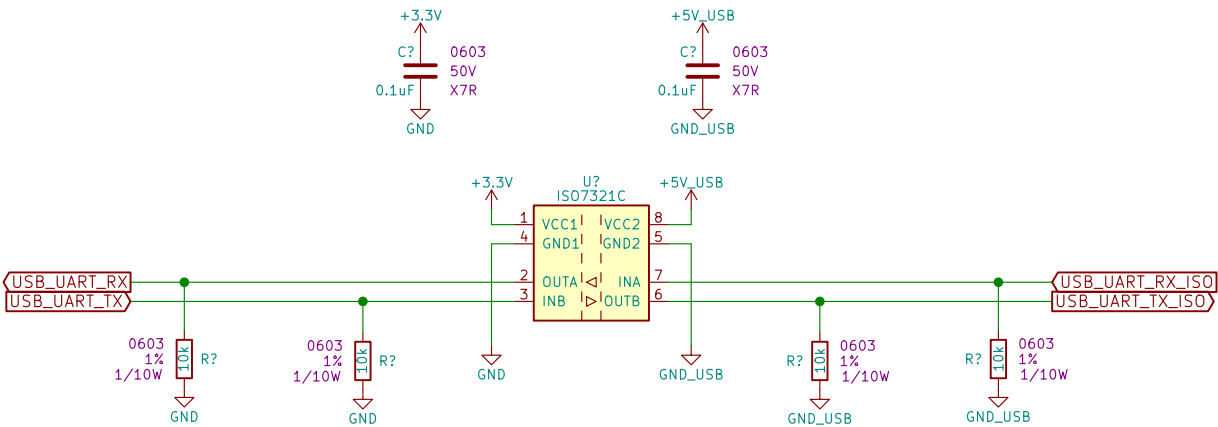
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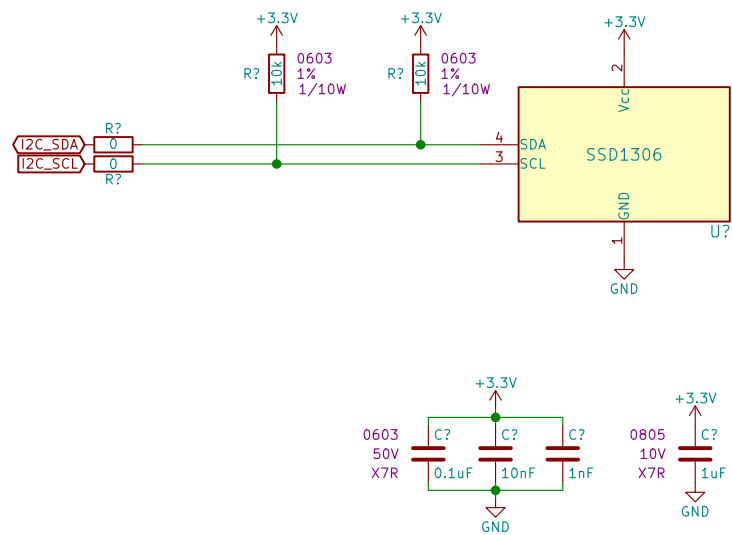
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USB UART Isolation



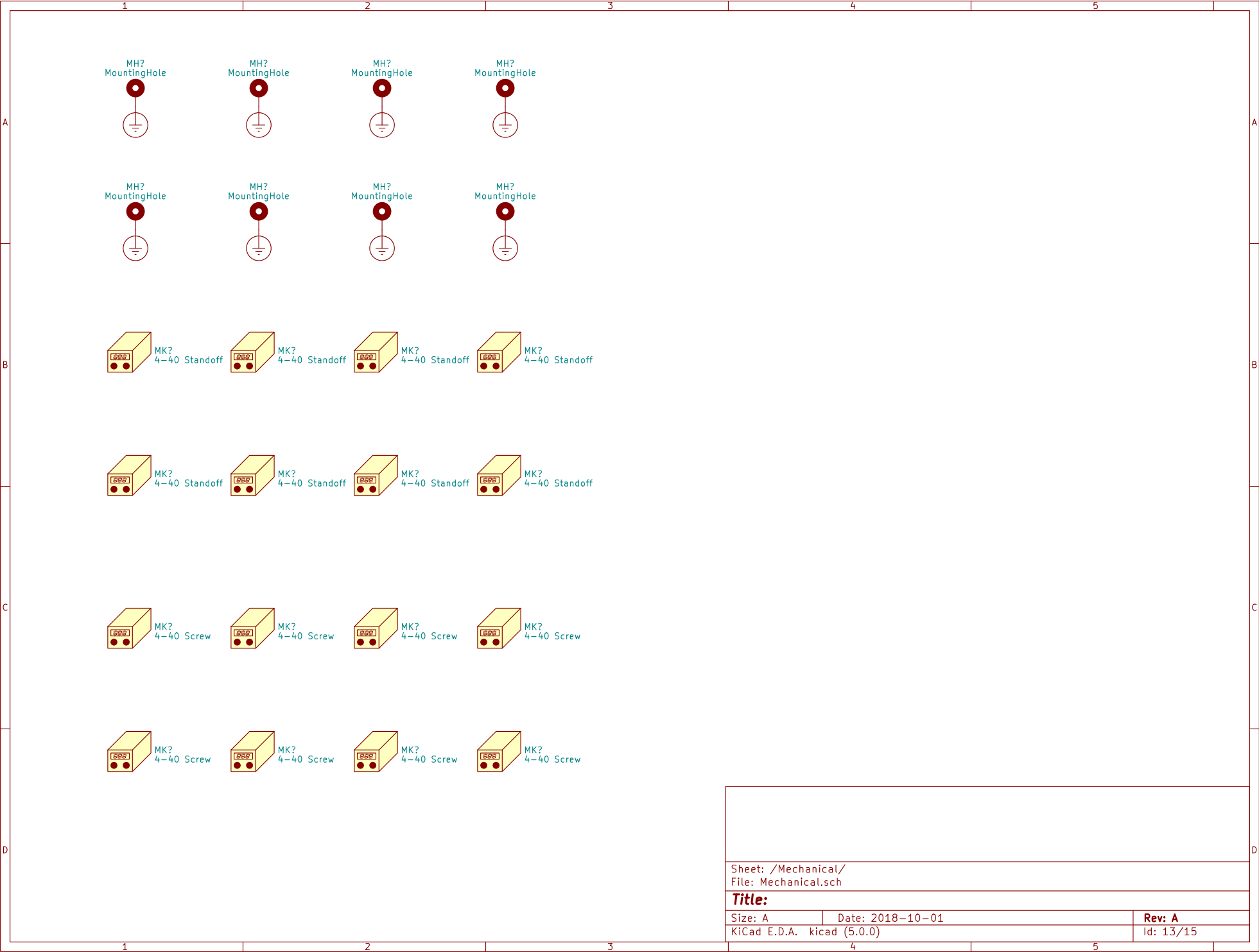


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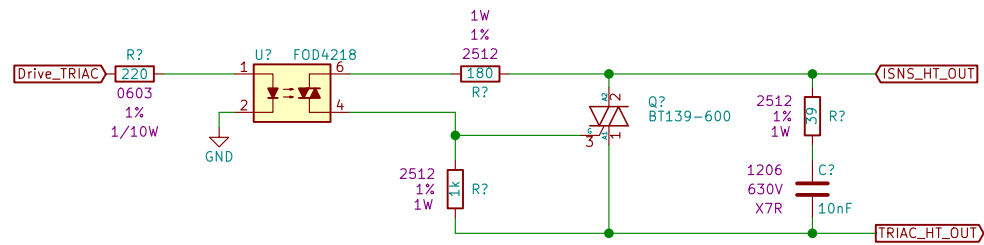
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