Table of Contents:

AC Power Meter/Dimmer

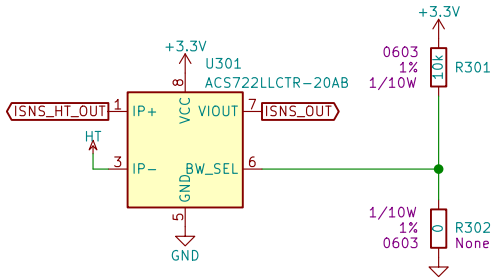
1. AC Input — AC_Input.sch
2. AC Current Sensor — AC_Current_Sensor.sch
3. AC Output — AC_Output.sch
4. +12V Power Supply — POS12_Power_Supply.sch
5. +3.3V Power Supply — POS3P3_Power_Supply.sch
6. Microcontroller — Microcontroller.sch
7. Microcontoller Programming — Microcontroller_Programming.sch
8. Sensor Signal Conditioning — Sensor_Signal_Conditioning.sch
9. USB UART Bridge — USB_UART_Bridge.sch
10. USB UART Isolation — USB_UART_Isolation.sch
11. OLED Display — OLED_Display.sch
12. Mechanical — Mechanical.sch
13. Zero Cross Detect — Zero_Cross_Detect.sch
14. Output Switch — Output_Switch.sch
15. Status LEDs — Status_LEDs.sch
16. Pushbuttons — Pushbuttons.sch
17. Firmware Notes — Firmware_Notes.sch

Sheet: /		
File: AC_Power_Meter.sch		
Title:		
Size: A	Date: 2018-10-01	**Rev: A**
KiCad E.D.A. kicad (5.0.0)		Id: 1/18

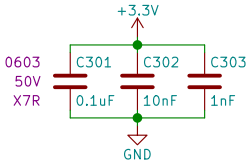
AC Input Connections

Sheet: /AC Input/ File: AC_Input.sch		
Title:		
Size: A	Date: 2018-10-01	**Rev: A**
KiCad E.D.A. kicad (5.0.0)		Id: 2/18

AC Current Sensor



10.7ARMS Max to support full output swing of conditioning stages



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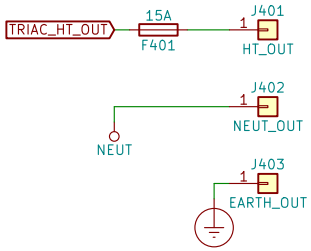
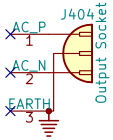
KiCad E.D.A. kicad (5.0.0)		Id: 3/18

AC Output Connections



-> To Panel AC Output Socket



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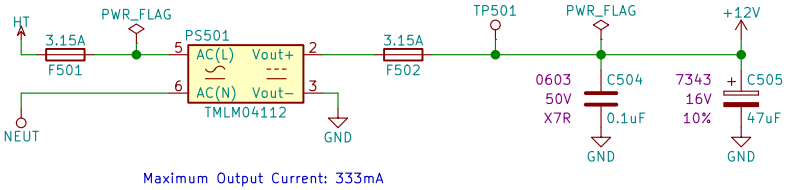
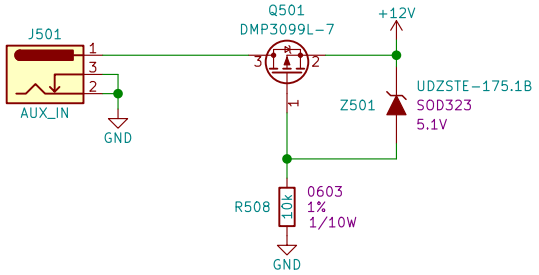
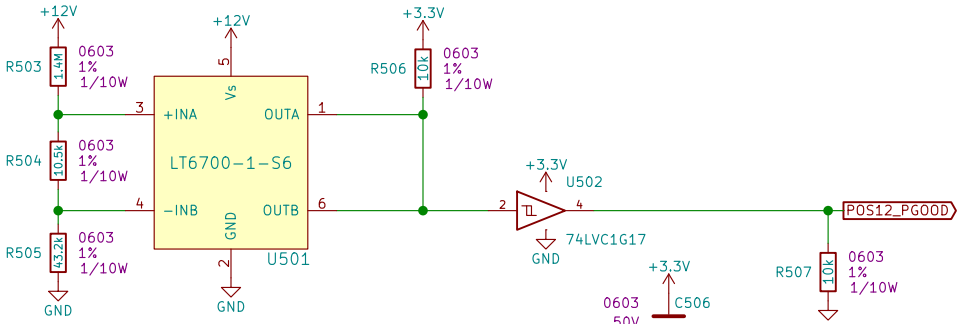
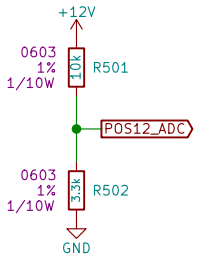
Size: A	Date: 2018-10-01	**Rev: A**
KiCad E.D.A. kicad (5.0.0)		Id: 4/18

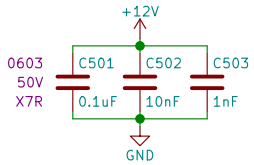
+12V 4W Isolated AC/DC Converter, +12V PGOOD Window Comparator











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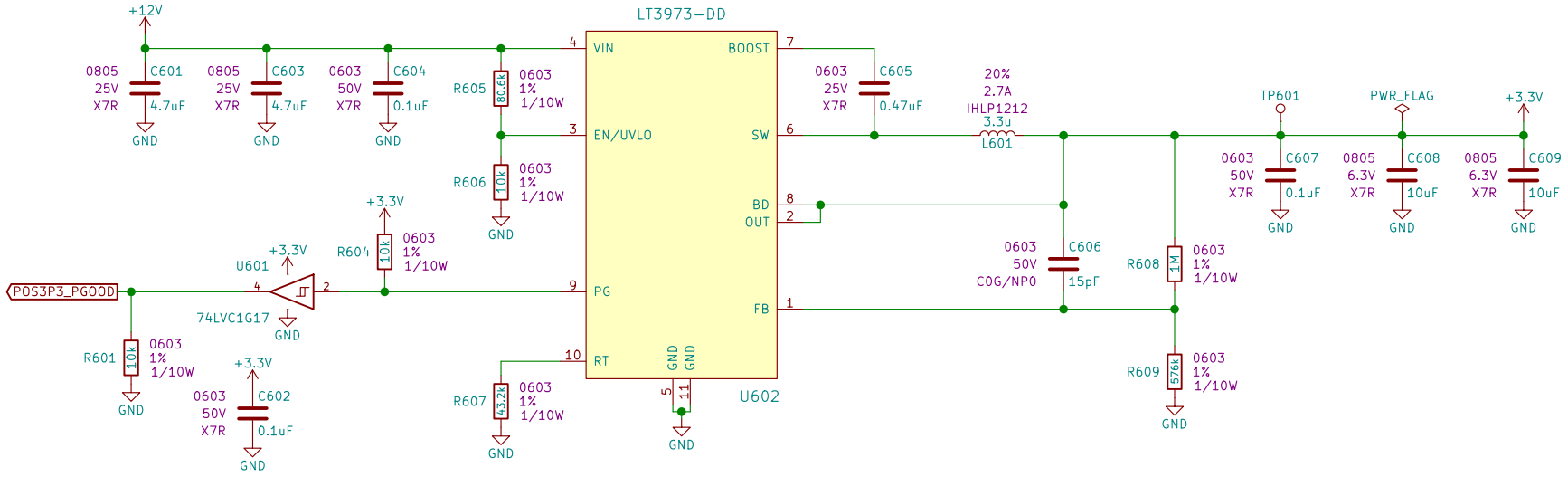
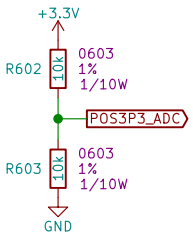
Size: A	Date: 2018-10-01	**Rev: A**
KiCad E.D.A. kicad (5.0.0)		Id: 5/18

+3.3V, 750mA, 2MHz Buck Converter





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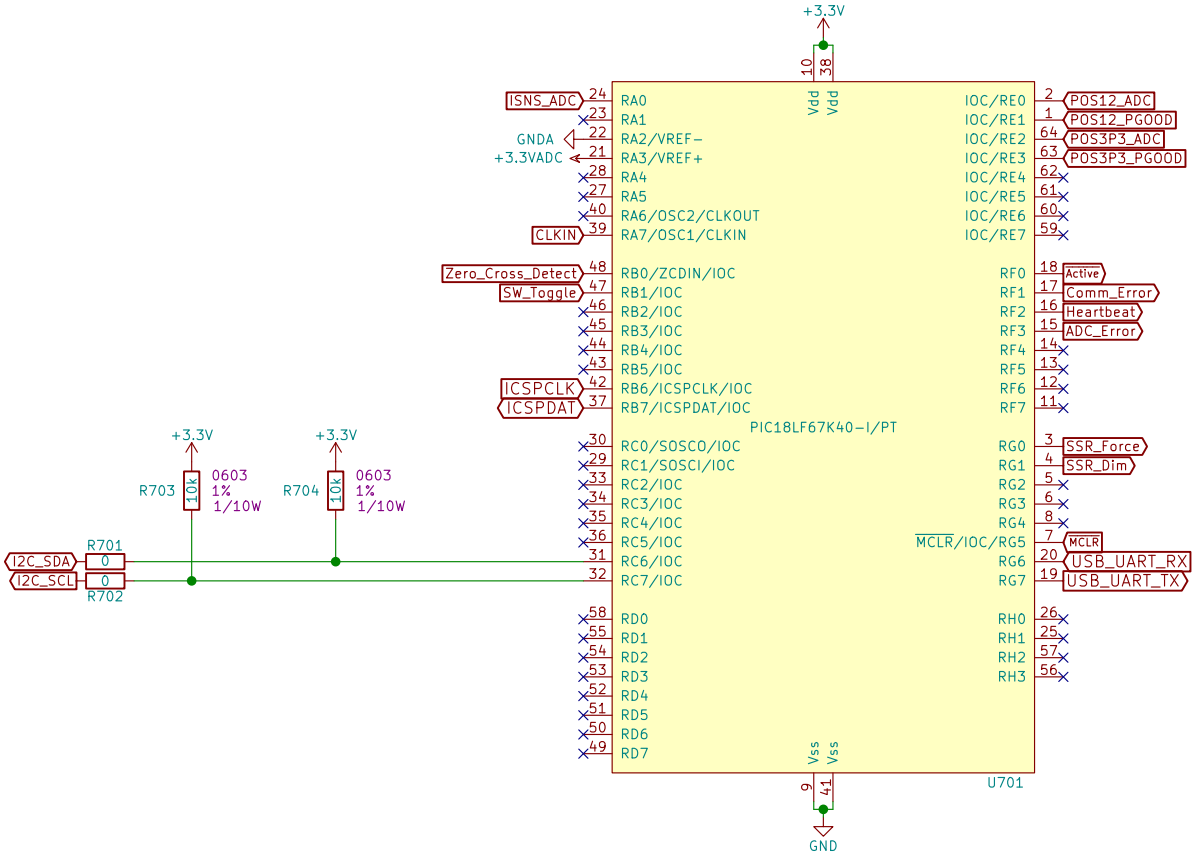
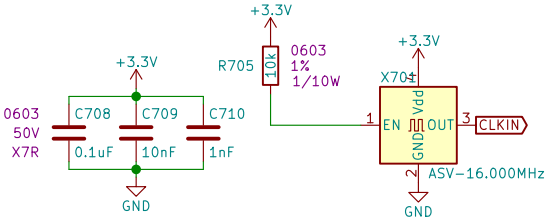
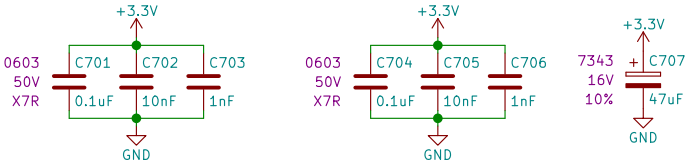
Size: A	Date: 2018-10-01
KiCad E.D.A. kicad (5.0.0)	Id: 6/18

8 bit High Performance Enhanced Flash PIC Microcontroller







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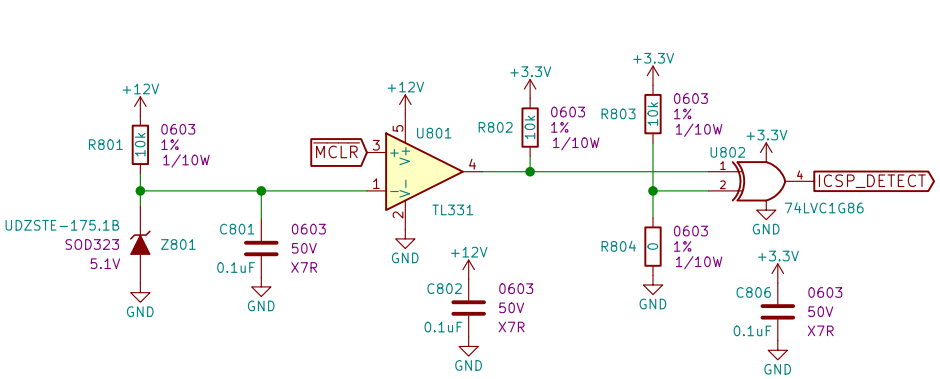
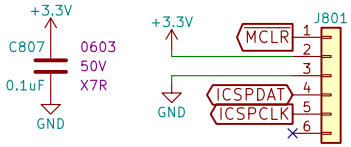
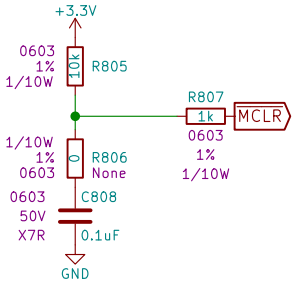
Title:

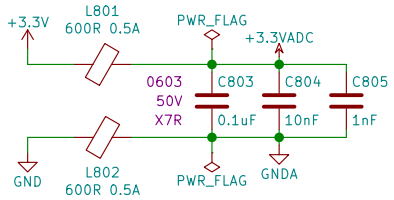
Size: A	Date: 2018-10-01	**Rev: A**
KiCad E.D.A. kicad (5.0.0)		Id: 7/18

Programming Header, $\overline{\text{MCLR}}$ Reset Filter, ICSP Detection









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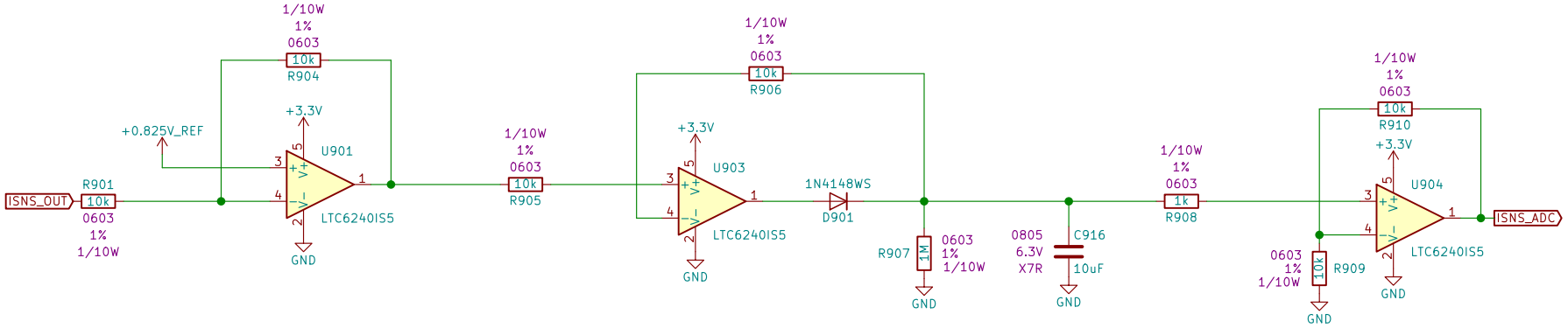
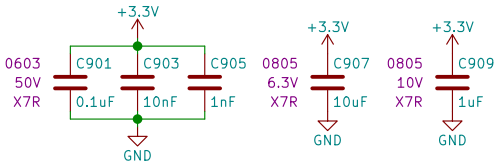
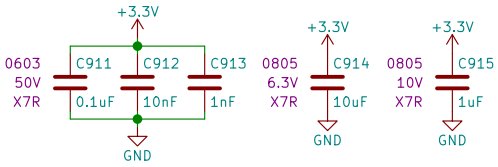
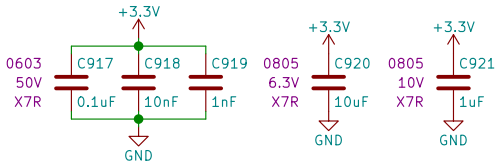
Title:		
Size: A	Date:	**Rev:**
KiCad E.D.A. kicad (5.0.0)		Id: 8/18

Offset Removal, Active Peak Detector, 2V/V Gain Stage

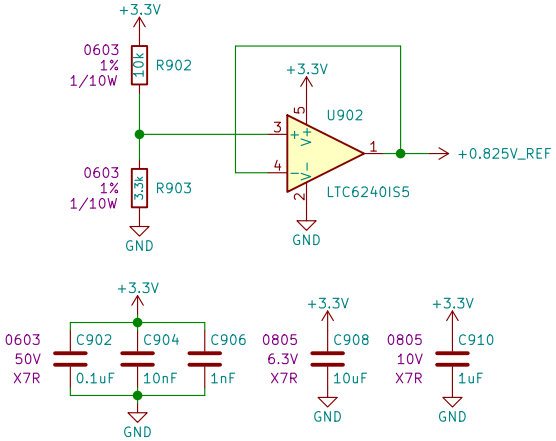








ISNS_ADC Scaling: 5.35ARMS/V



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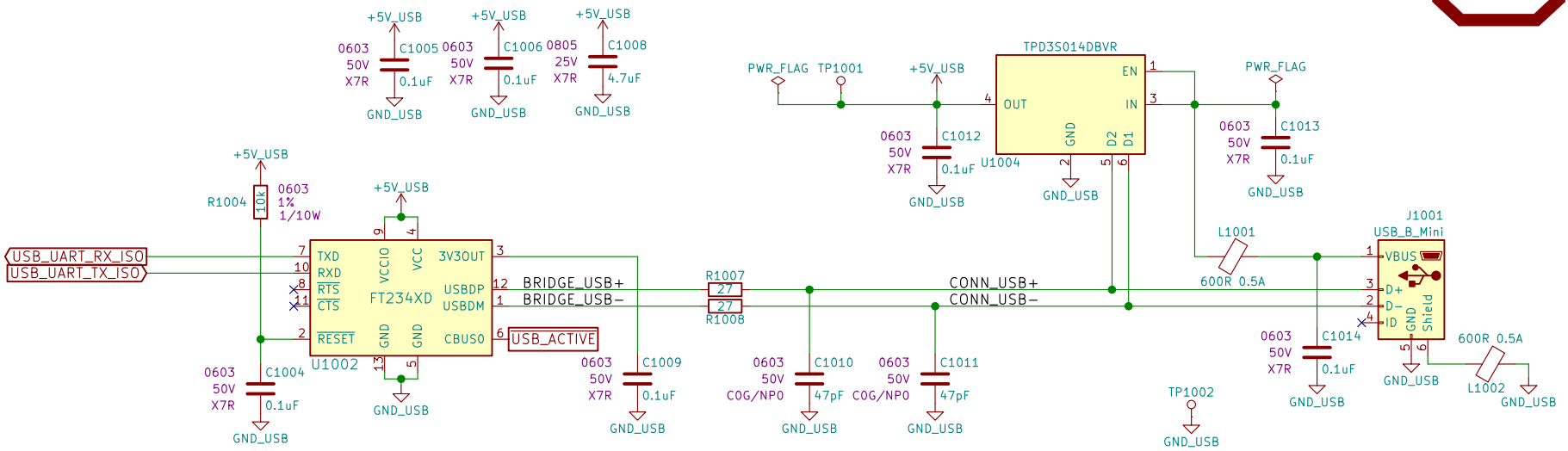
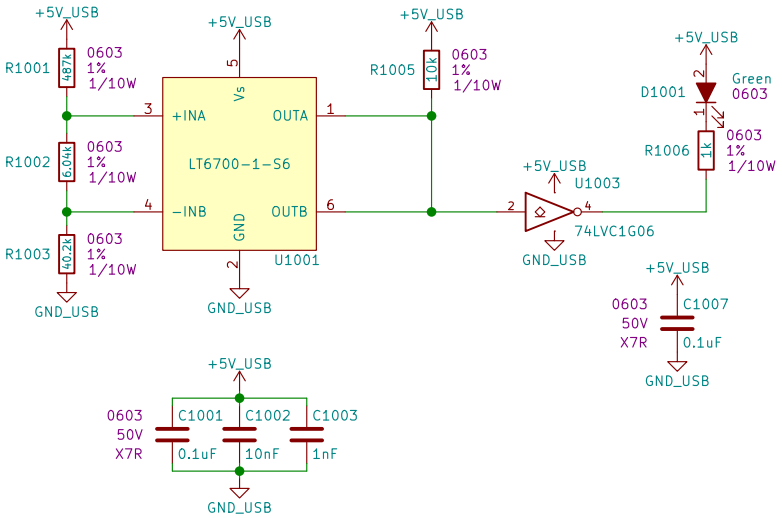
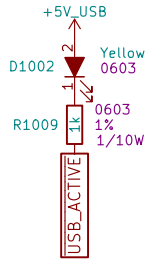
Size: A	Date: 2018-10-01	**Rev: A**
KiCad E.D.A. kicad (5.0.0)		Id: 9/18

UART to USB Bridge, +5V USB Window Comparator







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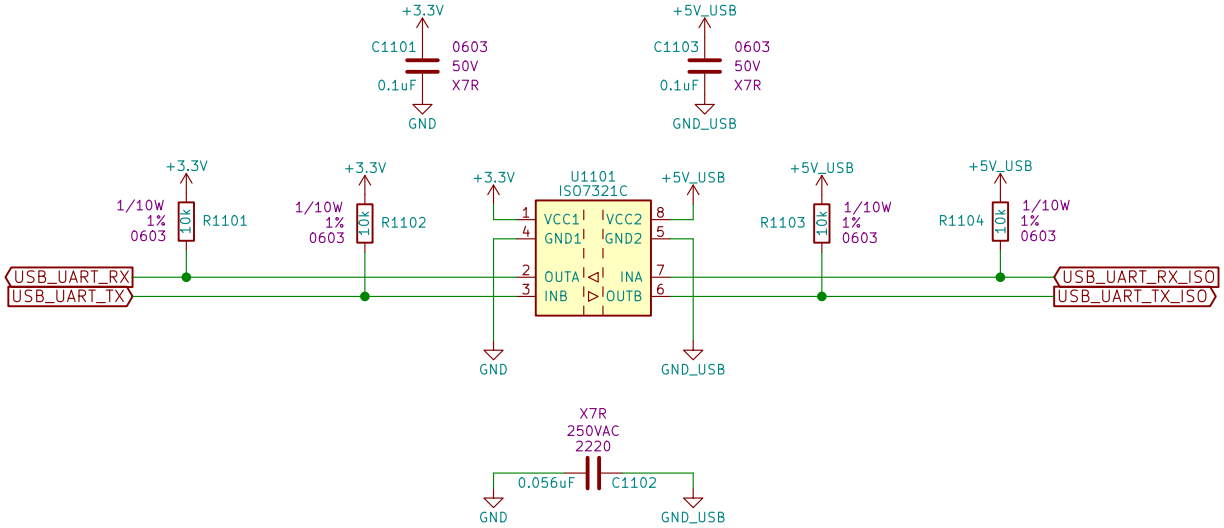
USB UART Isolation



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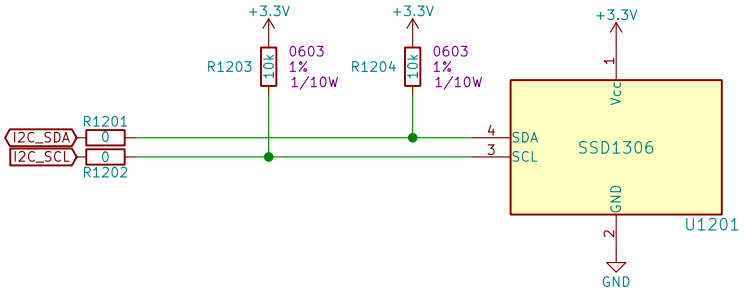
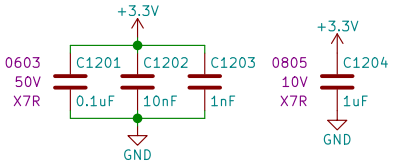
KiCad E.D.A. kicad (5.0.0)		Id: 11/18

OLED Display





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Title:		
Size: A	Date: 2018-10-01	**Rev: A**
KiCad E.D.A. kicad (5.0.0)		Id: 12/18

Mounting Holes and Mechanical Components

MH1301
MountingHole

MH1303
MountingHole

MH1305
MountingHole

MH1307
MountingHole

MH1302
MountingHole

MH1306
MountingHole

MH1308
MountingHole

4-40 Screw
MK1303

4-40 Screw
MK1307

4-40 Screw
MK1311

4-40 Screw
MK1315

4-40 Screw
MK1304

4-40 Screw
MK1312

4-40 Screw
MK1316

4-40 Standoff
MK1305

4-40 Standoff
MK1309

4-40 Standoff
MK1313

4-40 Standoff
MK1317

4-40 Standoff
MK1306

4-40 Standoff
MK1314

4-40 Standoff
MK1318

MK1301
Housing

MK1302
Lid

Sheet: /Mechanical/ File: Mechanical.sch		
Title:		
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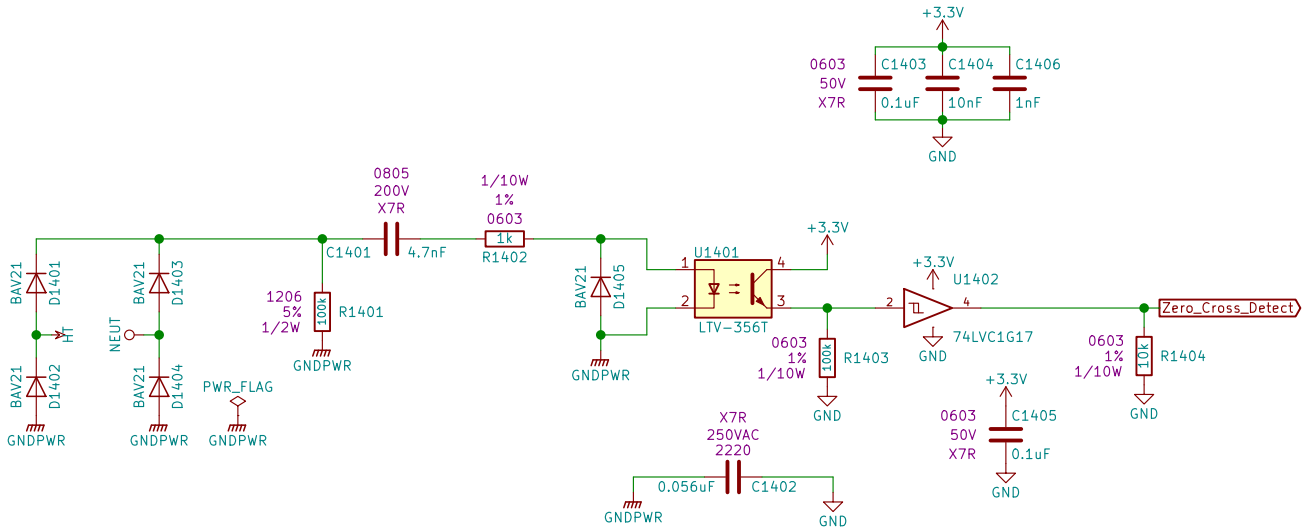
KiCad E.D.A. kicad (5.0.0)		Id: 13/18

Input AC Zero-Cross Detection



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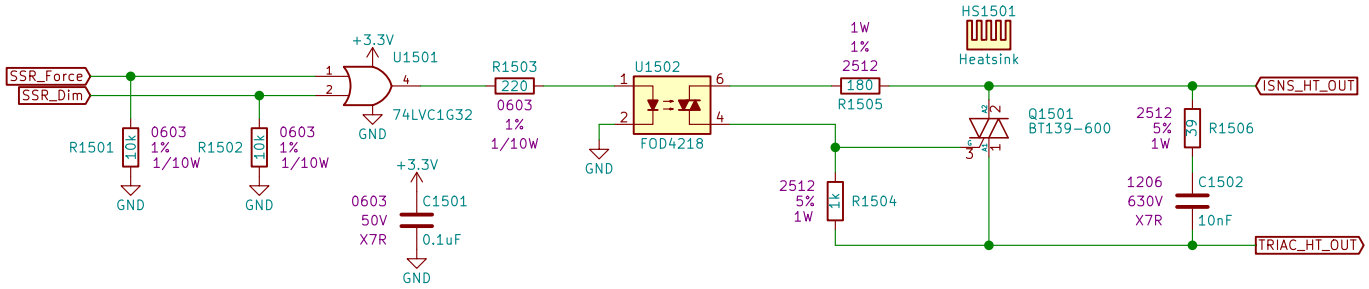
Size: A	Date:	**Rev:**
KiCad E.D.A. kicad (5.0.0)		Id: 14/18

Output AC Solid State Switch, Random Phase



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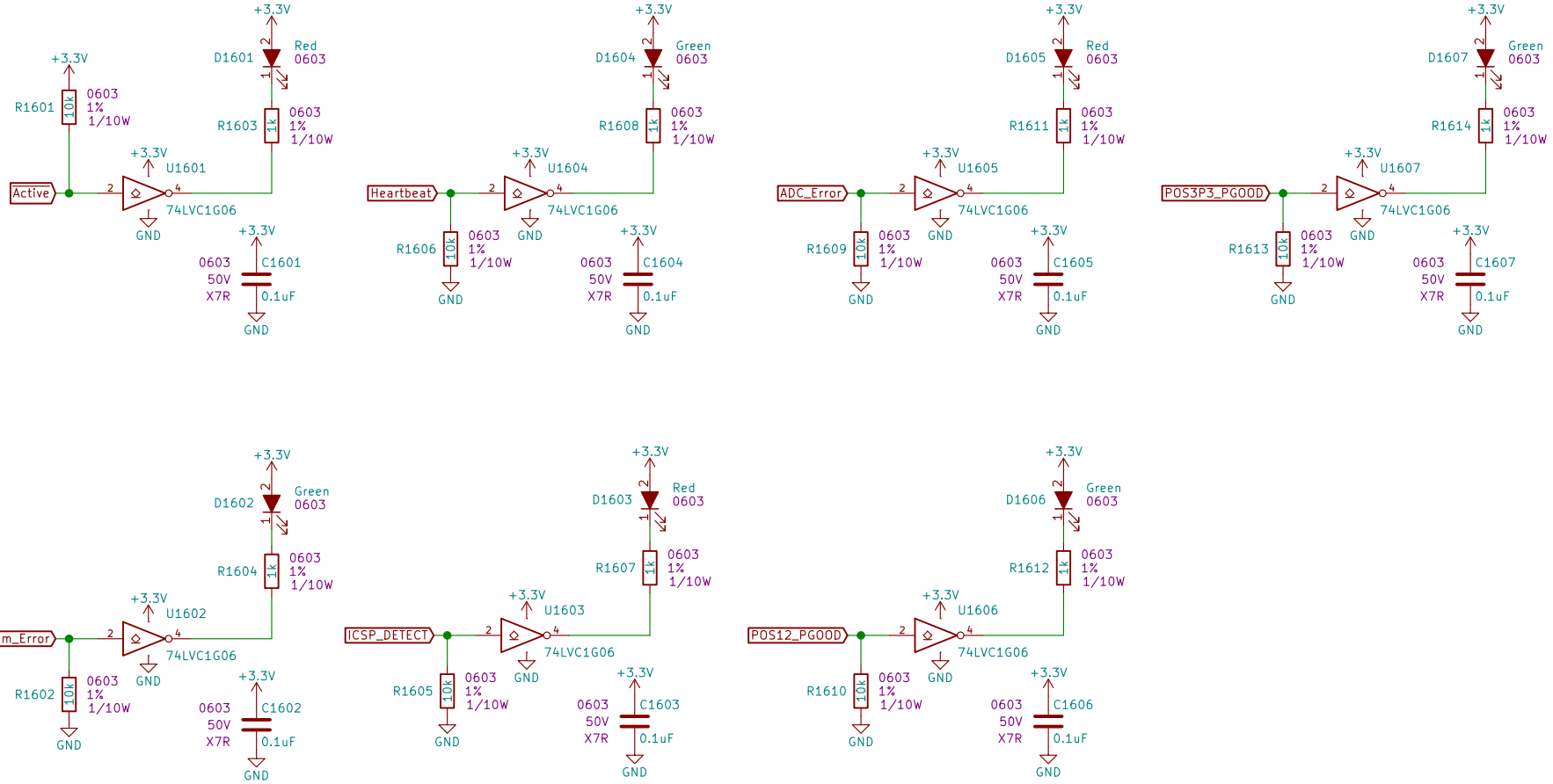
KiCad E.D.A. kicad (5.0.0)		Id: 15/18

Status LEDs



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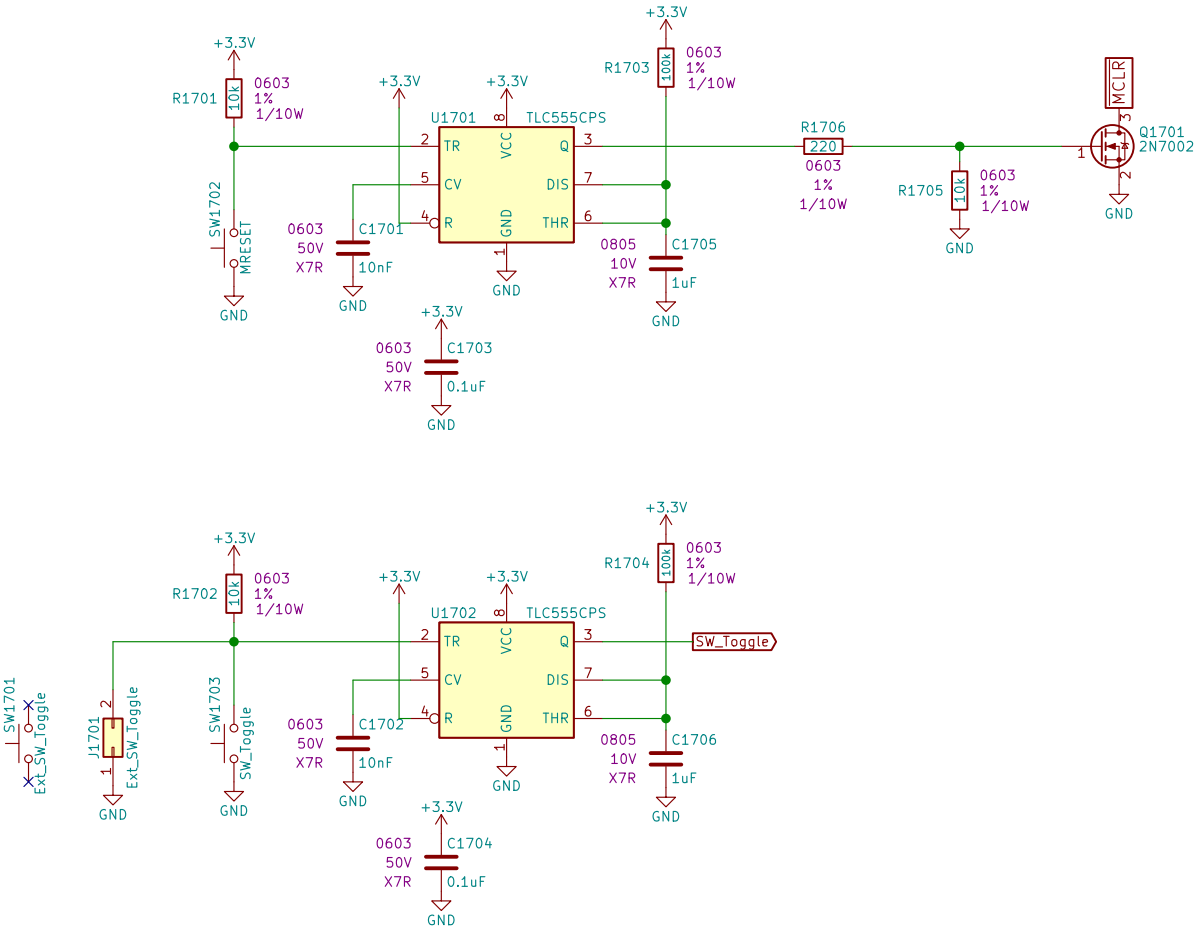
Title:

Size: A | Date: | **Rev:**

KiCad E.D.A. kicad (5.0.0) | Id: 16/18

Pushbuttons and Debouncing



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File: Pushbuttons.sch		
Title:		
Size: A	Date:	**Rev:**
KiCad E.D.A. kicad (5.0.0)		Id: 17/18

Firmware Notes

* Configure RA0 as both an ADC input and the inverting input into an internal comparator
* Comparator will be used with internal DAC to set a current limit
* Configure ADC clock as FRC, external +/-VREF
* Configure clocking structure to use ECM clock mode, 16MHz clock input, 4xPLL = 64MHz SYSCLK
* Configure RB0 as EXTINT0 for ZCD, and RB1 as EXTINT1 for output switching
* Configure RB6:7 as MSSP1 I2C IO
* Configure RE0 and RE2 as ADC inputs
* Configure RE1 and RE3 as interrupt on change inputs
* Configure RF0 as open drain output, force low after booting
* Configure RF1:3 as push pull outputs, start low
* Configure RG6 as EUSART2 RX and RG7 as EUSART2 TX
* Configure the ADC to use digital filtering with lowest crossover frequency
* Use Timer7 to gather ADC data on all channels and run calculations on it at a fixed time base
* Use Timer5 as the time base for output dimming
* Use Timer6 as hearthbeat time base

Sheet: /Firmware Notes/		
File: Firmware_Notes.sch		
Title:		
Size: A	Date:	**Rev:**
KiCad E.D.A. kicad (5.0.0)		Id: 18/18