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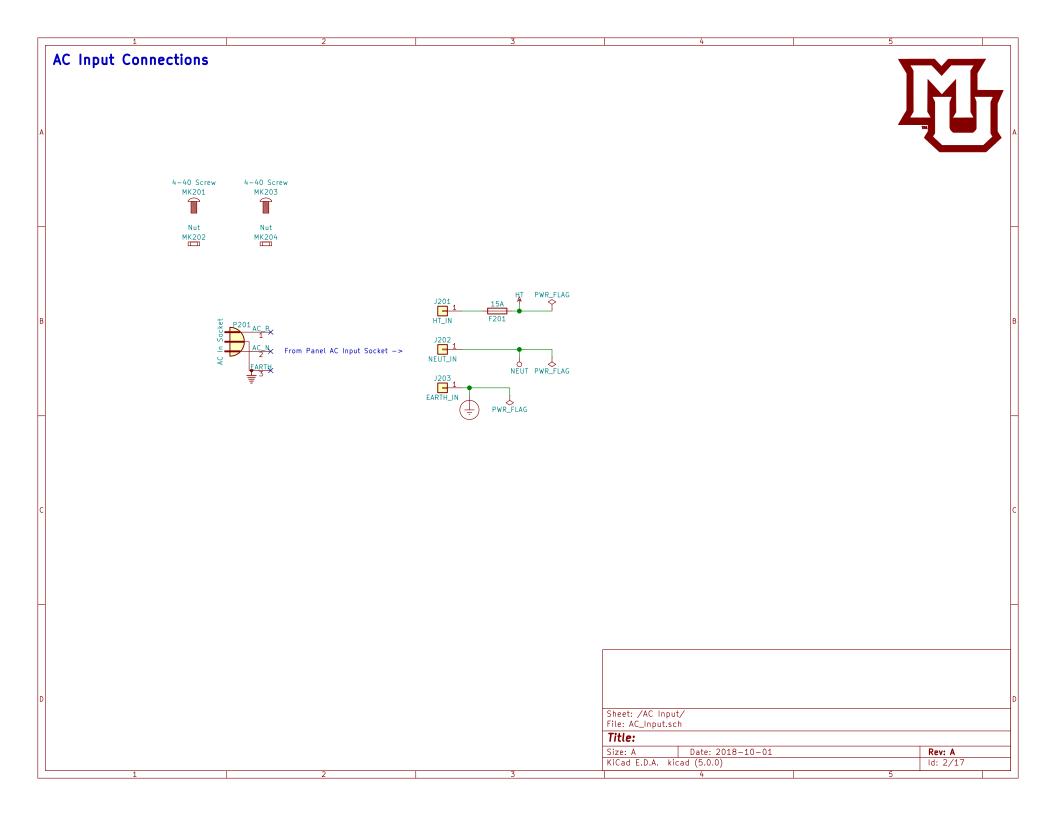
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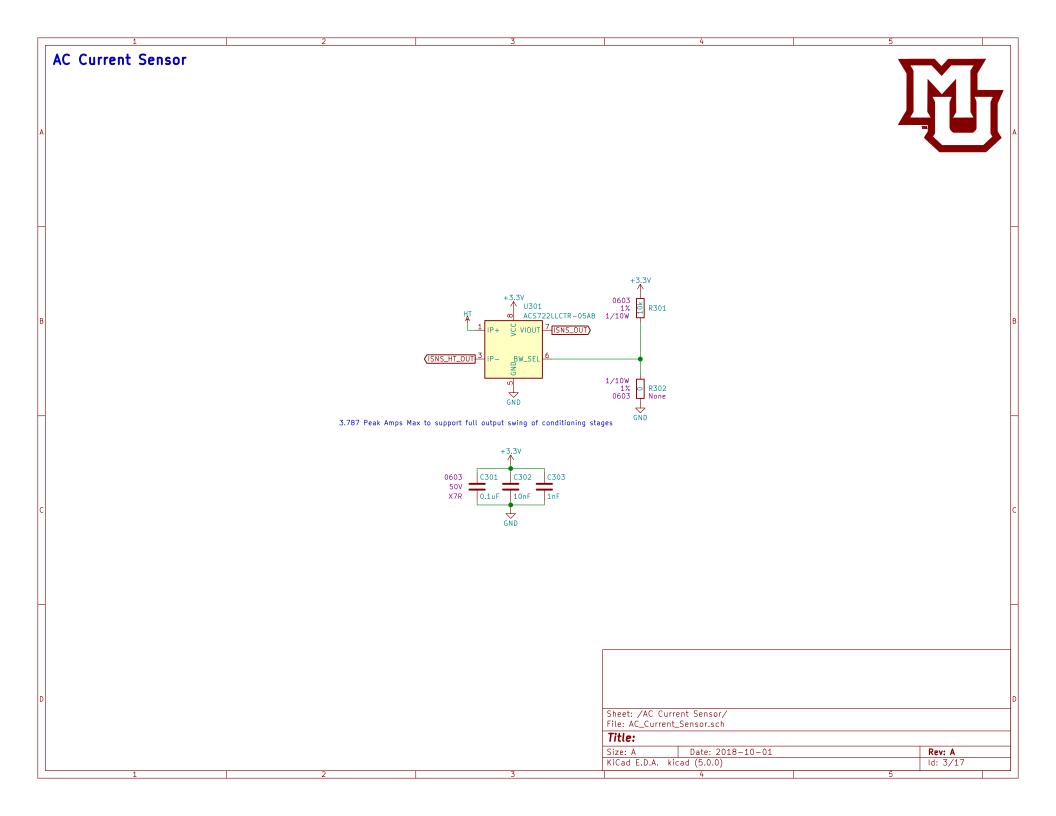
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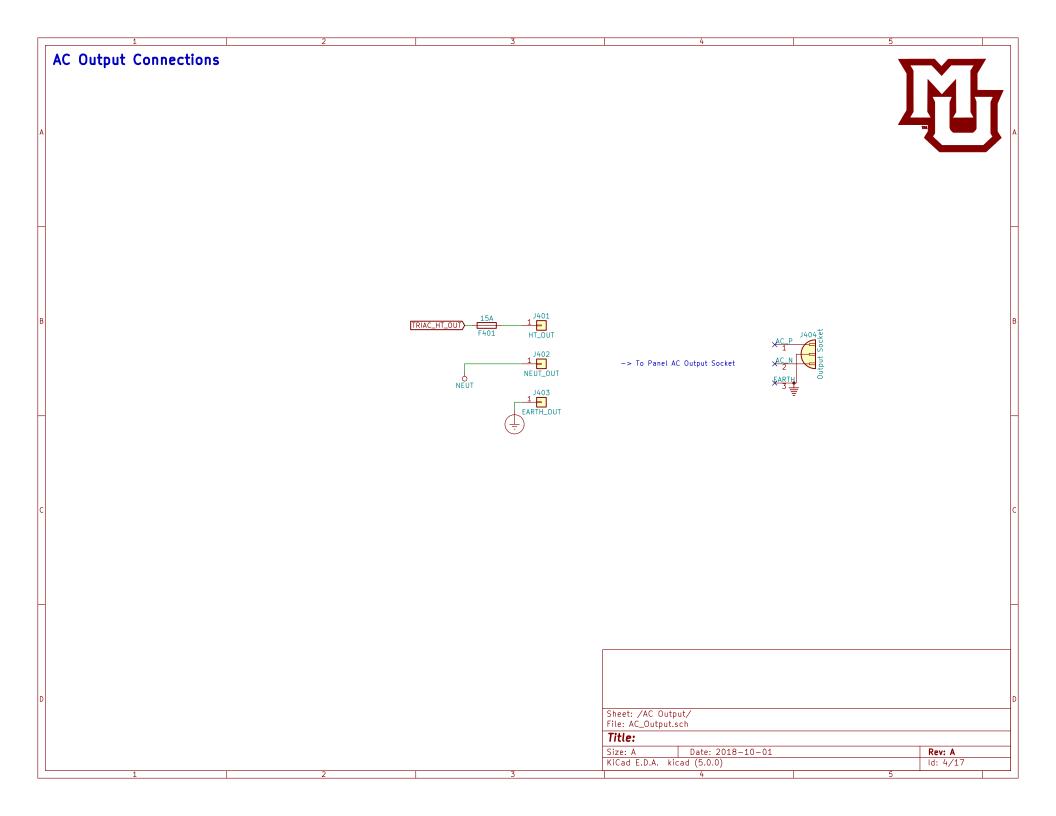
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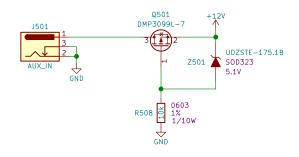


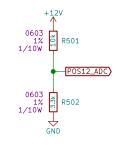


+12V 4W Isolated AC/DC Converter, +12V PGOOD Window Comparator







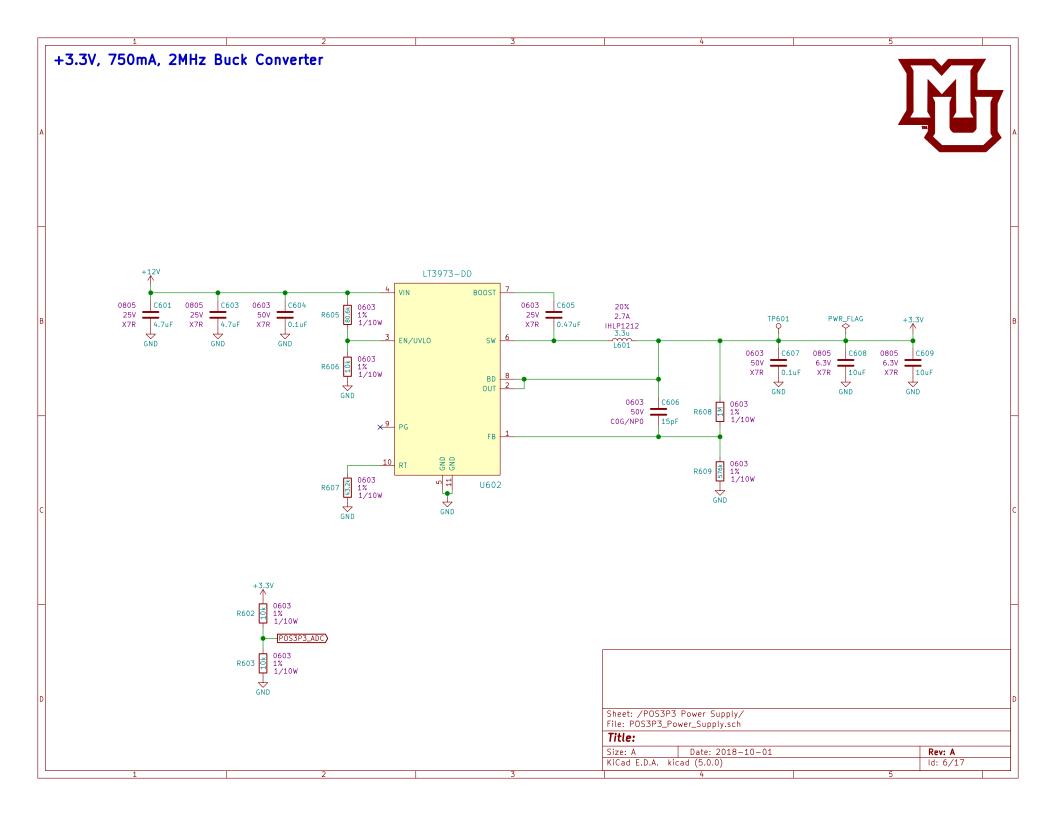


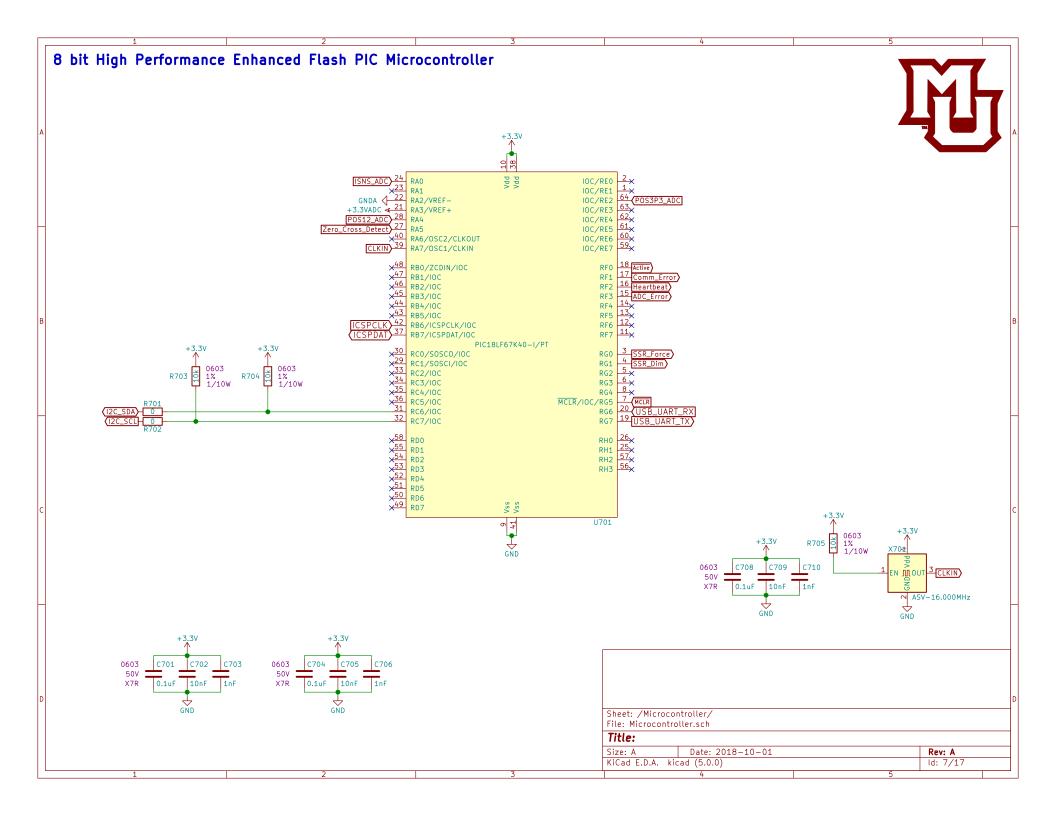
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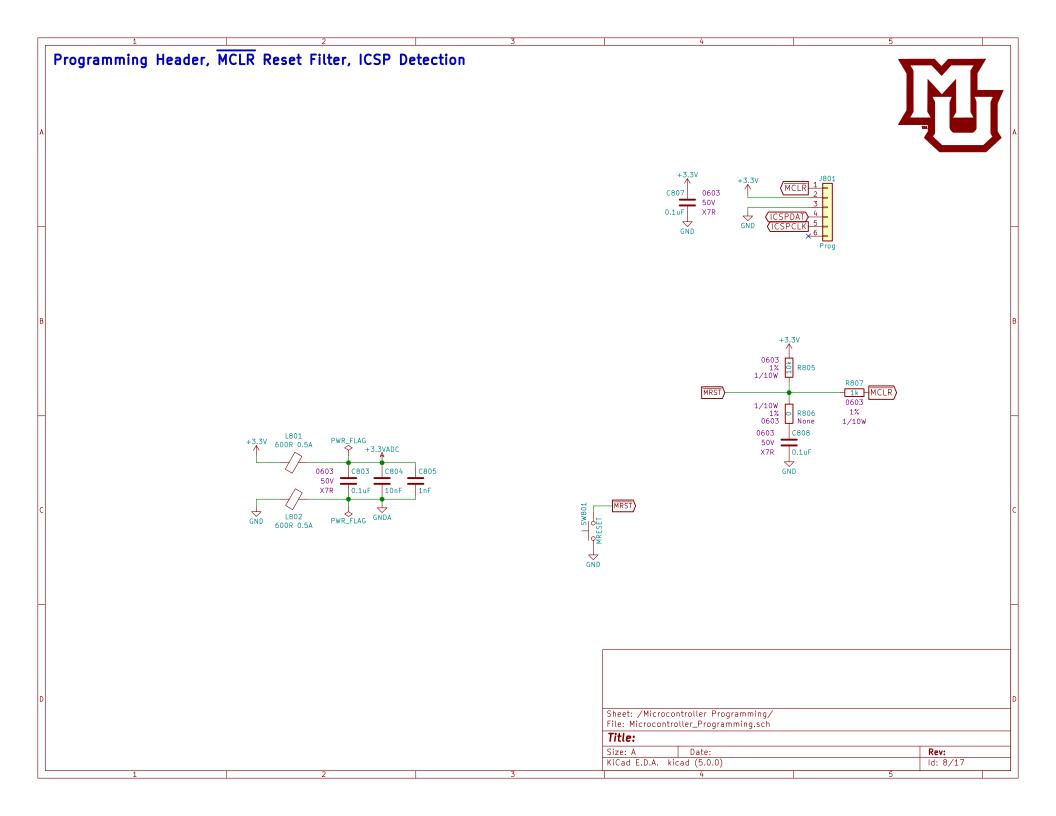
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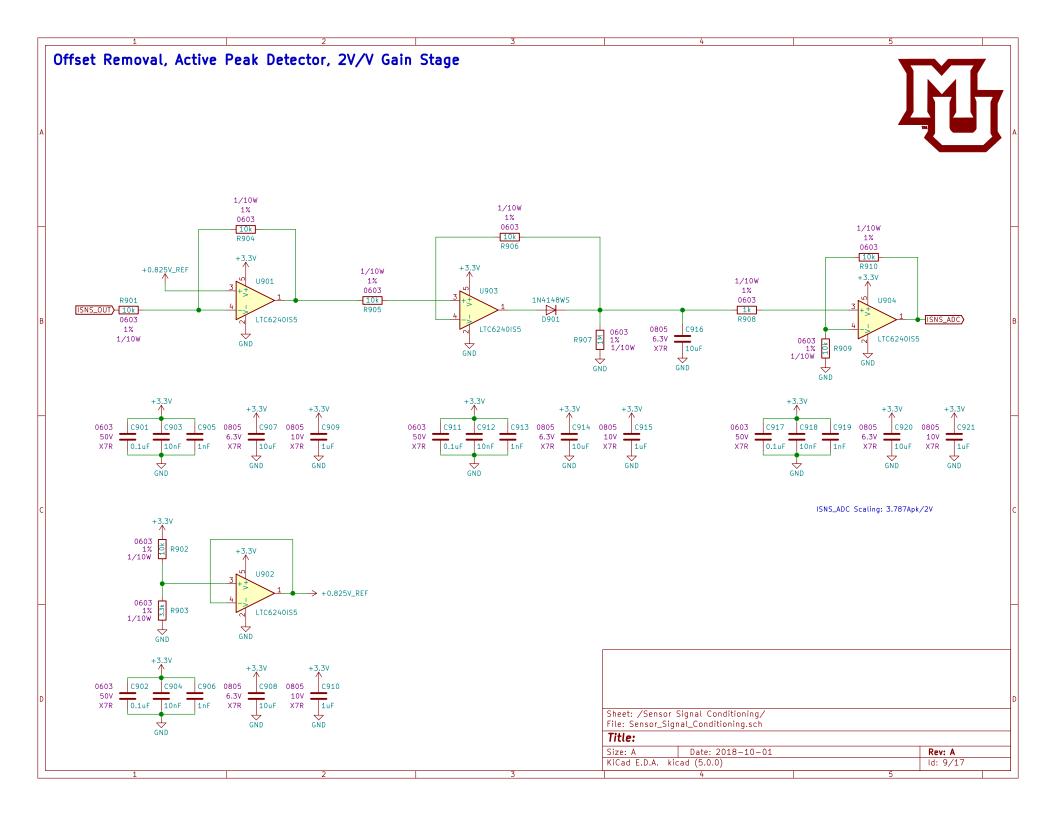
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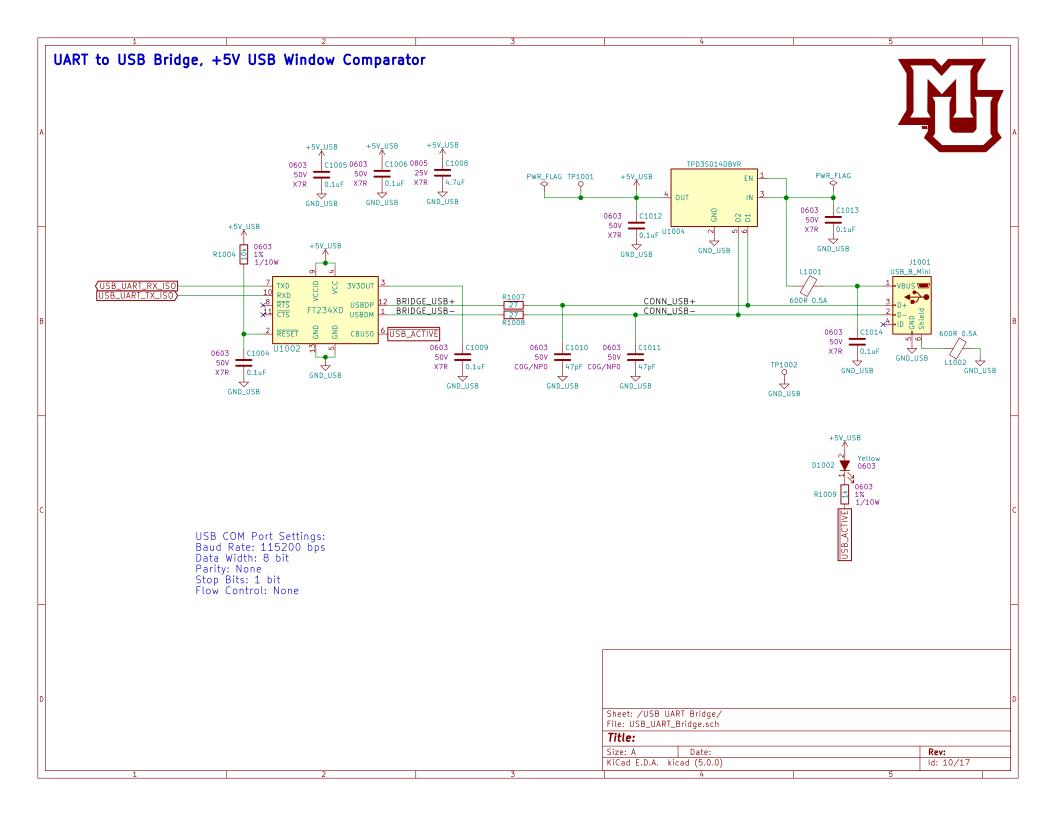
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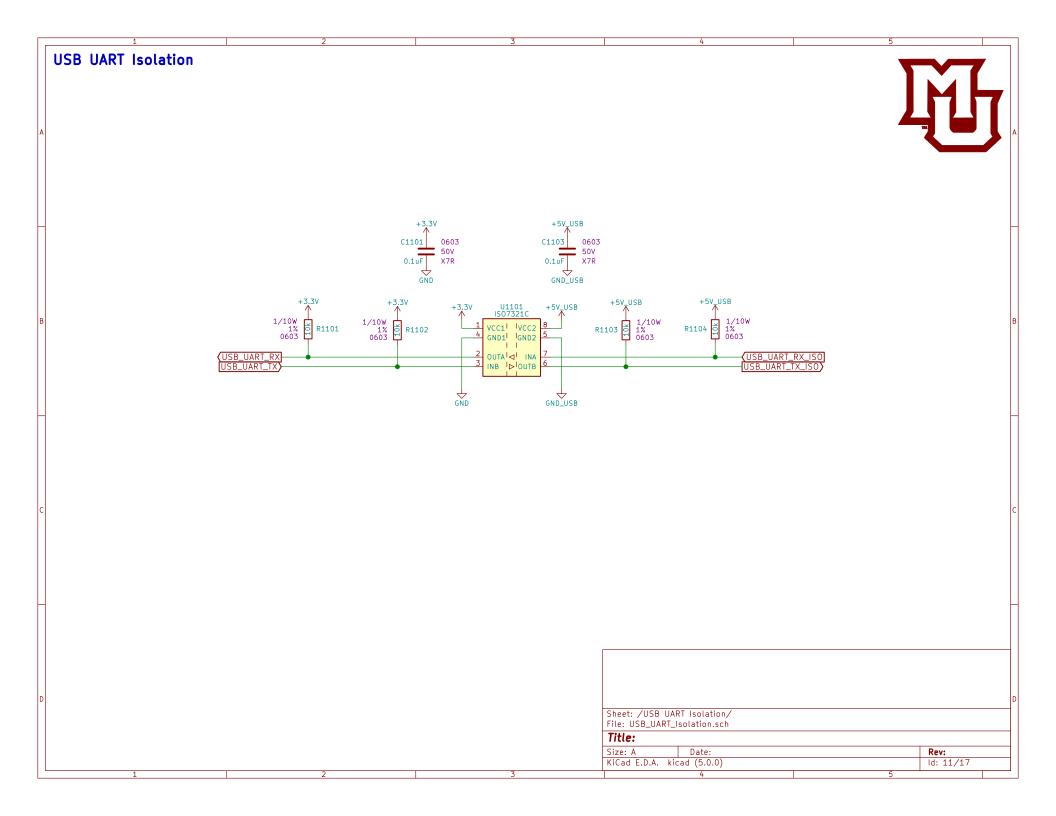


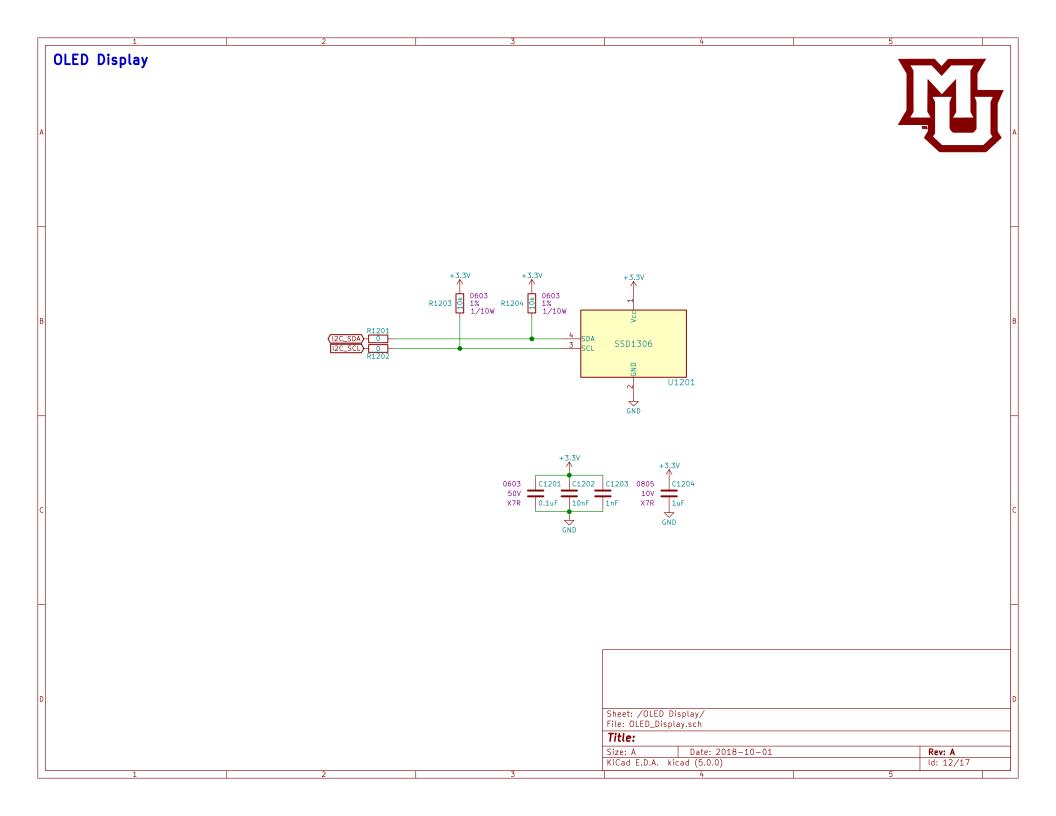




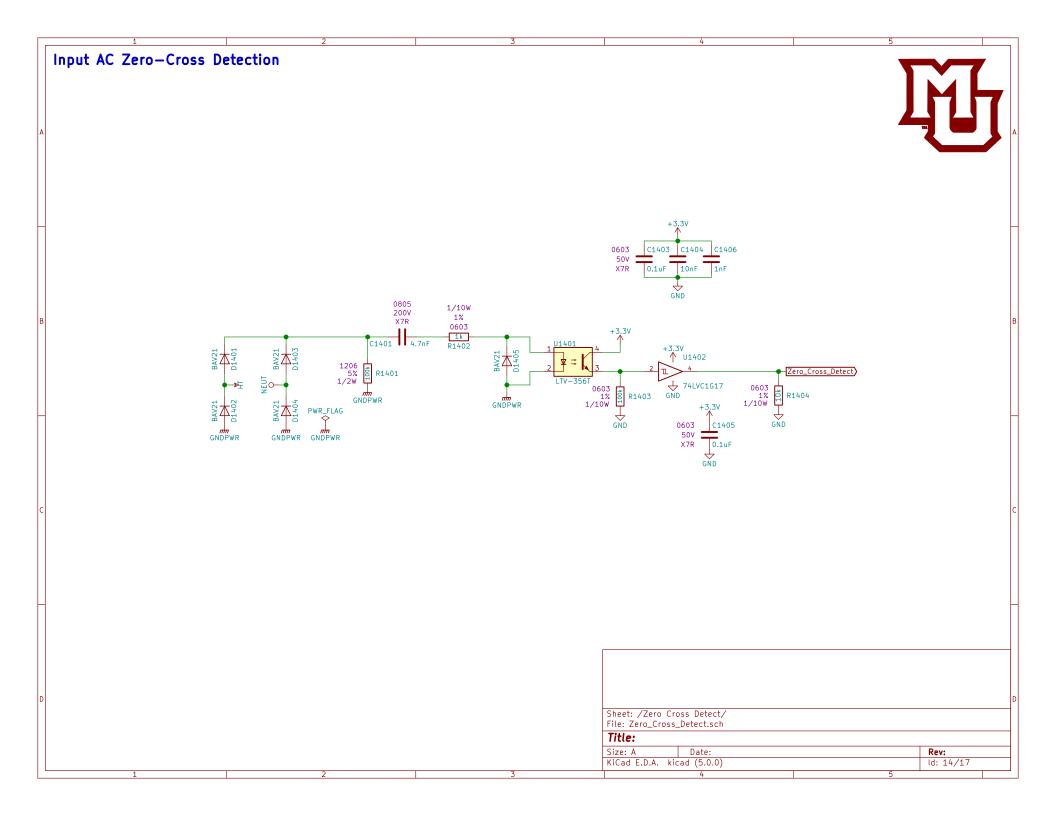








Mounting Holes and Mechanical Components MH1301 MountingHole MH1303 MountingHole MH1305 MountingHole MH1307 MountingHole 0 0 0 4-40 Screw 4-40 Screw 4-40 Screw 4-40 Screw MK1303 MK1307 MK1311 MK1315 MH1302 MountingHole MH1306 MountingHole MH1308 MountingHole 4-40 Screw 4-40 Screw 4-40 Screw MK1304 MK1312 MK1316 4-40 Standoff 4-40 Standoff 4-40 Standoff 4-40 Standoff MK1305 MK1309 MK1313 MK1317 4-40 Standoff 4-40 Standoff 4-40 Standoff MK1306 MK1314 MK1318 Sheet: /Mechanical/ File: Mechanical.sch Title: Size: A Date: 2018-10-01 Rev: A KiCad E.D.A. kicad (5.0.0) ld: 13/17



Output AC Solid State Switch, Random Phase HS1501 WWW Heatsink +3.3V ↑ U1501 1% 2512 R1503 ____180 R1505 (ISNS_HT_OUT 0603 Q1501 BT139-600 1% 1/10W R1501 0603 1% R1502 0603 1% 1/10W GND GND 1206 630V X7R 2512 5% R1504 0603 50V X7R C1501 0.1uF GND TRIAC_HT_OUT) Sheet: /Output Switch/ File: Output_Switch.sch Title:

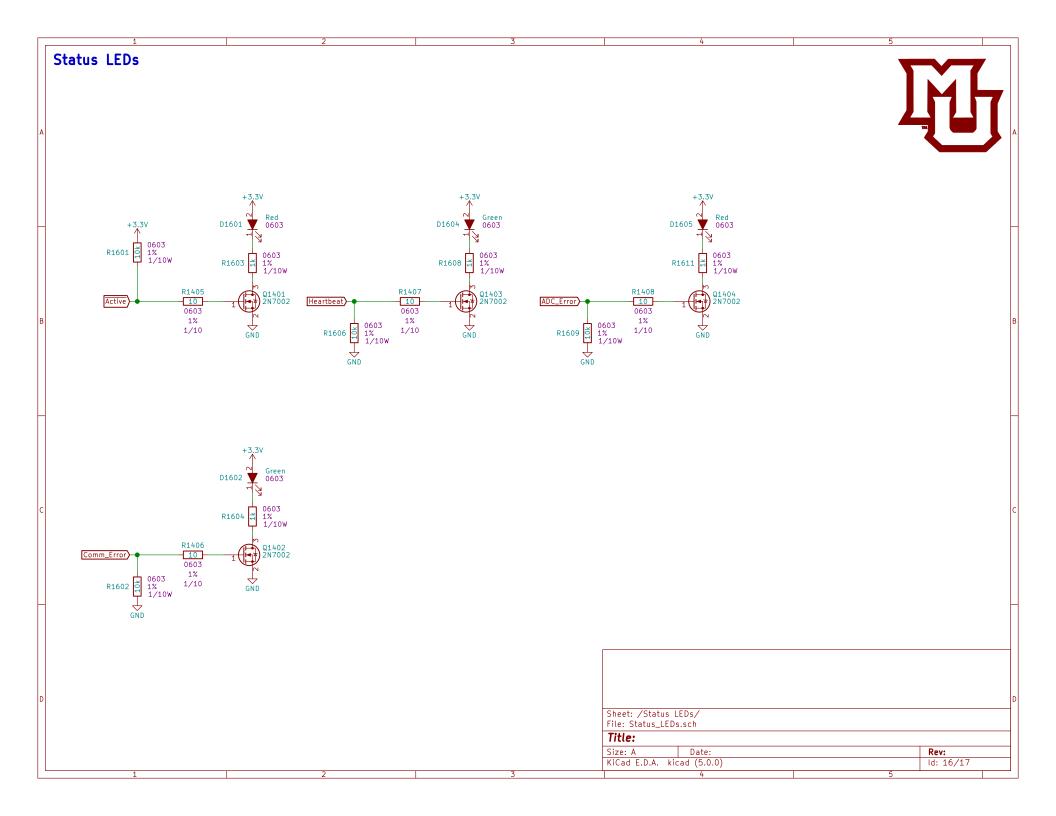
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Firmware Notes * Configure RAO as both an ADC input and the inverting input into an internal comparator

* Comparator will be used with internal DAC to set a current limit

* Configure ADC clock as FRC, external +/-VREF

* Configure RAO as intercupt to use ECM clock mode, 16MHz clock input, 4xPLL = 64MHz SYSCLK

* Configure RA4 as an ADC input

* Configure RA6 as EXTINTO for ZCD

* Configure RB0 as EXTINT1 for output switching

* Configure RC2 and RC3 as interrupt on change inputs

* Configure RC2 and RC3 as interrupt on change inputs

* Configure RC9 as an ADC input

* Configure RC0 as open drain output, force low after booting

* Configure RC0 as push pull outputs, start low

* Configure RC1 as as push pull outputs, start low

* Configure RC6 as EUSART2 RX and RG7 as EUSART2 TX

* Configure the ADC to use digital filtering with lowest crossover frequency

* Use Timer7 to gather ADC data on all channels and run calculations on it at a fixed time base

* Use Timer6 as hearthbeat time base * Use Timer6 as hearthbeat time base Sheet: /Firmware Notes/ File: Firmware_Notes.sch Title: Rev: Size: A Date: KiCad E.D.A. kicad (5.0.0) ld: 17/17