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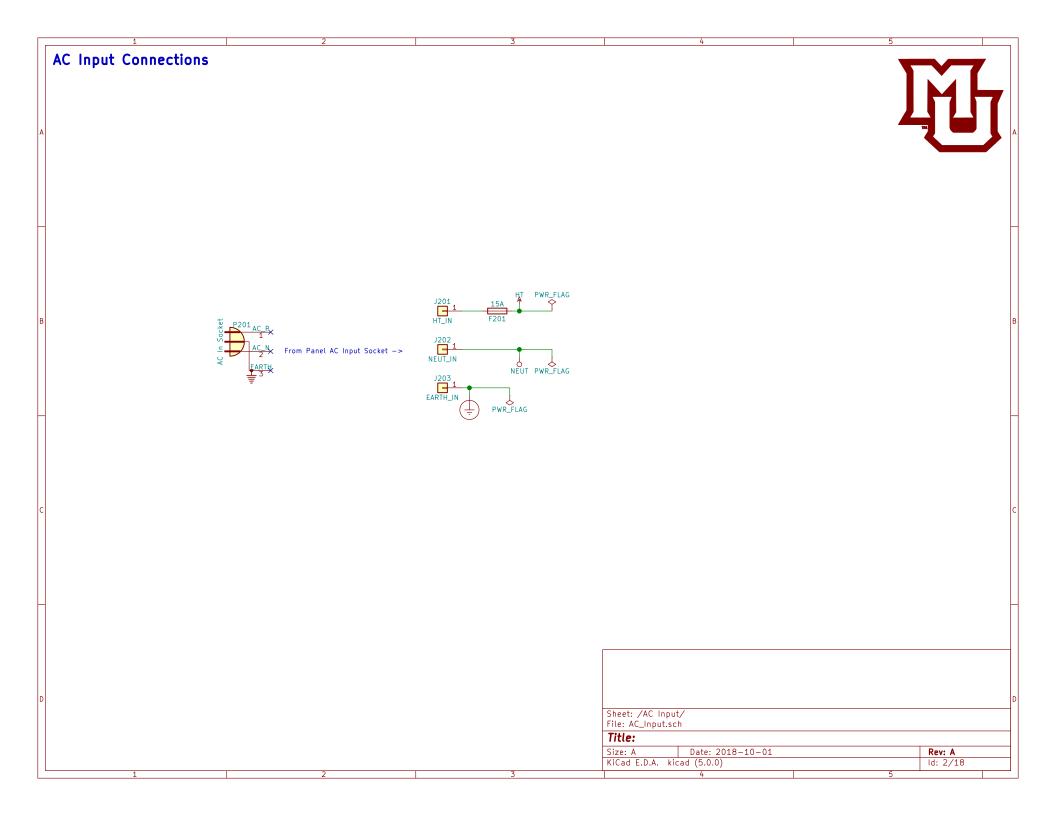
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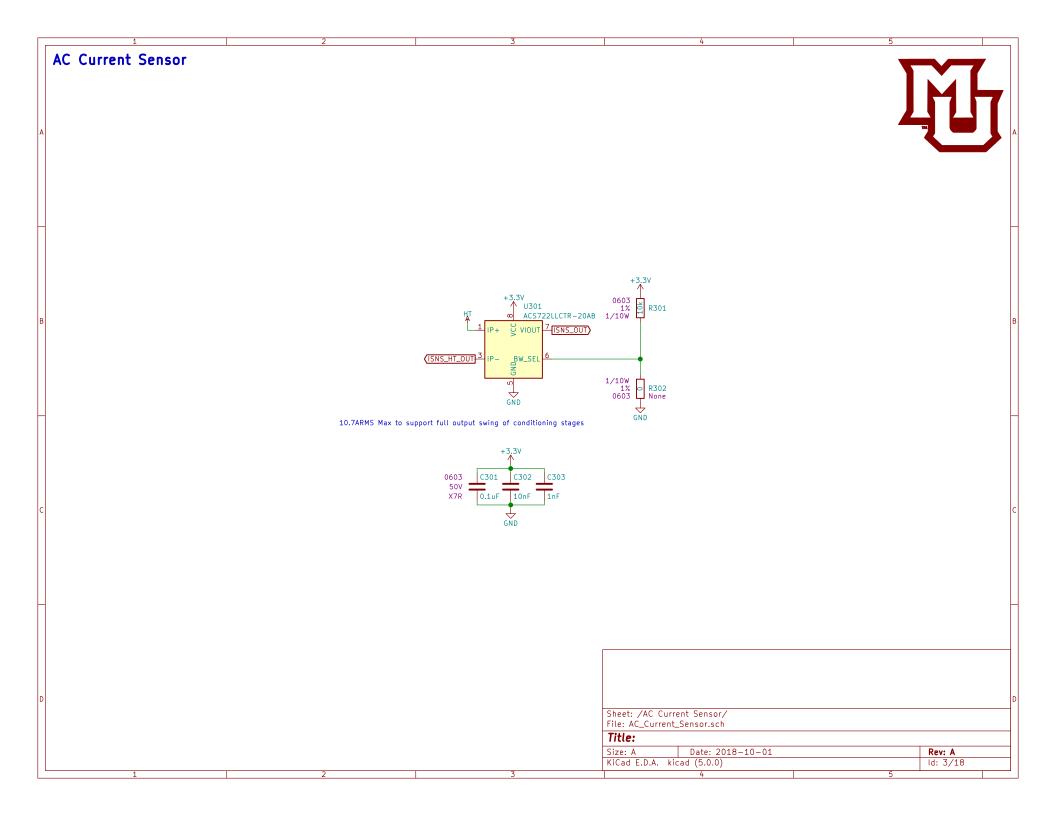
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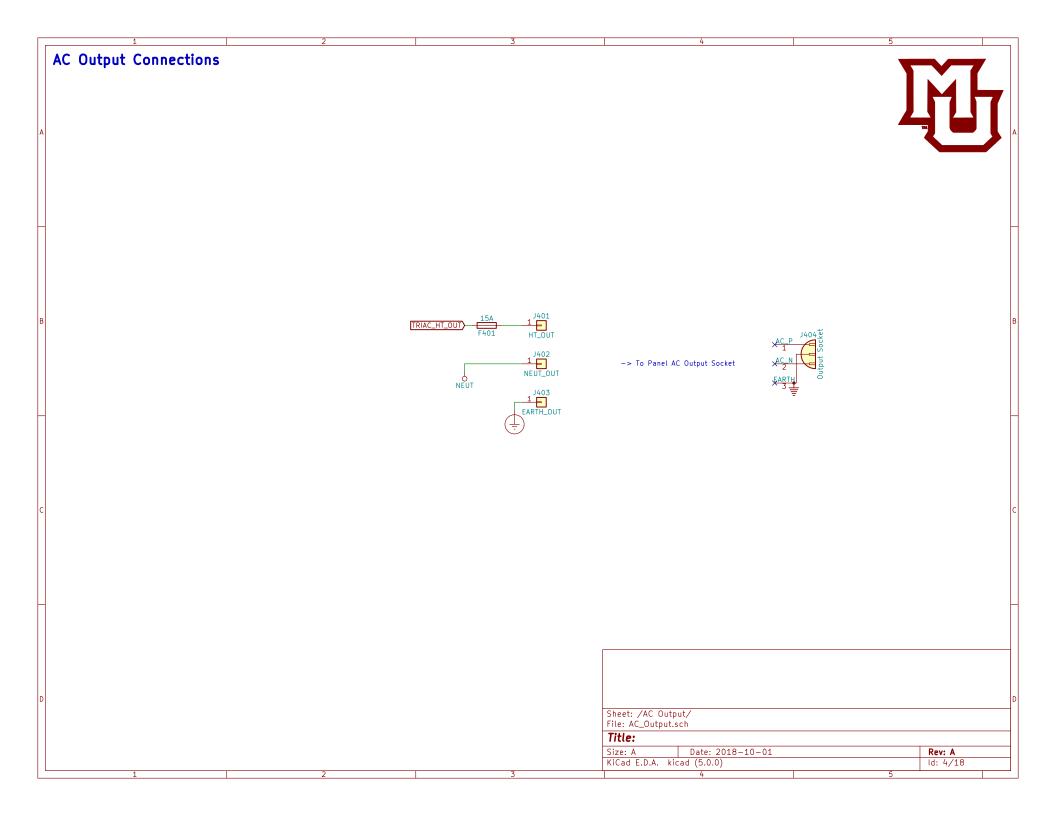
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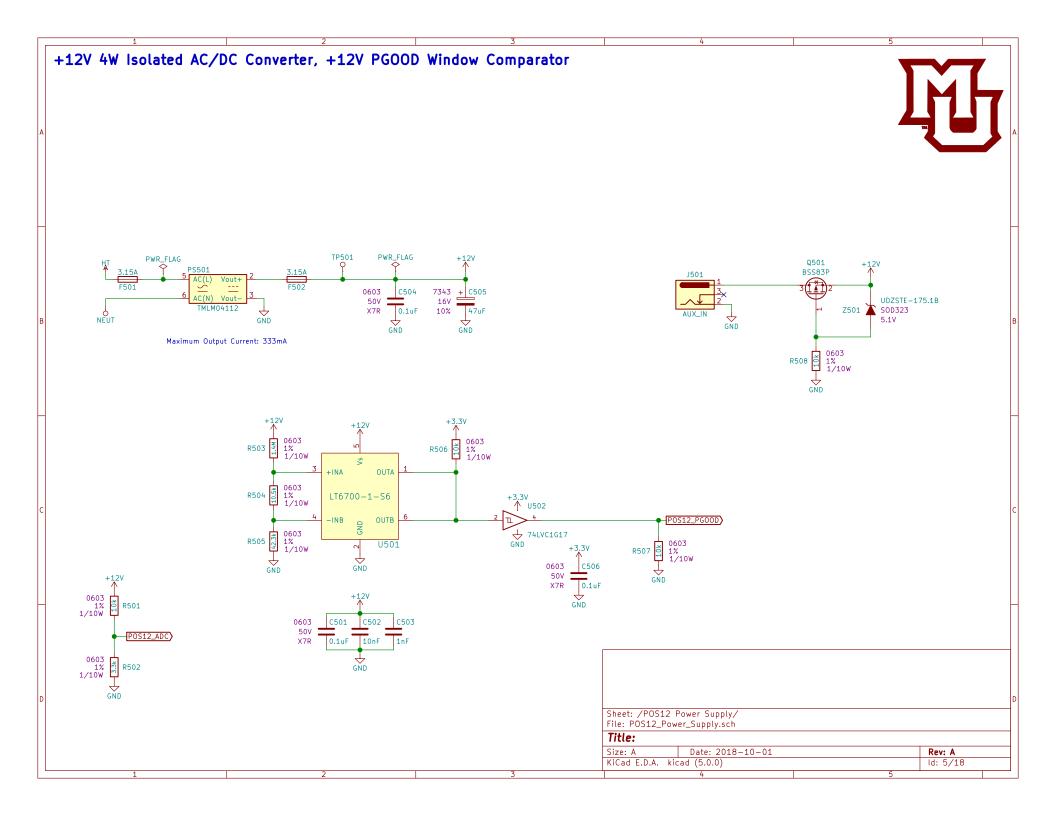
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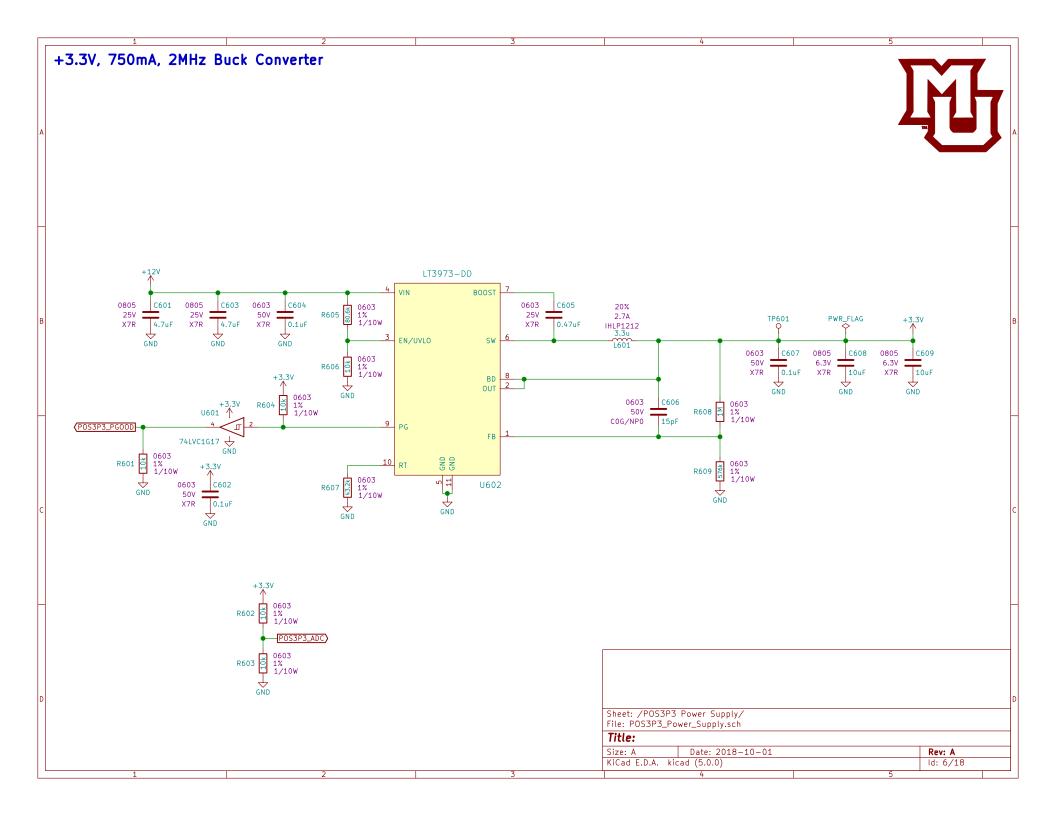
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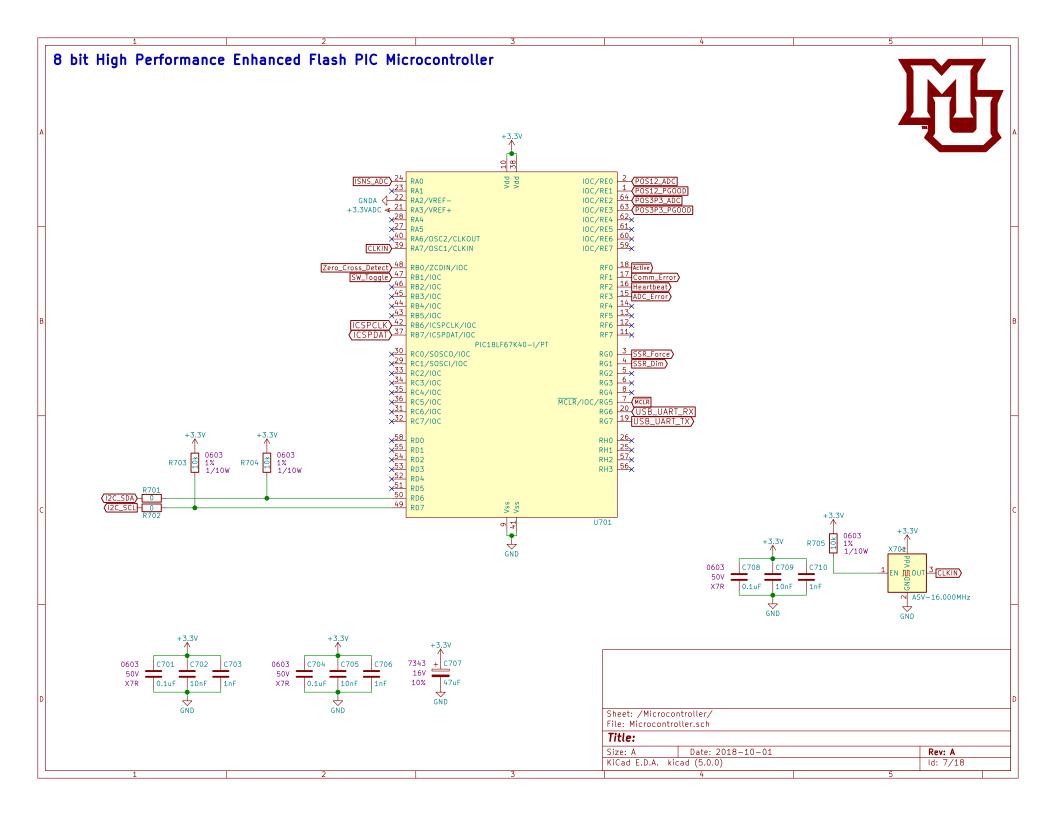






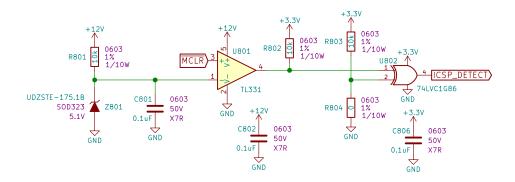


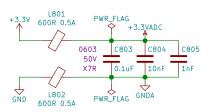


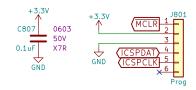


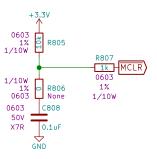
### Programming Header, MCLR Reset Filter, ICSP Detection









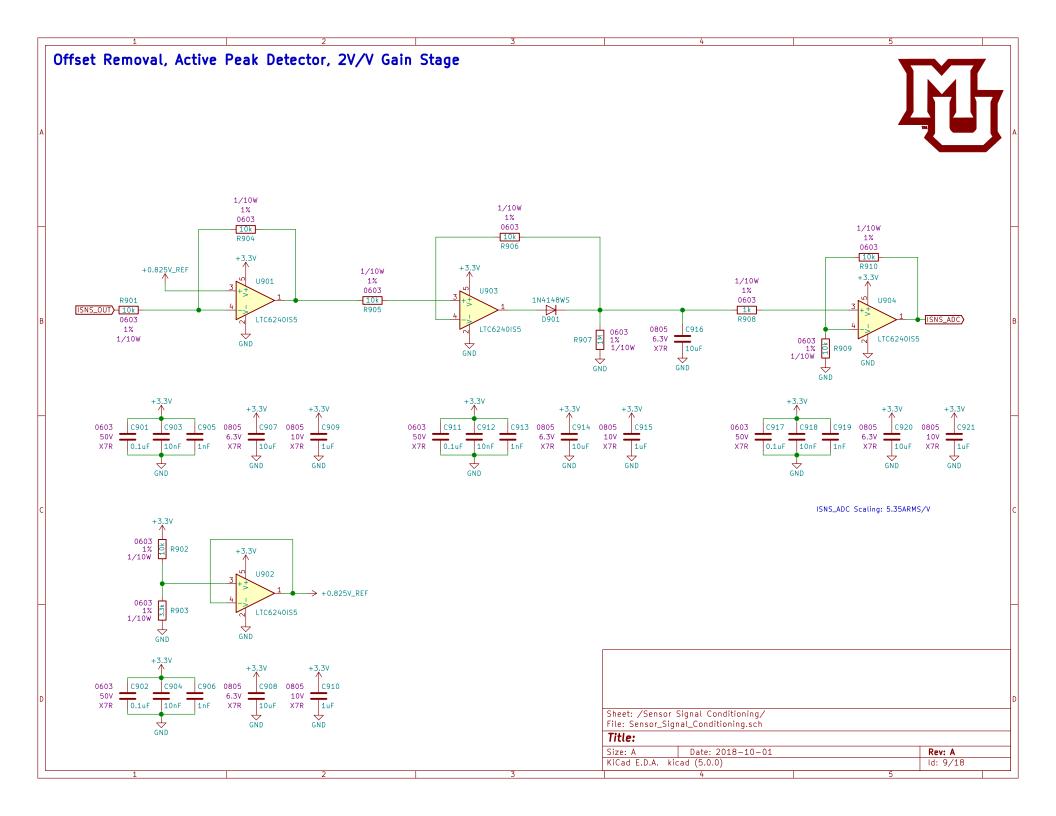


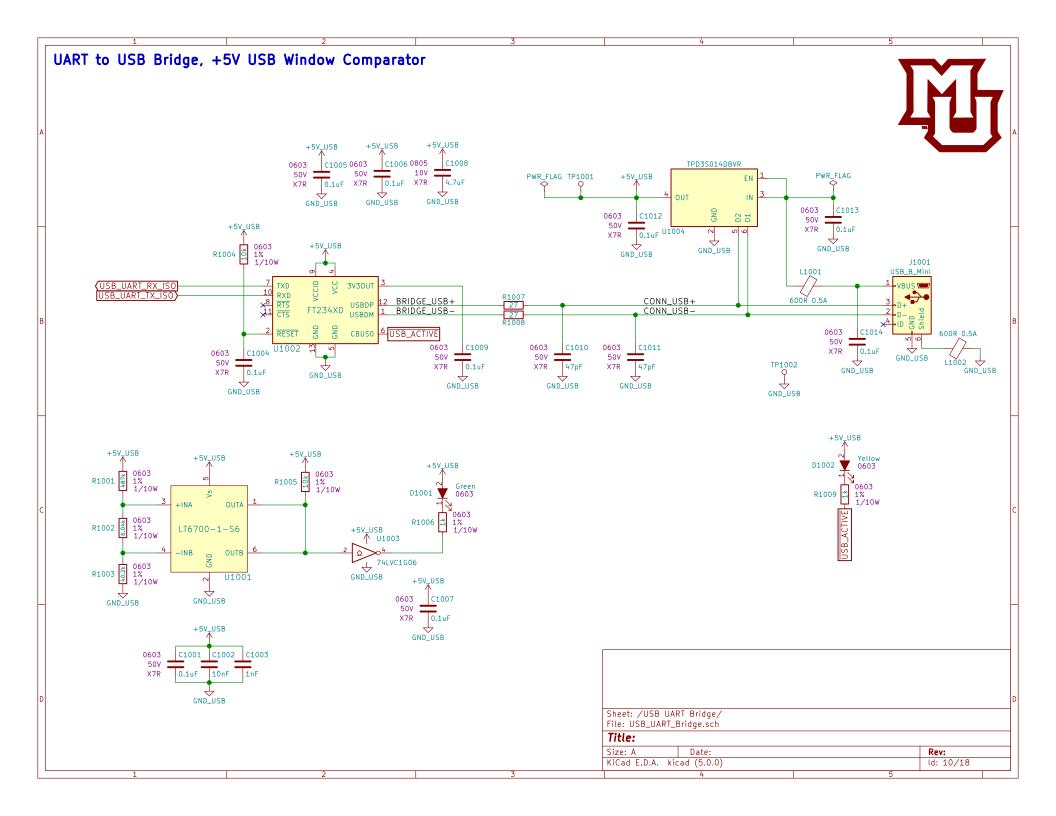
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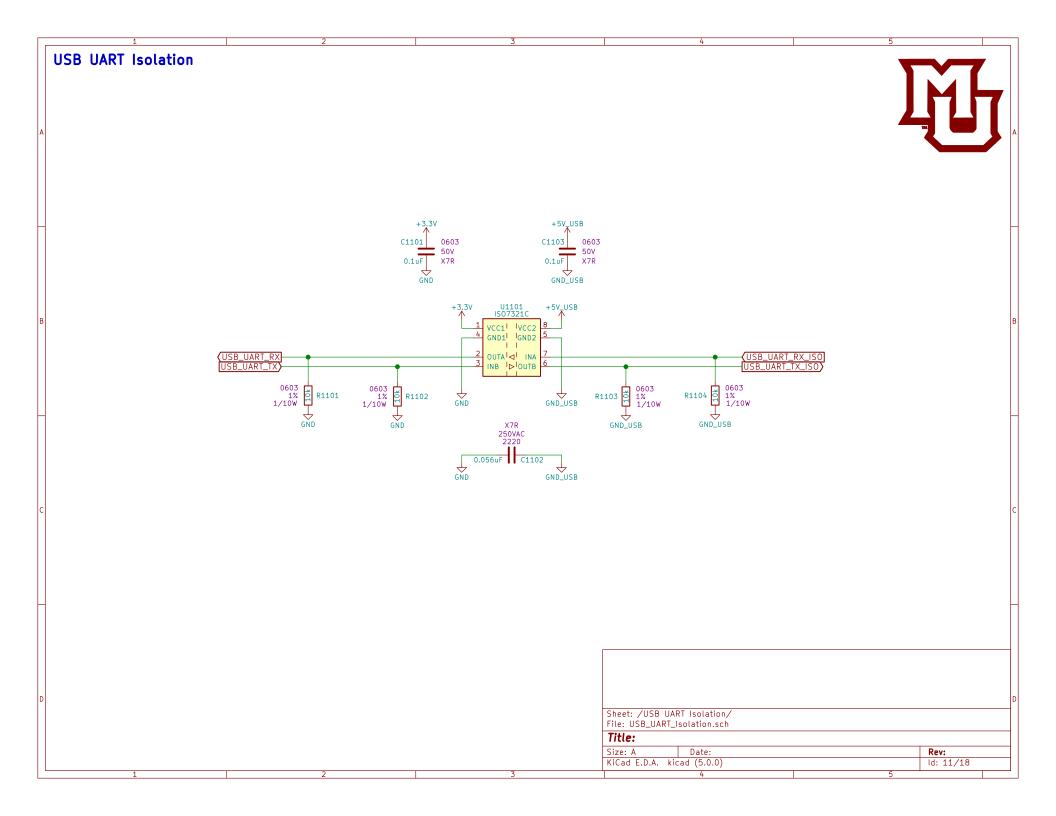
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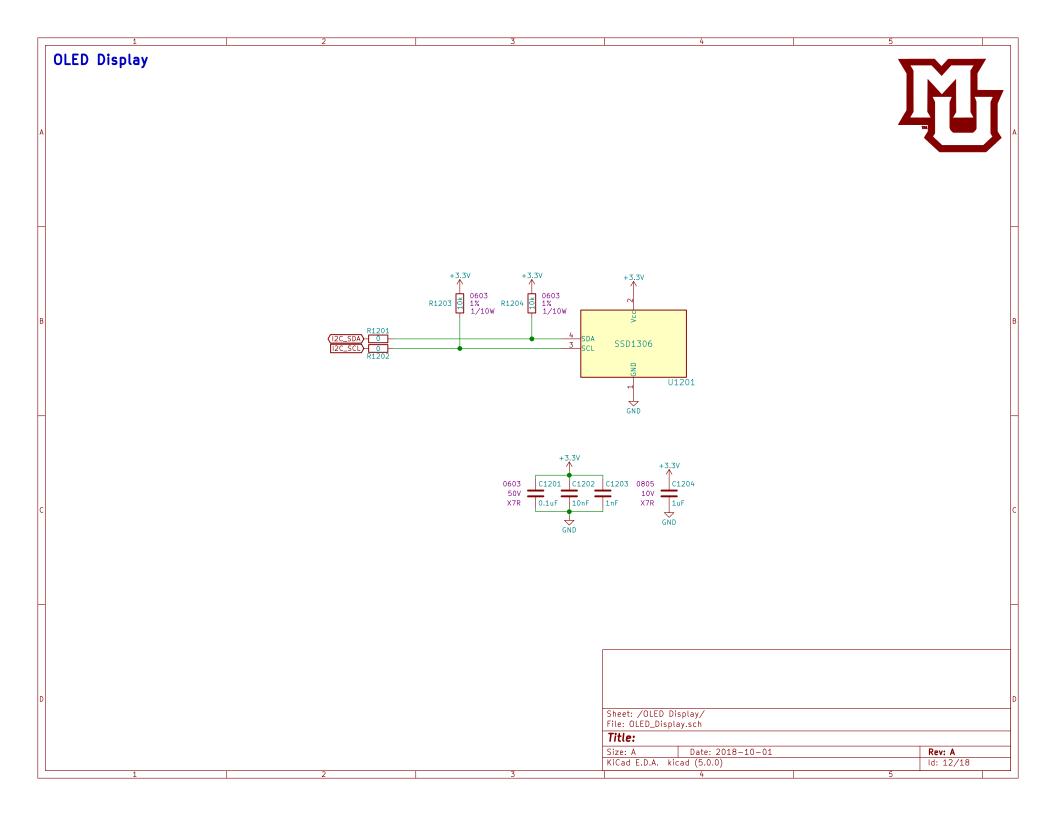
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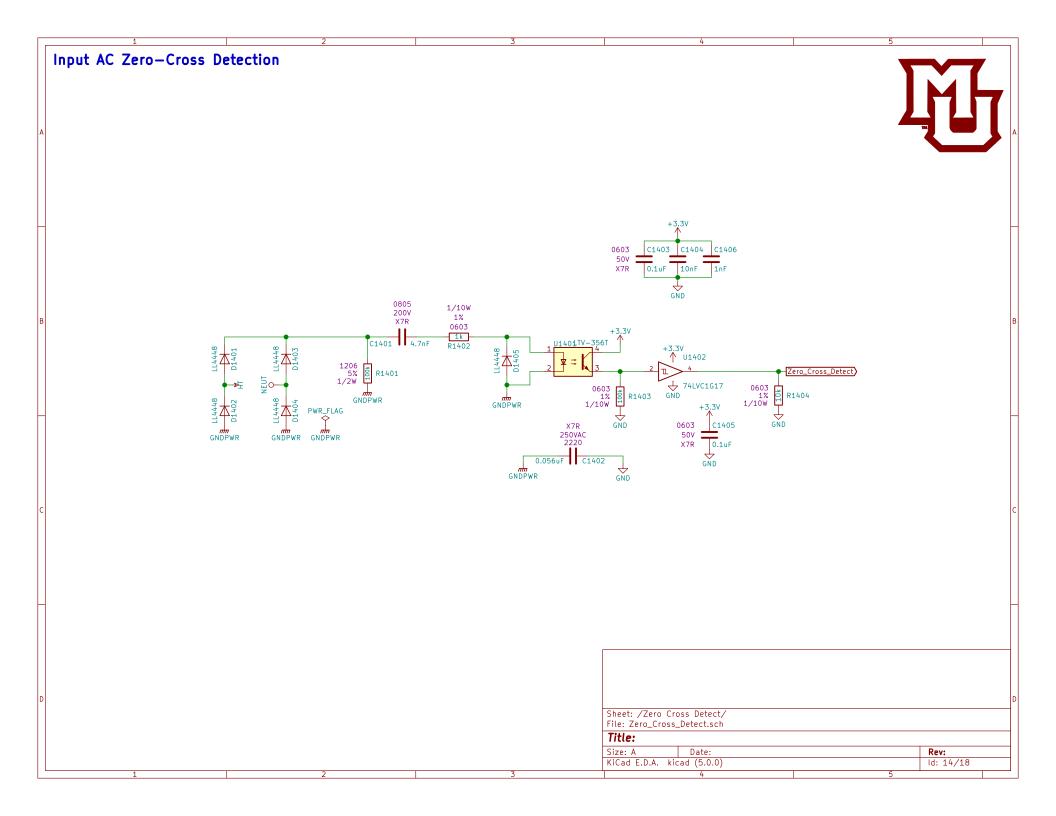






#### Mounting Holes and Mechanical Components MH1301 MountingHole MH1303 MountingHole MH1305 MountingHole MH1307 MountingHole 4-40 Screw 4-40 Screw 4-40 Screw 4-40 Screw MK1303 MK1307 MK1311 MK1315 4-40 Screw 4-40 Screw 4-40 Screw 4-40 Screw MK1304 MK1308 MK1312 MK1316 MH1302 MountingHole MH1304 MH1306 MH1308 MountingHole MountingHole MountingHole 4-40 Standoff 4-40 Standoff 4-40 Standoff 4-40 Standoff MK1305 MK1309 MK1313 MK1317 4-40 Standoff 4-40 Standoff 4-40 Standoff 4-40 Standoff MK1306 MK1310 MK1314 MK1318 Sheet: /Mechanical/ File: Mechanical.sch Title: Size: A Date: 2018-10-01 Rev: A

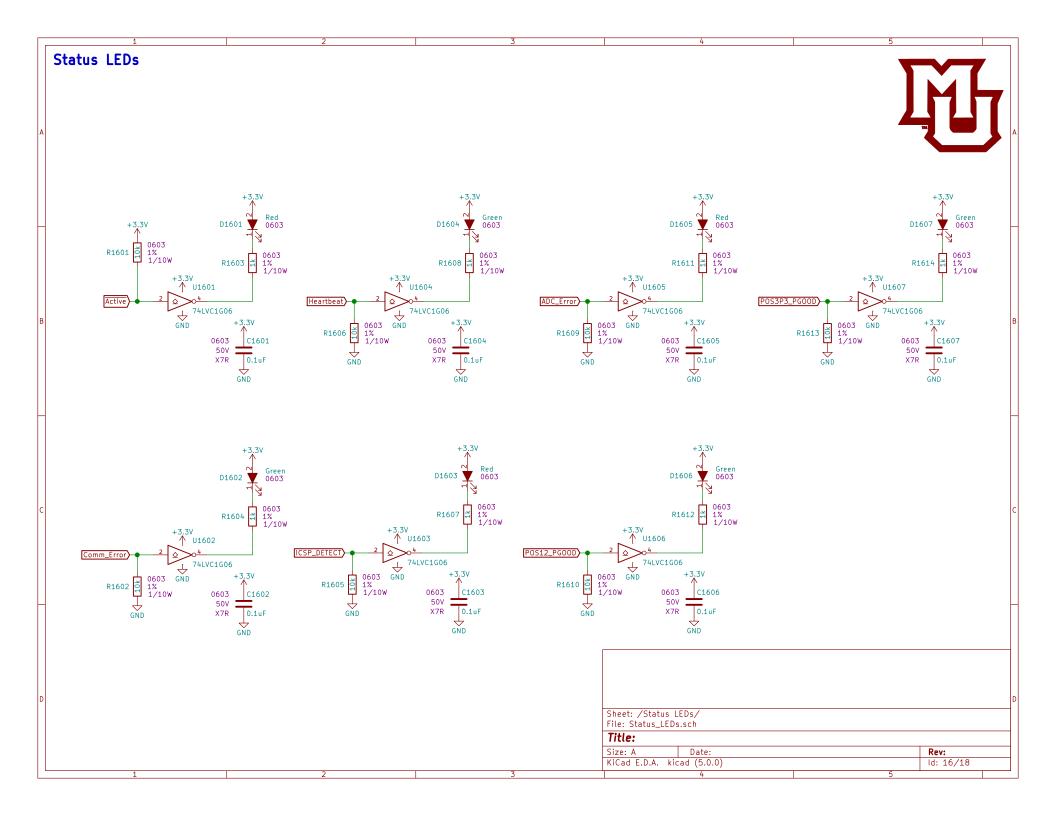
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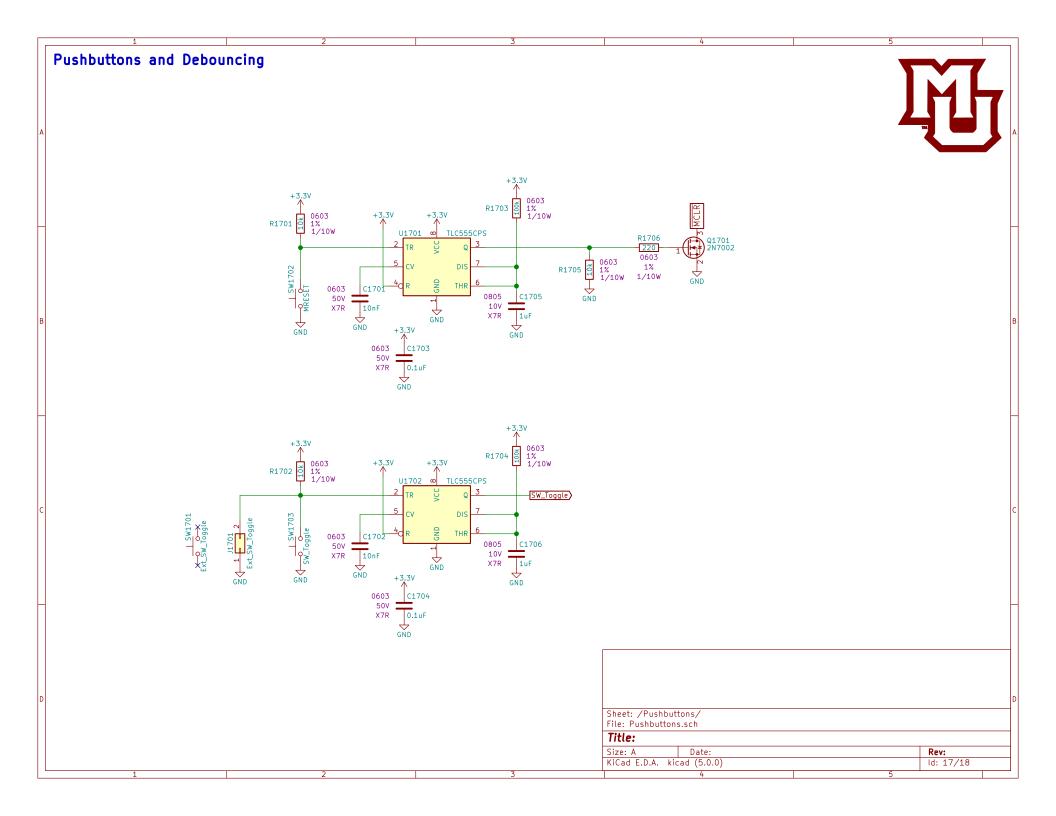


# Output AC Solid State Switch, Random Phase HS1501 WWW Heatsink +3.3V ↑ U1501 1% 2512 R1503 SSR\_Force SSR\_Dim 220 \_\_\_\_180 R1505 (ISNS\_HT\_OUT 0603 Q1501 BT139-600 74LVC1G32 GND 1% 1/10W R1501 0603 1% R1502 0603 1% 1/10W GND GND 1206 630V X7R 2512 1% 1W R1504 0603 50V X7R C1501 0.1uF GND TRIAC\_HT\_OUT) Sheet: /Output Switch/ File: Output\_Switch.sch Title: Size: A Date: Rev:

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Firmware Notes \* Configure RAO as both an ADC input and the inverting input into an internal comparator

\* Comparator will be used with internal DAC to set a current limit

\* Configure ADC clock as FRC, external +/-VREF

\* Configure clocking structure to use ECM clock mode, 16MHz clock input, 4xPLL = 64MHz SYSCLK

\* Configure RBO as EXTINTO for ZCD, and RB1 as EXTINT1 for output switching

\* Configure RD6:7 as MSSP2 IZC IO

\* Configure RE0 and RE3 as Interrupt on change inputs

\* Configure RF1 as open drain output, force low after booting

\* Configure RF0 as open drain output, force low after booting

\* Configure RF1:3 as push pull outputs, start low

\* Configure RG6 as EUSART2 RX and RG7 as EUSART2 TX

\* Configure CG6 as EUSART2 RX and RG7 as EUSART2 TX

\* Use Timer7 to gather ADC data on all channels and run calculations on it at a fixed time base

\* Use Timer6 as hearthbeat time base

\* Use Timer6 as hearthbeat time base \* Use Timer6 as hearthbeat time base Sheet: /Firmware Notes/ File: Firmware\_Notes.sch Title: Size: A Rev: Date: KiCad E.D.A. kicad (5.0.0) ld: 18/18