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Electronic Display

Marquette Universiy Senior Design 2018, Group E44 Drew Maatman, Kevin Etta, Logan Wedel, Caroline Gilger, Tuoxuan Ren

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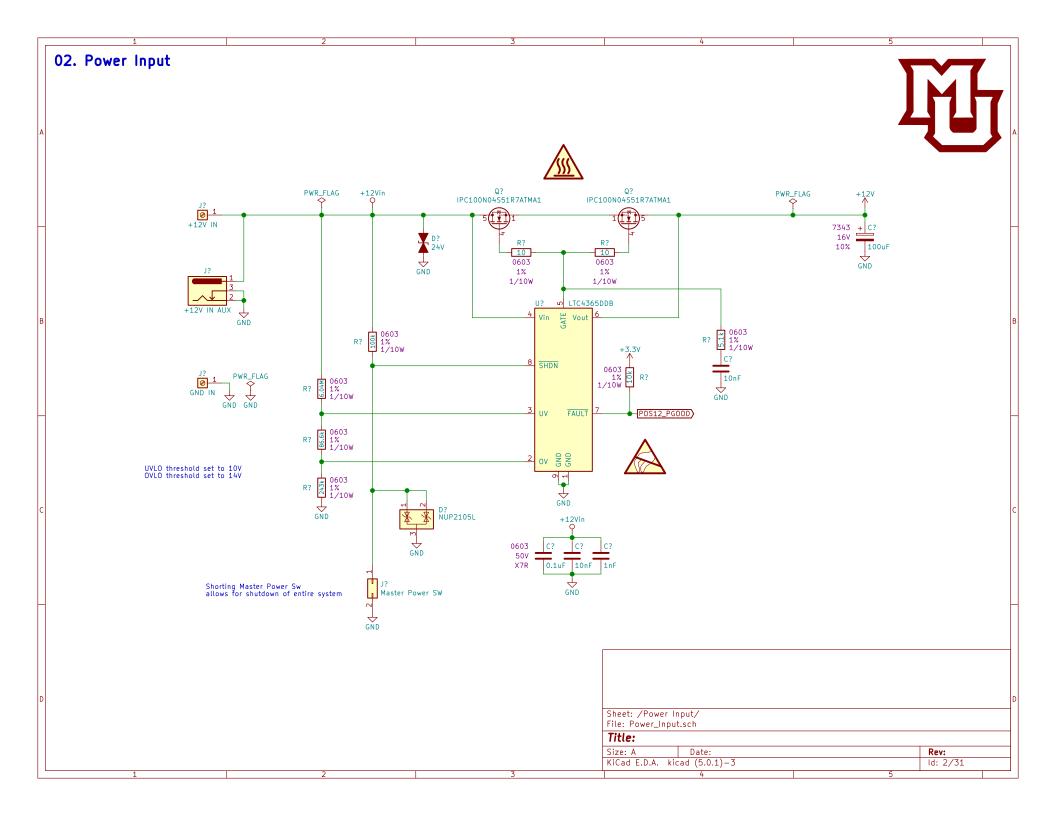
Test Points

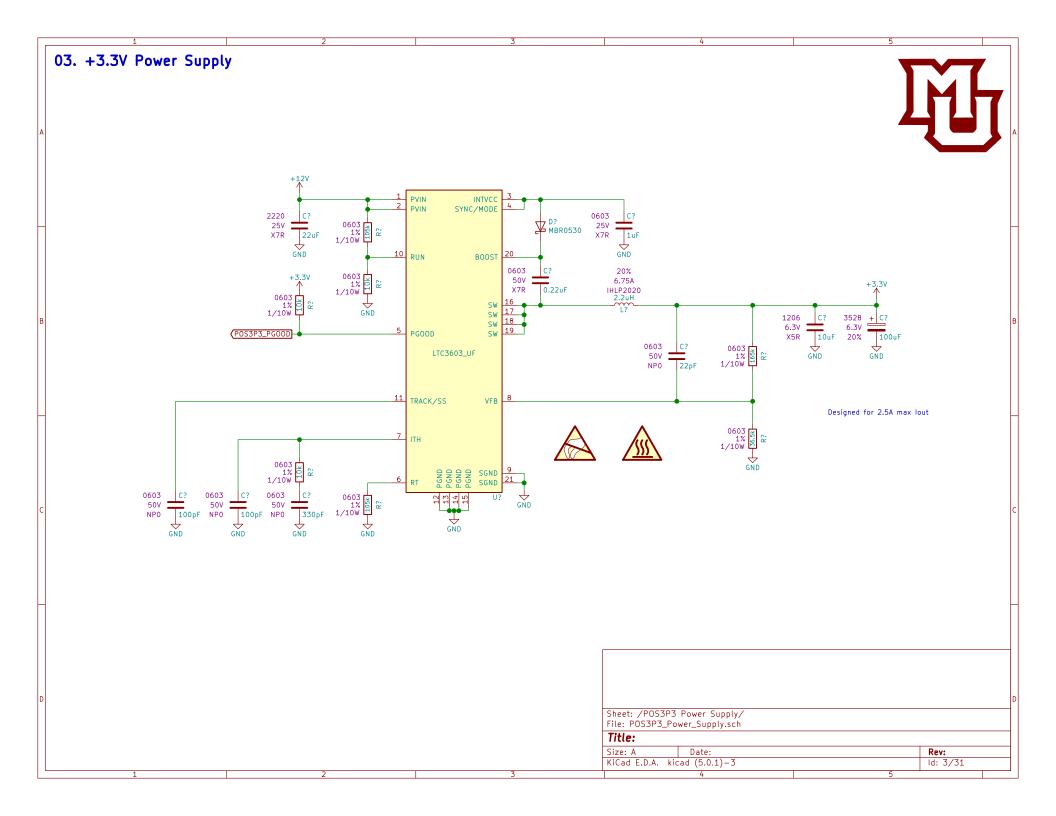
Test_Points.sch

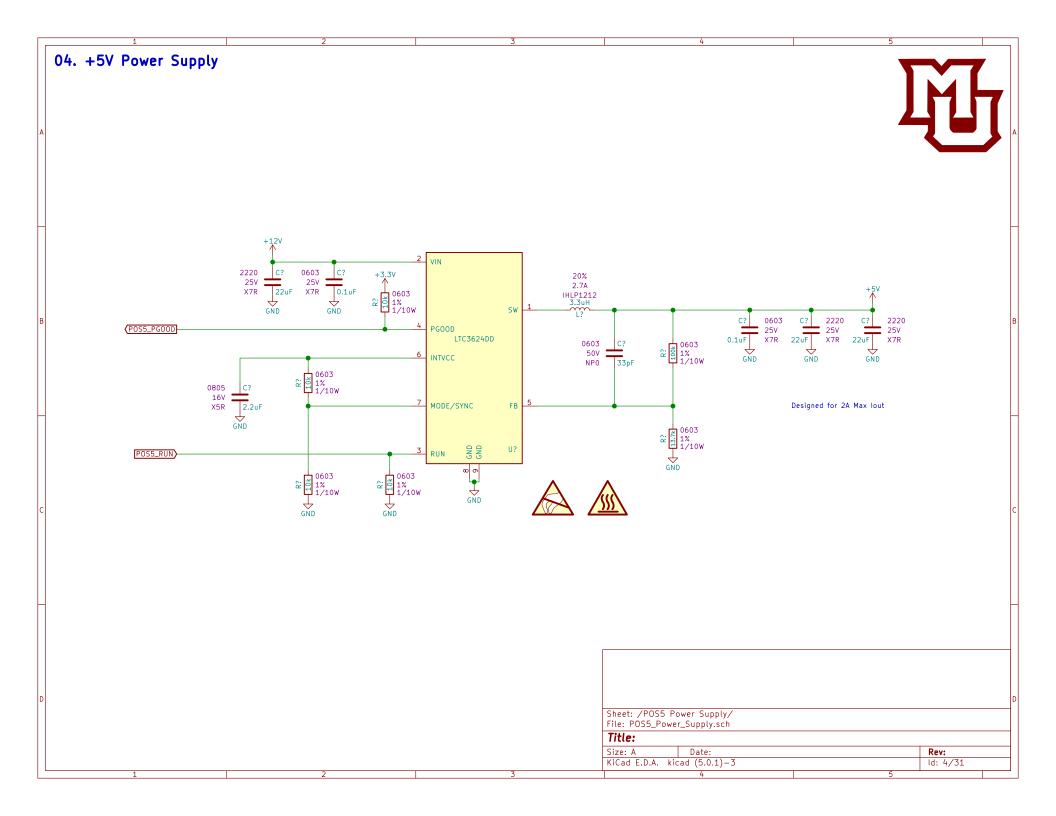
Mechanical

Mechanical.sch

To Do List: * Assign Refdes's * Verify pinouts * Verify peripheral wiring on mic * Draw custom footprints * Assign footprints * Assign footprints * Assign foigh-Key Partnumbers * Run ERC, resolve errors * Generate netlist * Generate BOM Layout PCB
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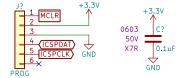


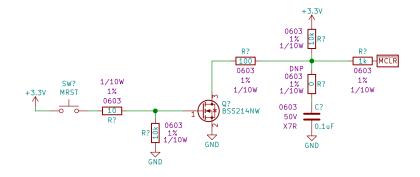




05. Microcontroller Programming







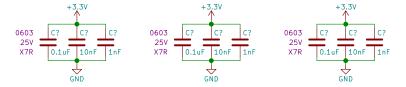
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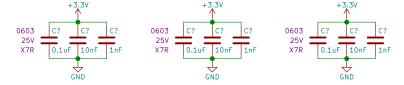
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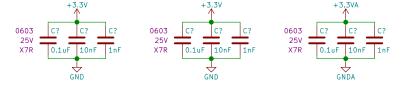
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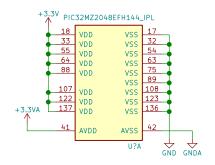
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06. Microcontroller Power













+3.3V 3528 + C? 6.3V 20% 100uF	+3.3V \(\frac{\text{L?}}{600R \ 0.5A} \) +3.3V
	GND COOR 0.5A GNDA

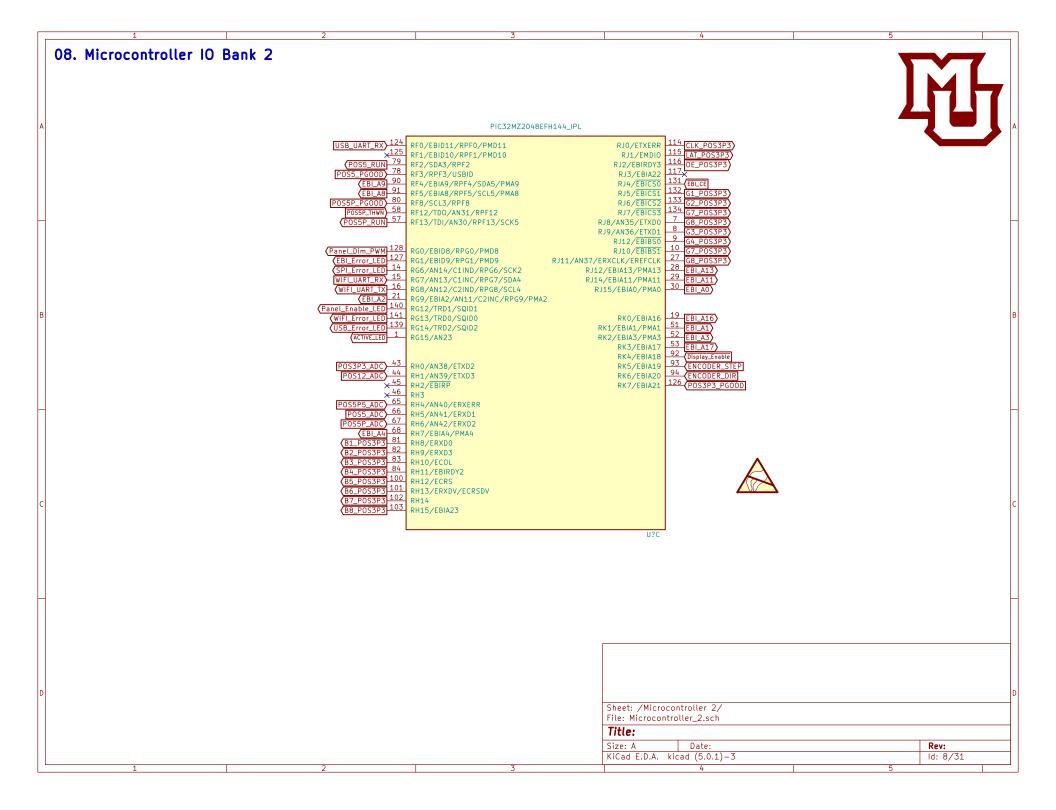
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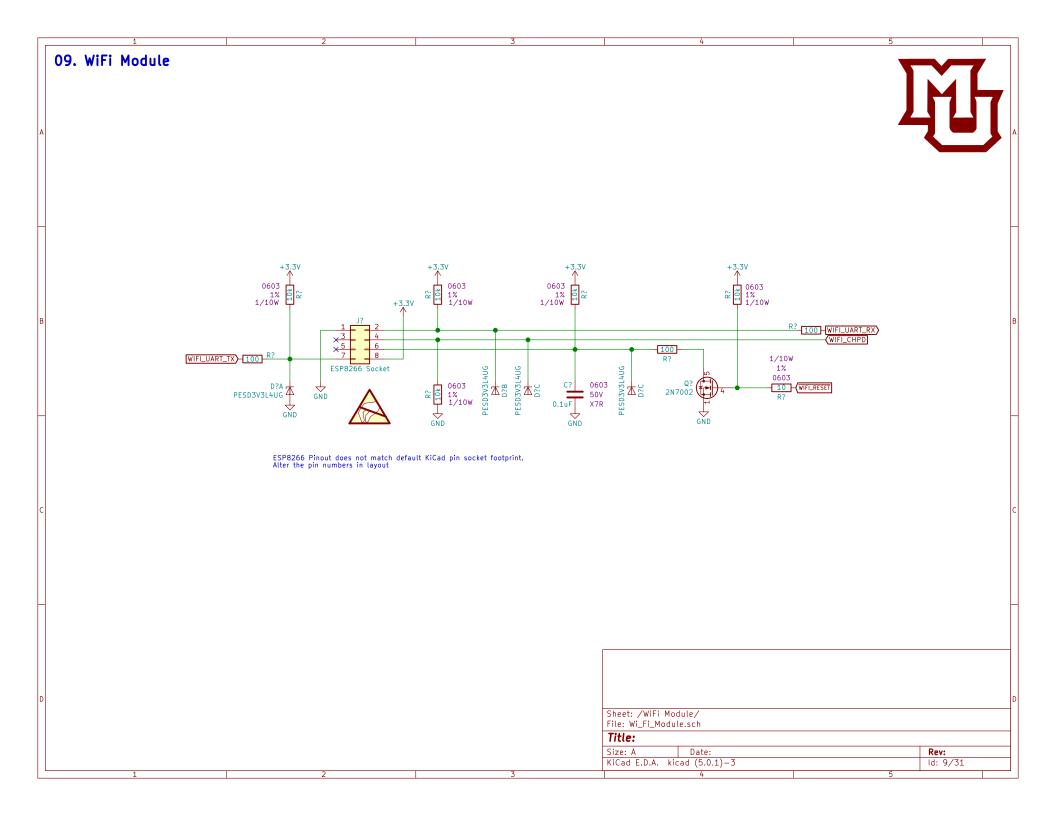
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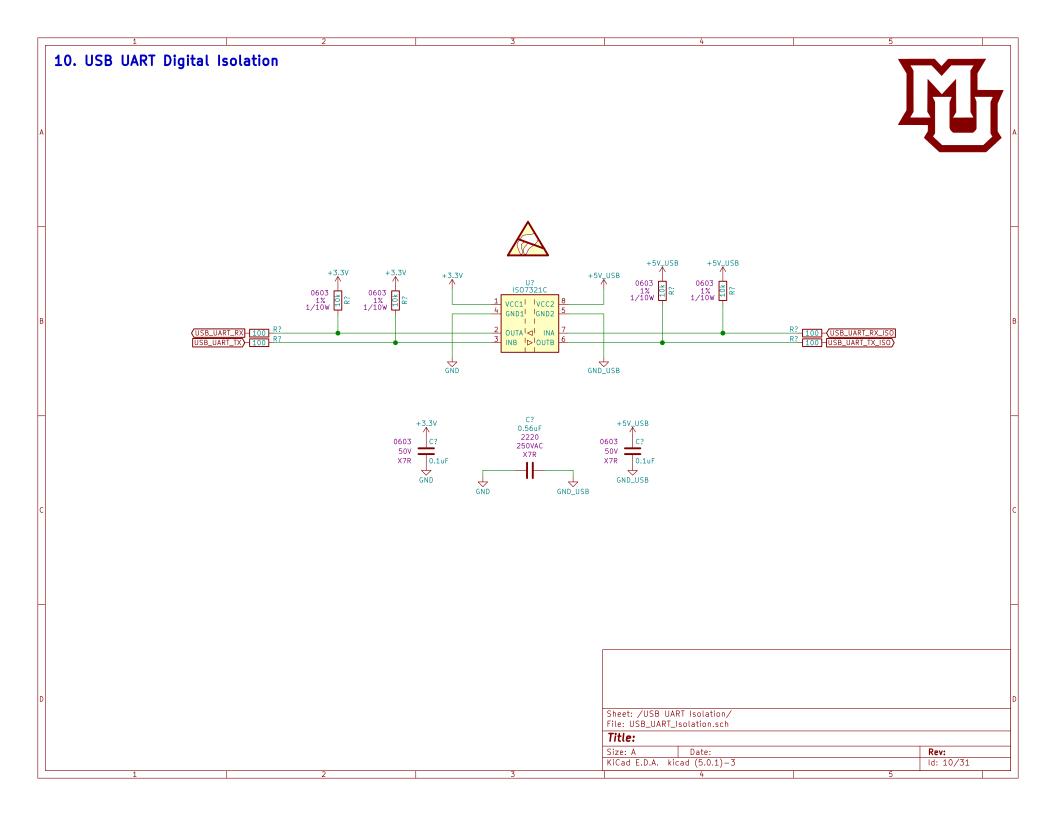
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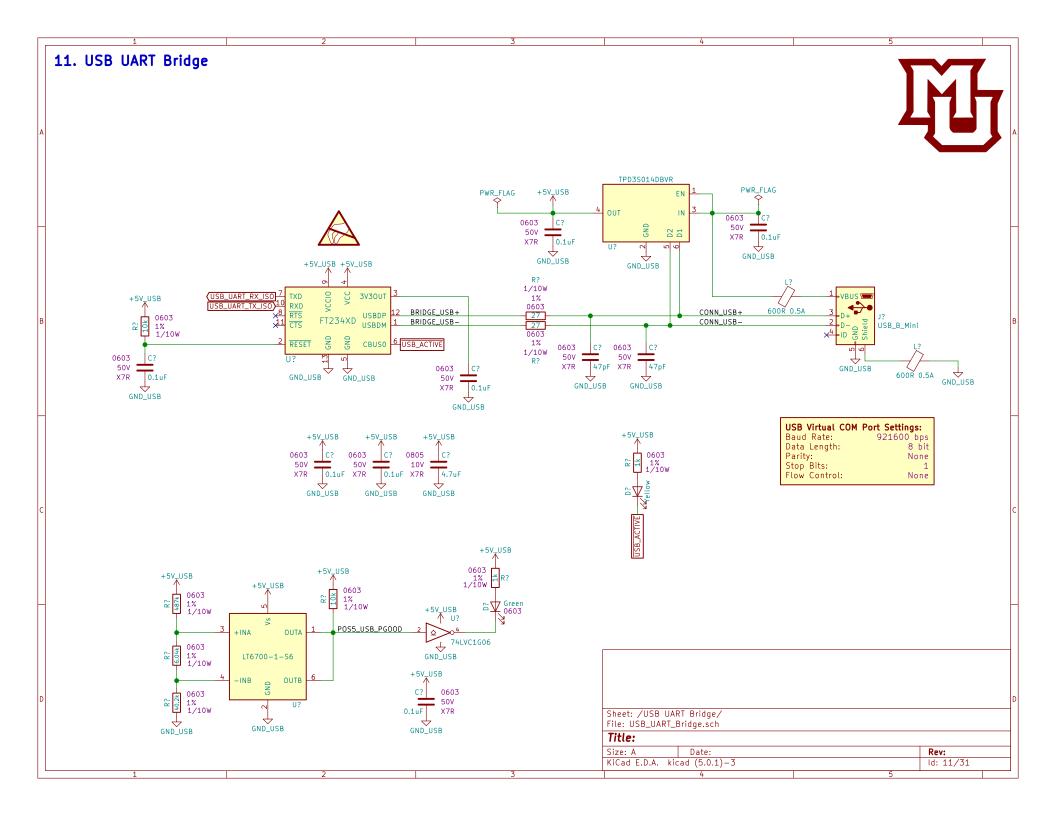
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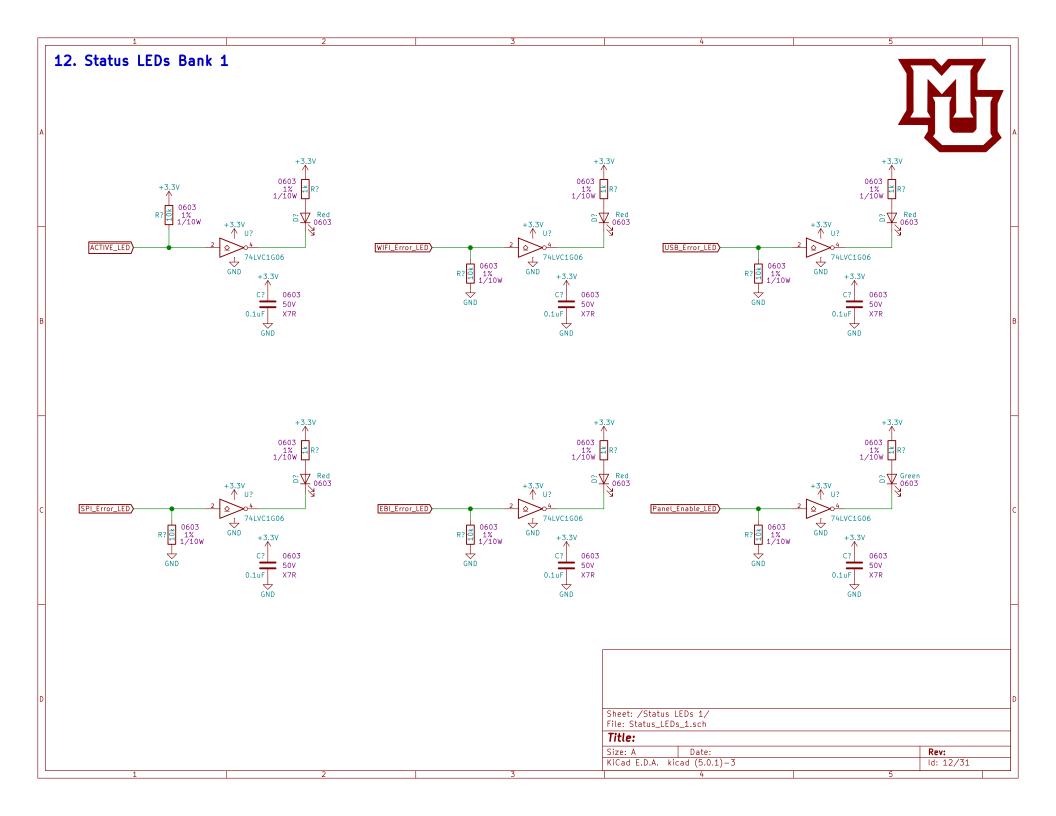
07. Microcontroller IO Bank 1 PIC32MZ2048EFH144_IPL RDO/RPDO/RTCC/INTO 104 R1_POS3P3 RAO/TMS/AN24 X 56 RA1/TCK/AN29 RD1/RPD1/SCK1 109 R2_POS3P3 FLASH_WP7 85 RA2/SCL2 RD2/EBID14/RPD2/PMD14 110 R3_POS3P3 FLASH_WP8 86 RA3/EBIRDY1/SDA2 RD3/EBID15/RPD3/PMD15 111 R4_POS3P3 (EBI_A14 87 RA4/EBIA14/PMCS1/PMA14 118 R5_P0S3 RD4/SQICSO/RPD4 (EBI_A5 2 RA5/EBIA5/AN34/PMA5 RD5/SQICS1/RPD5 119 R6_POS3P3 FLASH_WP1 129 RA6/TRCLK/SQICLK RD6/ETXEN/RPD6 120 R7_POS3P3 FLASH_WP2 130 RA7/TRD3/SQID3 RD7/ETXCLK/RPD7 121 R8_POS3P FLASH_WP3 39 RA9/VREF-/CVREF-/AN27 RD9/EBIA15/RPD9/PMCS2/PMA15 97 EBI_A15 RD10/RPD10/SCK4 98 USB_UART_TX FLASH_WP4 40 RA10/VREF+/CVREF+/AN28 FLASH_WP5 95 RA14/RPA14/SCL1 RD11/EMDC/RPD11 99 A_POS3P3 FLASH_WP6 96 RA15/RPA15/SDA1 RD12/EBID12/RPD12/PMD12 112 B_P0S3P3 RD13/EBID13/PMD13 113 C_POS3P3 RD14/AN32/RPD14 69 D_POS3P3 RD15/AN33/RPD15/SCK6 70 E_POS3P3 (ICSPDAT) 36 RB0/PGED1/AN0/RPB0 ICSPCLK) 35 RB1/PGEC1/AN1/RPB1 FLASH_CE1 34 RB2/AN2/C2INB/RPB2 FLASH_CE2 31 RB3/AN3/C2INA/RPB3 REO/EBIDO/PMDO 138 EBI_IO1 FLASH_CE3 26 RB4/AN4/C1INB RE1/EBID1/PMD1 142 EBI_102 FLASH_CE4 25 RB5/AN45/C1INA/RPB5 RE2/EBID2/PMD2 FLASH_CE5 37 RB6/PGEC2/AN46/RPB6 RE3/EBID3/RPE3/PMD3 EBI 103 FLASH_CE6 38 RB7/PGED2/AN47/RPB7 RE4/EBID4/AN18/PMD4 EBLA10 47 RB8/EBIA10/AN48/RPB8/PMA10 RB9/EBIA7/AN49/RPB9/PMA7 RE5/EBID5/AN17/RPE5/PMD5 EBI 105 RE6/EBID6/AN16/PMD6 **√**EBI_106 FLASH_CEF 50 RB10/CVREFOUT/AN5/RPB10 FLASH_CEE 50 RB11/AN6 RE7/EBID7/AN15/PMD7 EBI_107 RE8/AN25/RPE8 FLASH_Hold 59 RB12/AN7 24 WIFI_RESET RE9/AN26/RPE9 × 60 RB13/AN8 FLASH_SCK 61 RB14/AN9/RPB14/SCK3 X 62 RB15/AN10/RPB15/OCFB EBI_A6 RC1/EBIA6/AN22/RPC1/PMA6 (EBLA12 11 RC2/EBIA12/AN21/RPC2/PMA12 (EBI_WE 12 RC3/EBIWE/AN20/RPC3/PMWR EBI_OE 13 RC4/EBIOE/AN19/RPC4/PMRD ×71 RC12/0SC1/CLKI VBUS FLASH_MISO 106 VUSB3V3 76 × D-X 72 RC15/0SC2/CLK0 Sheet: /Microcontroller 1/ File: Microcontroller_1.sch Title: Size: A Date: Rev: KiCad E.D.A. kicad (5.0.1)-3ld: 7/31

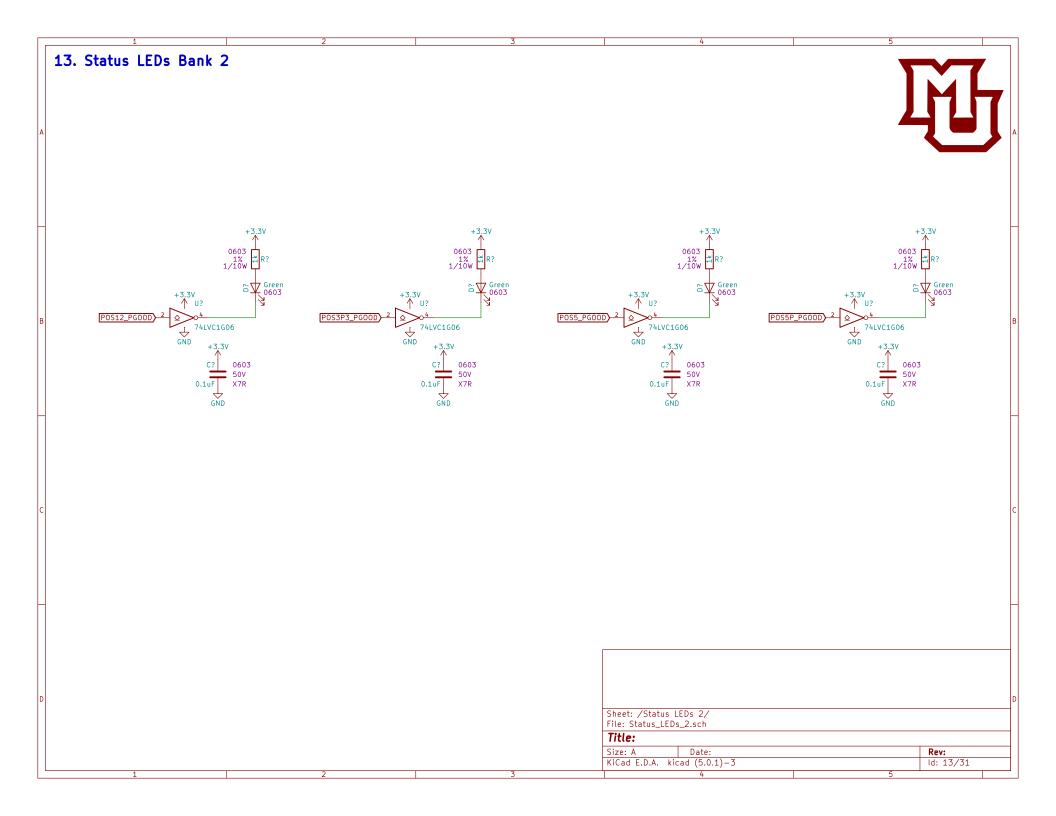


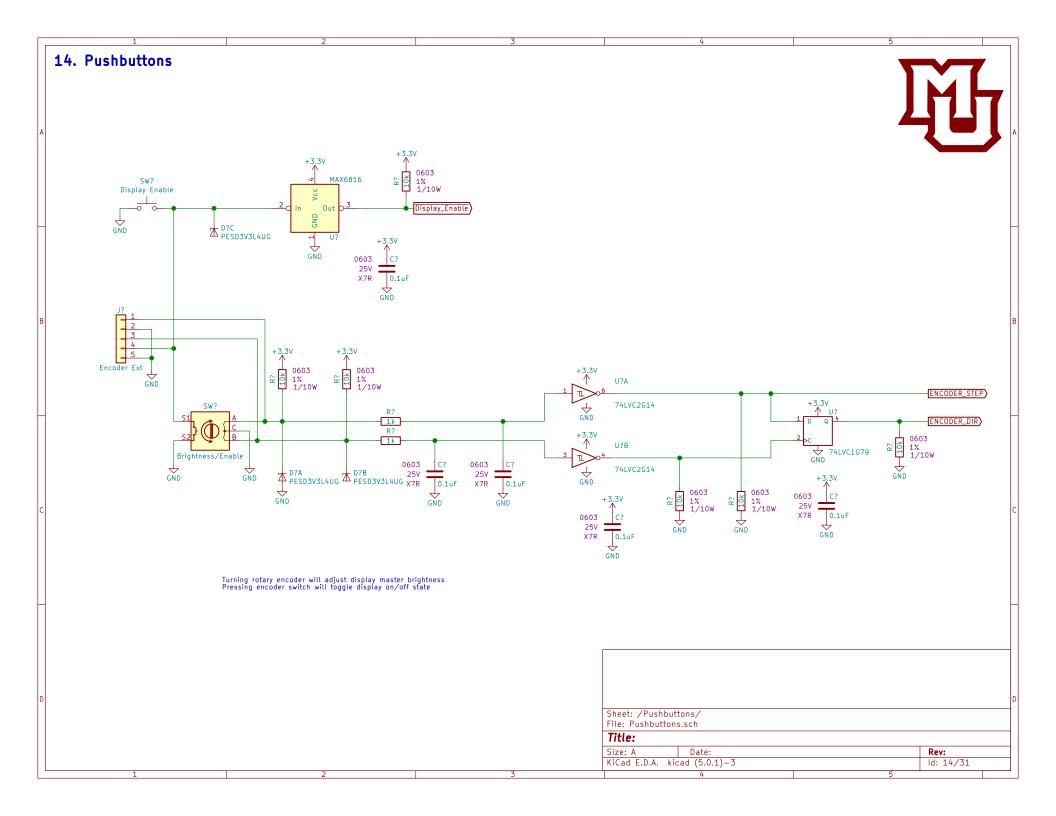






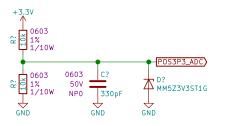


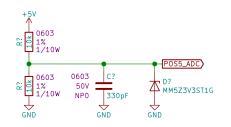


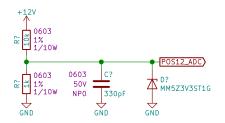


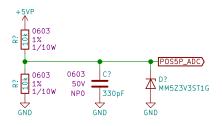
15. Internal Rail Monitoring

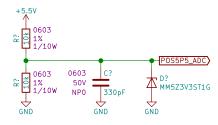












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