Sheet: +12V Input

File: POS12_Input.sch

Sheet: +3.3V Power Supply

File: POS3P3_Power_Supply.sch

Sheet: PIC32MZ Programming

File: PIC32MZ_Programming.sch

Sheet: PIC32MZ Bypass

File: PIC32MZ_Bypass.sch

Sheet: PIC32MZ

File: PIC32MZ.sch

Sheet: PIC32MZ Clocking

File: PIC32MZ_Clocking.sch

Sheet: Ethernet PHY

File: Ethernet_PHY.sch

Sheet: Ethernet Magnetics

File: Ethernet_Magnetics.sch

Sheet: GPIO

File: GPIO.sch

Sheet: USB UART Isolation

File: USB_UART_Isolation.sch

Sheet: USB UART Bridge

File: USB_UART_Bridge.sch

Sheet: PGOOD LEDs

File: PGOOD_LEDs.sch

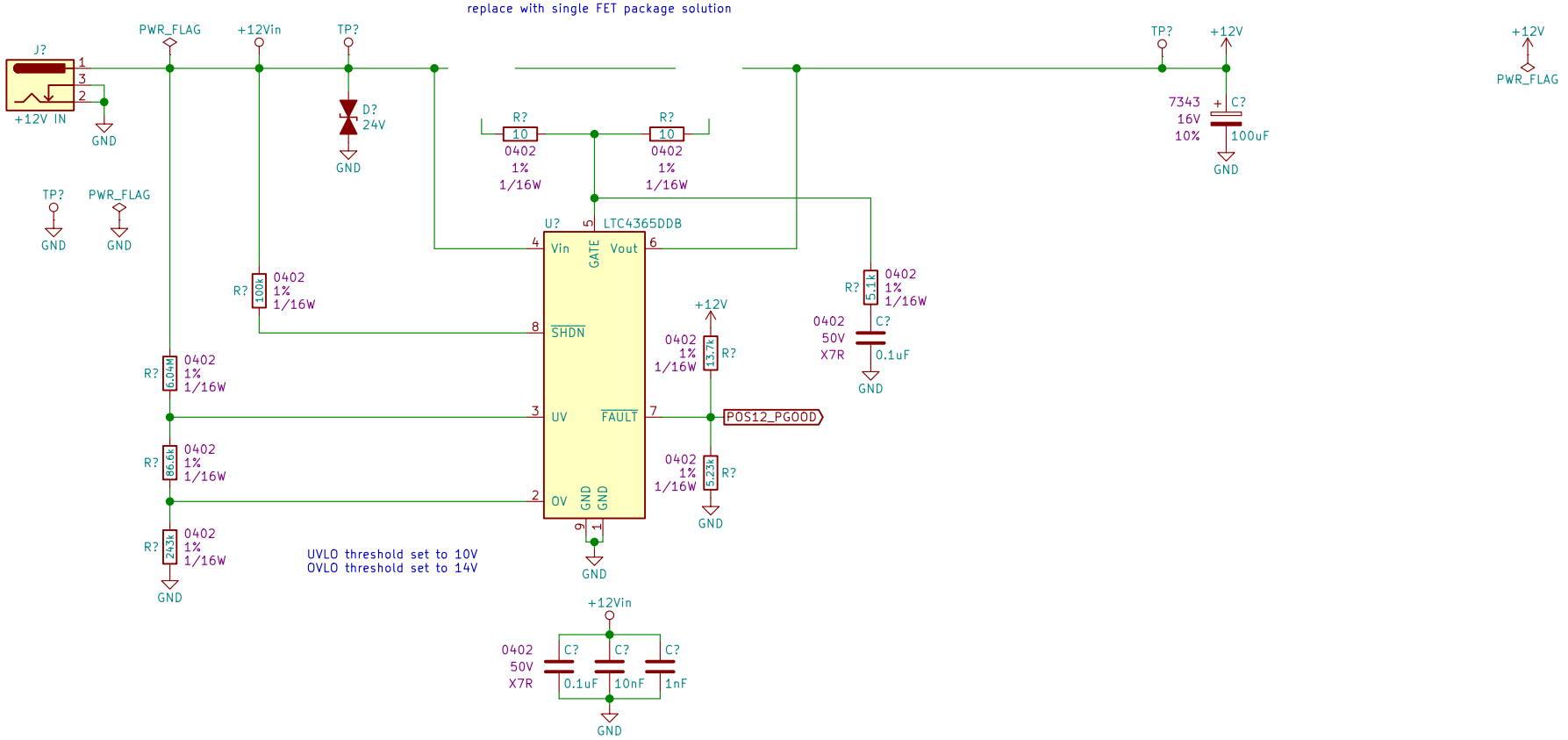
Sheet: Status LEDs

File: Status_LEDs.sch

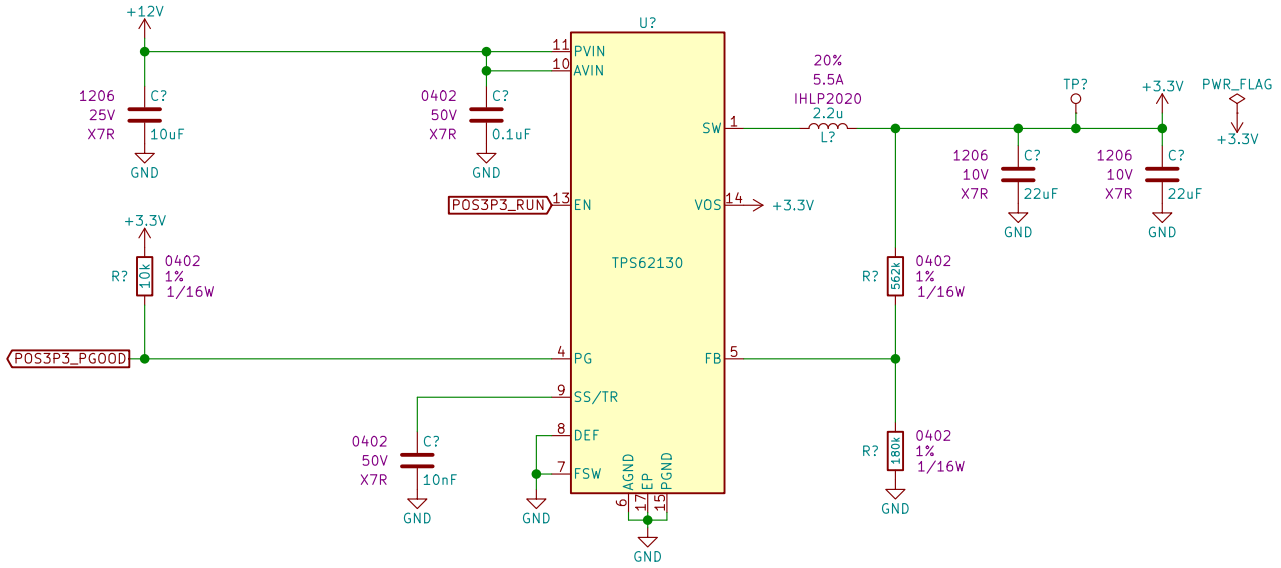
Sheet: Mechanical

File: Mechanical.sch

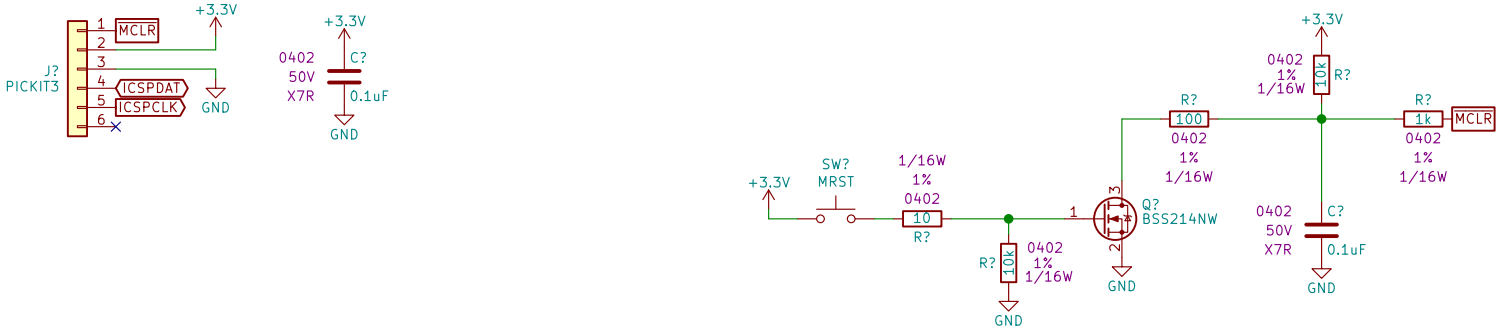
Sheet: / File: PIC32MZ_ETH_Demo.sch		
Title:		
Size: A	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.4)-1		Id: 1/15



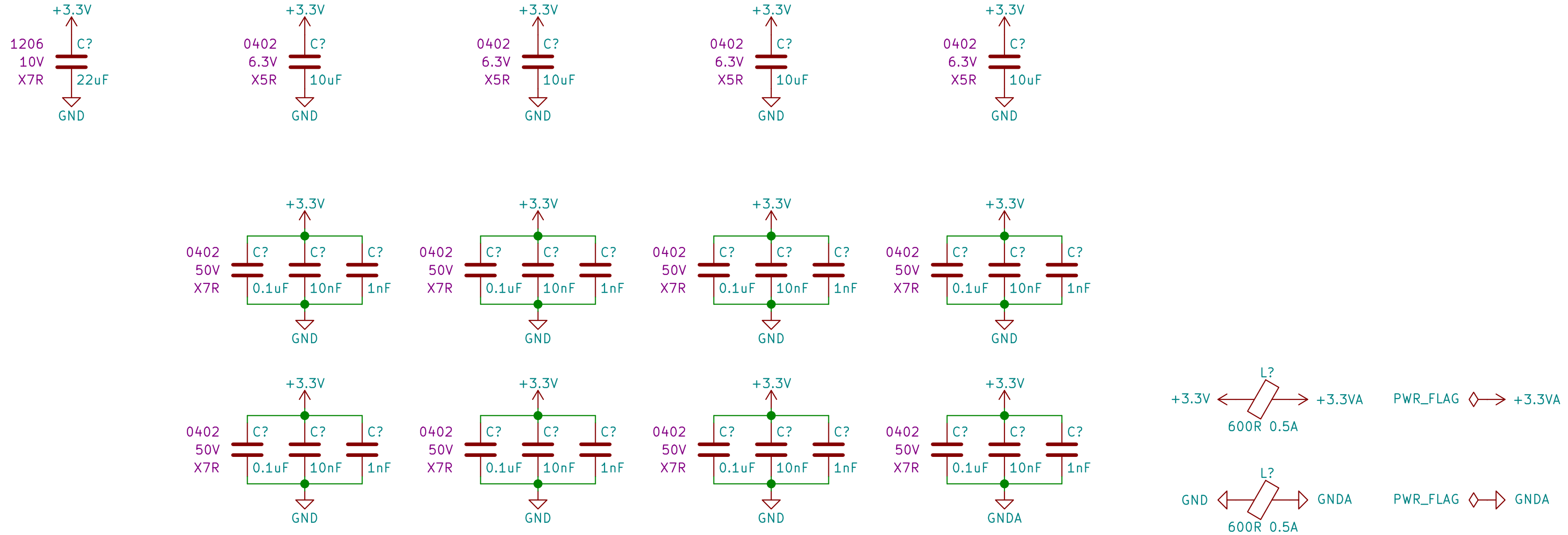
Sheet: /+12V Input/		
File: POS12_Input.sch		
Title: QI Charger		
Size: A	Date: 2019-01-03	**Rev: A**
KiCad E.D.A. kicad (5.1.4)-1		Id: 2/15



Sheet: /+3.3V Power Supply/		
File: POS3P3_Power_Supply.sch		
Title: QI Charger		
Size: A	Date: 2019-01-03	**Rev: A**
KiCad E.D.A. kicad (5.1.4)-1		Id: 3/15



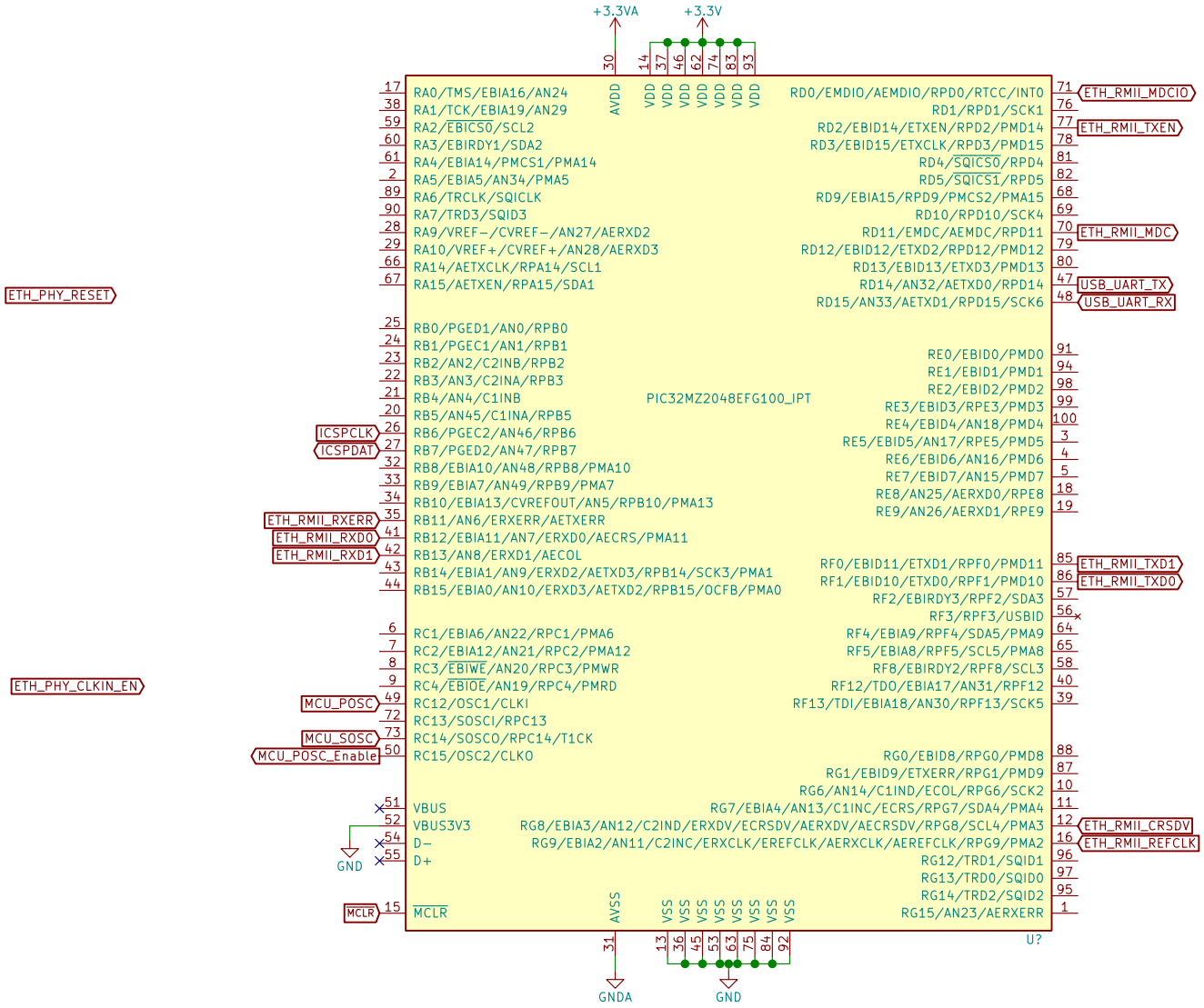
Drew Maatman		
Sheet: /PIC32MZ Programming/		
File: PIC32MZ_Programming.sch		
Title: VFD Clock		
Size: A	Date: 2019-04-11	**Rev: A**
KiCad E.D.A. kicad (5.1.4)-1		Id: 4/15



Sheet: /PIC32MZ Bypass/
File: PIC32MZ_Bypass.sch

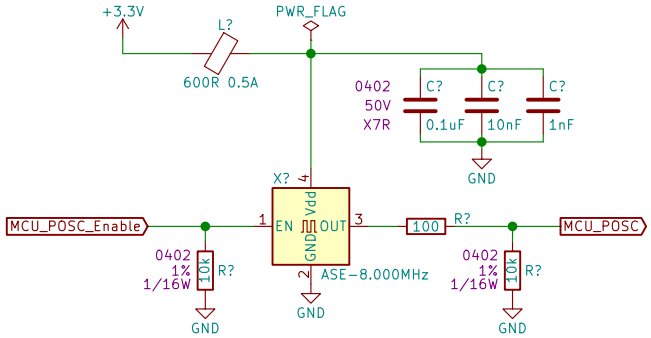
Title:

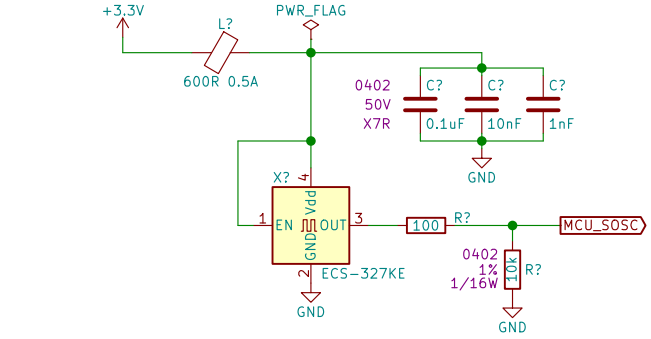
Size: A	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.4)-1		Id: 5/15



Drew Maatman	
Sheet: /PIC32MZ/	
File: PIC32MZ.sch	
Title: VFD Clock	
Size: A	Date: 2019-04-11
KiCad E.D.A. kicad (5.1.4)-1	Id: 6/15

Rev: A





Sheet: /PIC32MZ Clocking/ File: PIC32MZ_Clocking.sch		
Title:		
Size: A	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.4)-1		Id: 7/15

Sequnce this to turn on within 50mS of +3.3V (hard strap, not GPIO driven)

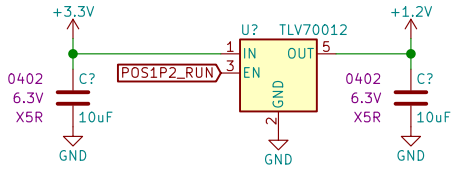
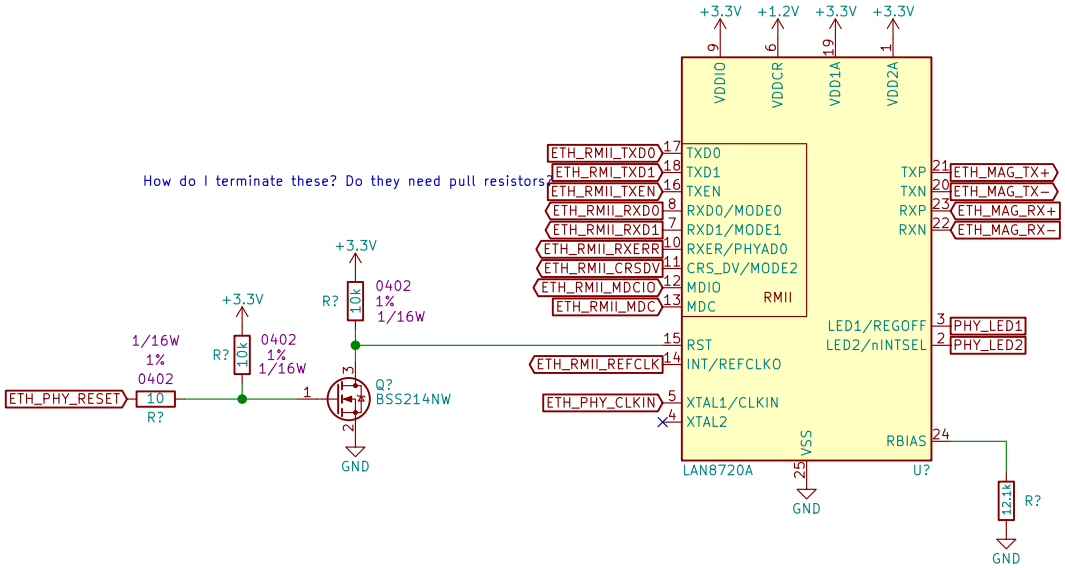
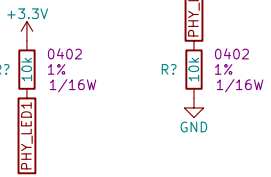
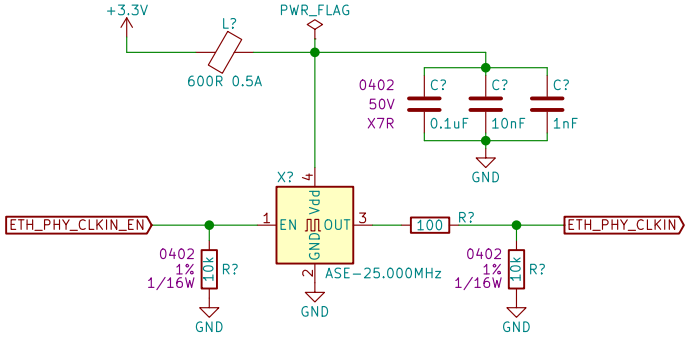


Figure out bypass

How do I terminate these? Do they need pull resistors?



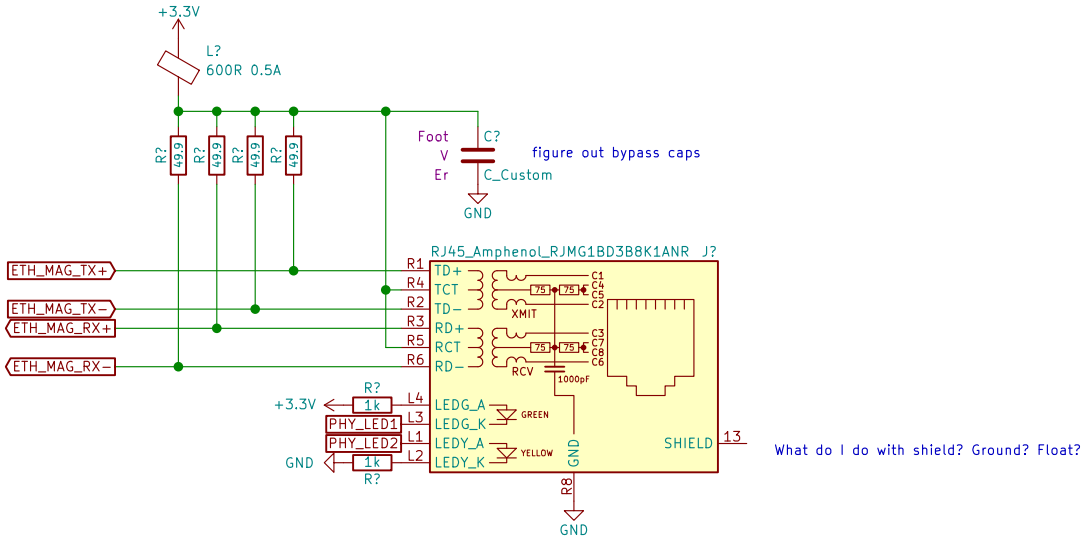




The pin strap settings on LED1/LED2 disable the internal
+1.2V core voltage regulator and enable REFCLK output

The LED1 output is driven active whenever the device detects a valid link,
and blinks when CRS is active (high) indicating activity.
The LED2 output is driven active when the operating speed is 100Mbps.
This LED will go inactive when the operating
speed is 10Mbps or during line isolation.

Sheet: /Ethernet PHY/		
File: Ethernet_PHY.sch		
Title:		
Size: A	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.4)−1		Id: 8/15

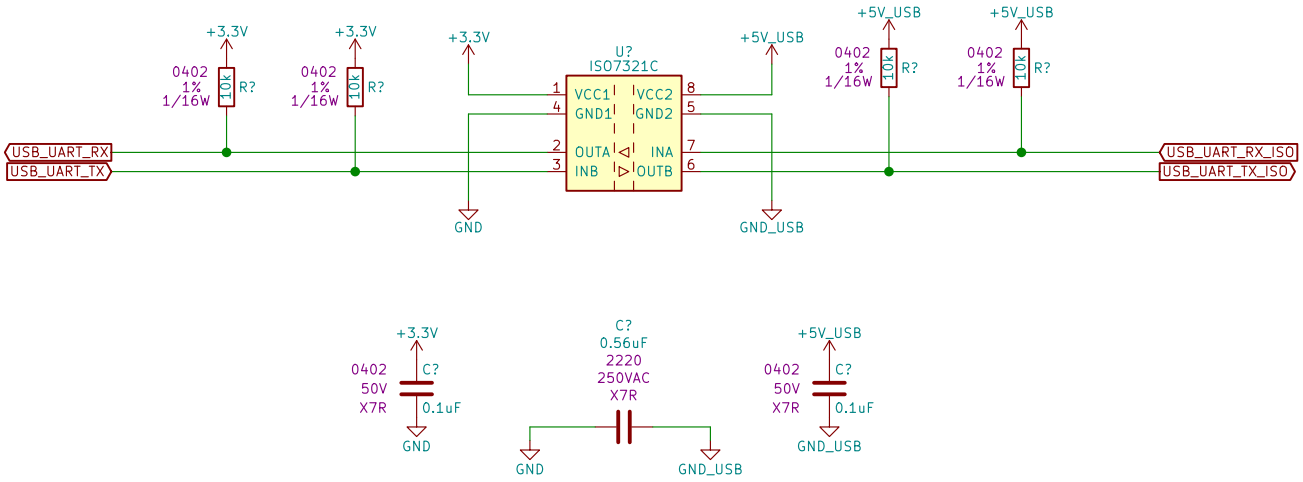


Sheet: /Ethernet Magnetics/		
File: Ethernet_Magnetics.sch		
Title:		
Size: A	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.4)−1		Id: 9/15

Sheet: /GPIO/
File: GPIO.sch

Title:

Size: A	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.4)-1		Id: 10/15



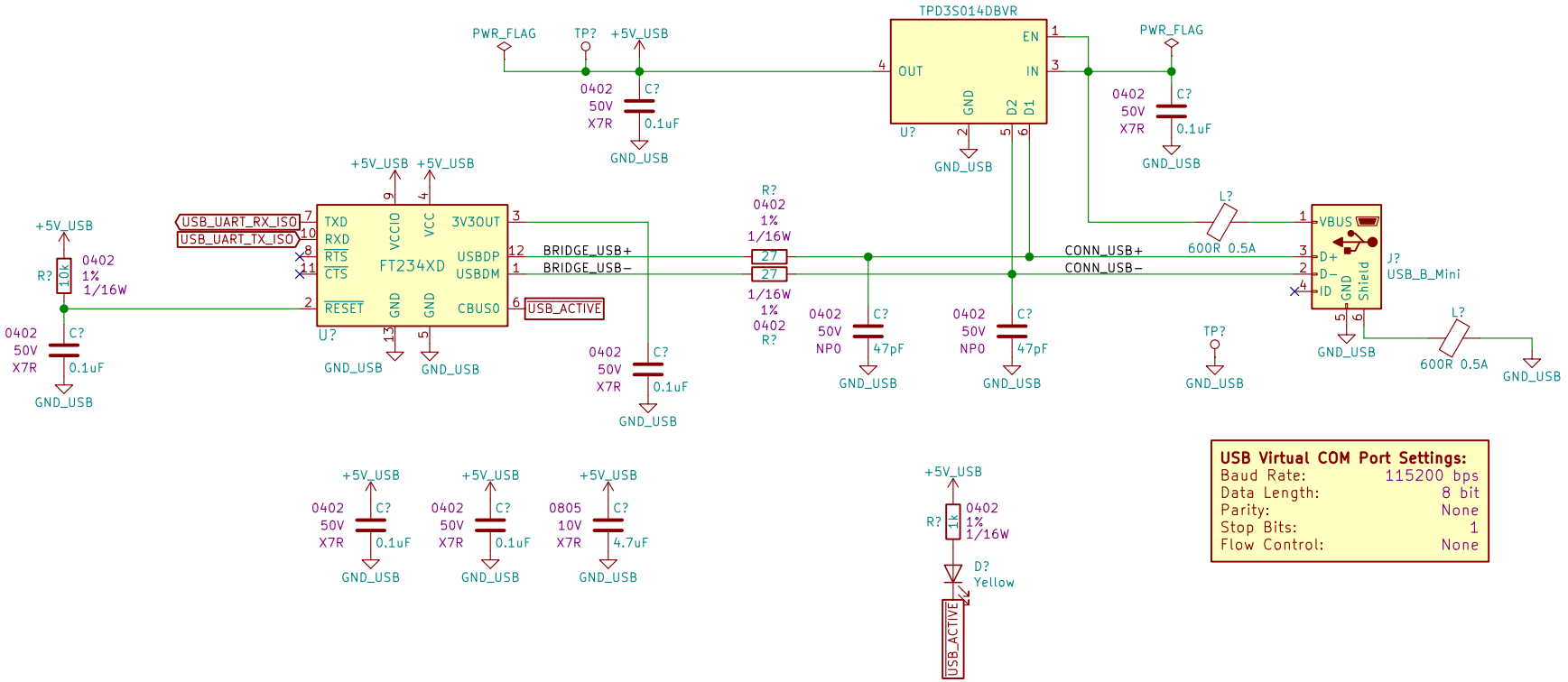
Drew Maatman

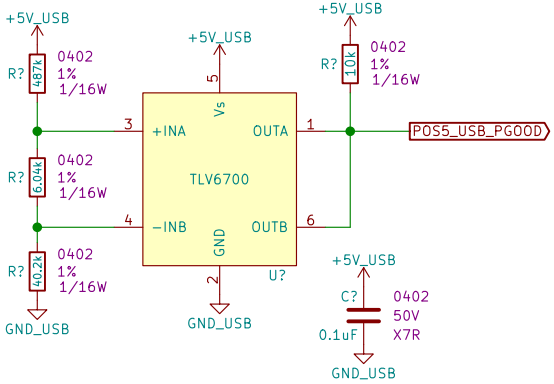
Sheet: /USB UART Isolation/
File: USB_UART_Isolation.sch

Title: VFD Clock

Size: A	Date: 2019-04-11	**Rev: A**
KiCad E.D.A. kicad (5.1.4)-1		Id: 11/15

11. USB UART Bridge





USB Virtual COM Port Settings:

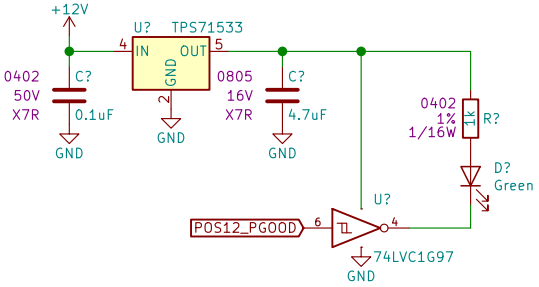
Baud Rate:	115200 bps
Data Length:	8 bit
Parity:	None
Stop Bits:	1
Flow Control:	None

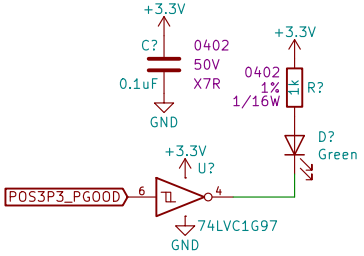
Drew Maatman

Sheet: /USB UART Bridge/
File: USB_UART_Bridge.sch

Title: VFD Clock

Size: A	Date: 2019-04-11	**Rev: A**
KiCad E.D.A. kicad (5.1.4)-1		Id: 12/15

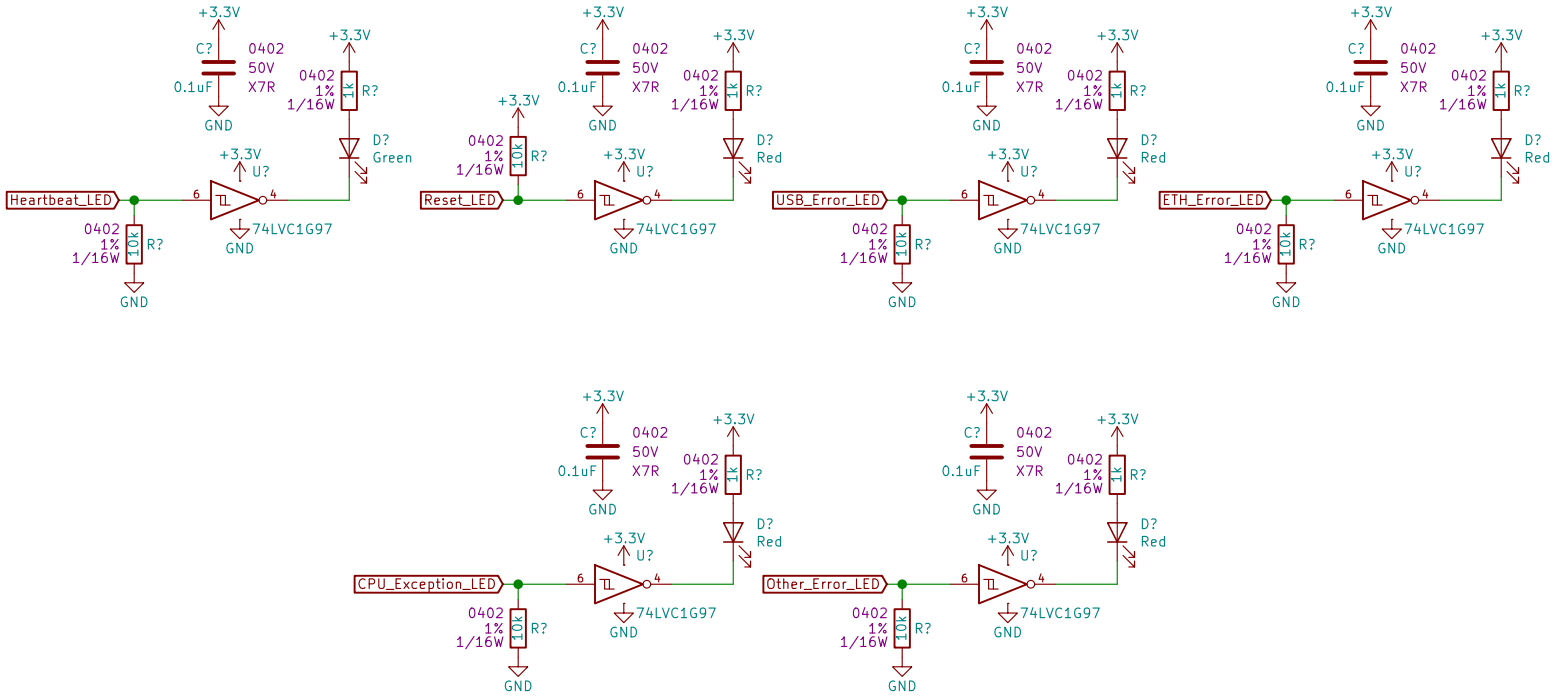




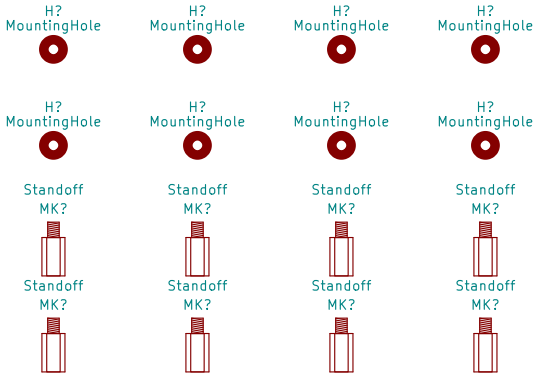
Sheet: /PGOOD LEDs/
File: PGOOD_LEDs.sch

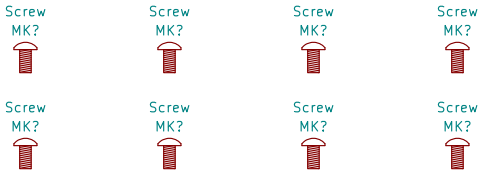
Title:

Size: A	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.4)-1		Id: 13/15



Sheet: /Status LEDs/		
File: Status_LEDs.sch		
Title:		
Size: A	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.4)-1		Id: 14/15





Drew Maatman		
Sheet: /Mechanical/		
File: Mechanical.sch		
Title: VFD Clock		
Size: A	Date: 2019-04-11	**Rev: A**
KiCad E.D.A. kicad (5.1.4)-1		Id: 15/15