

USB Hub

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+12V Telemetry



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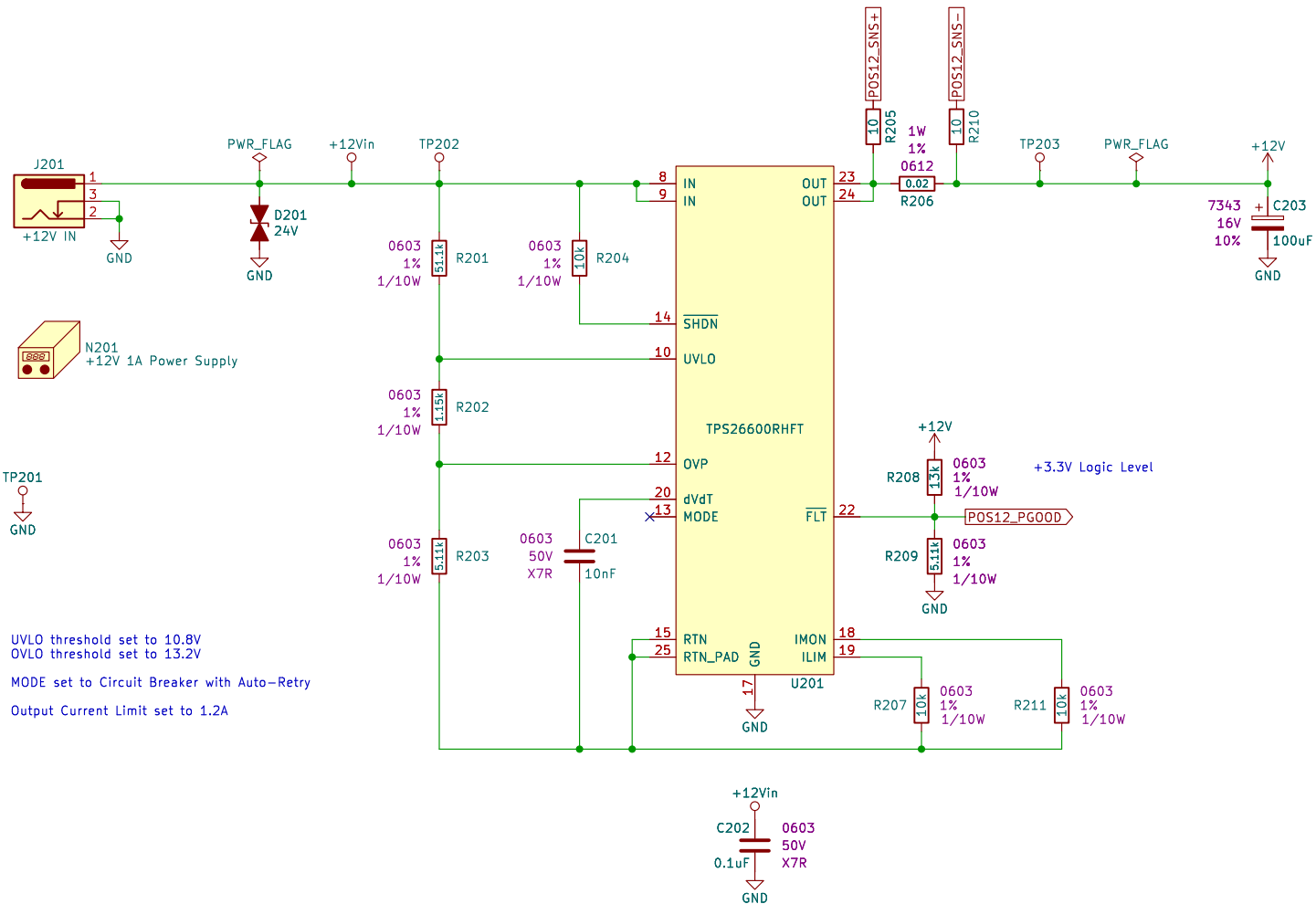
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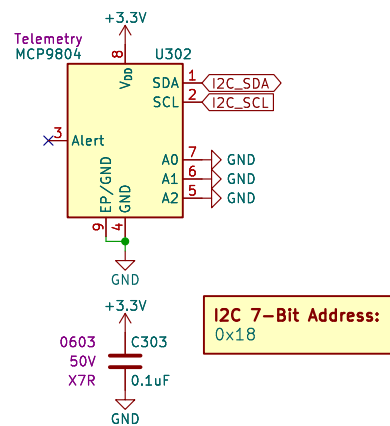
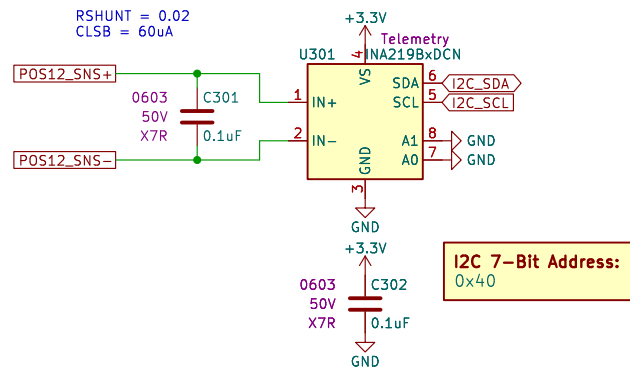
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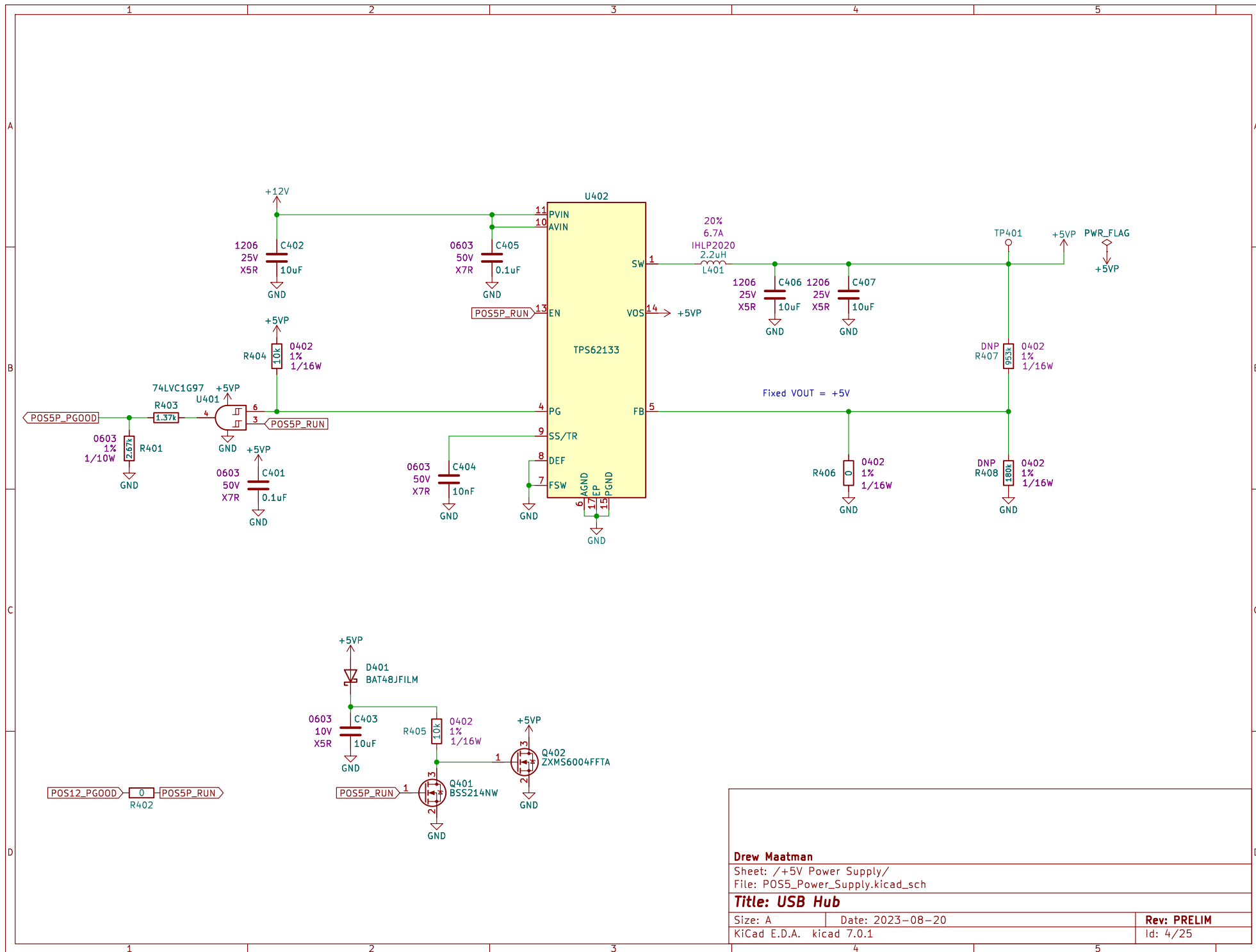
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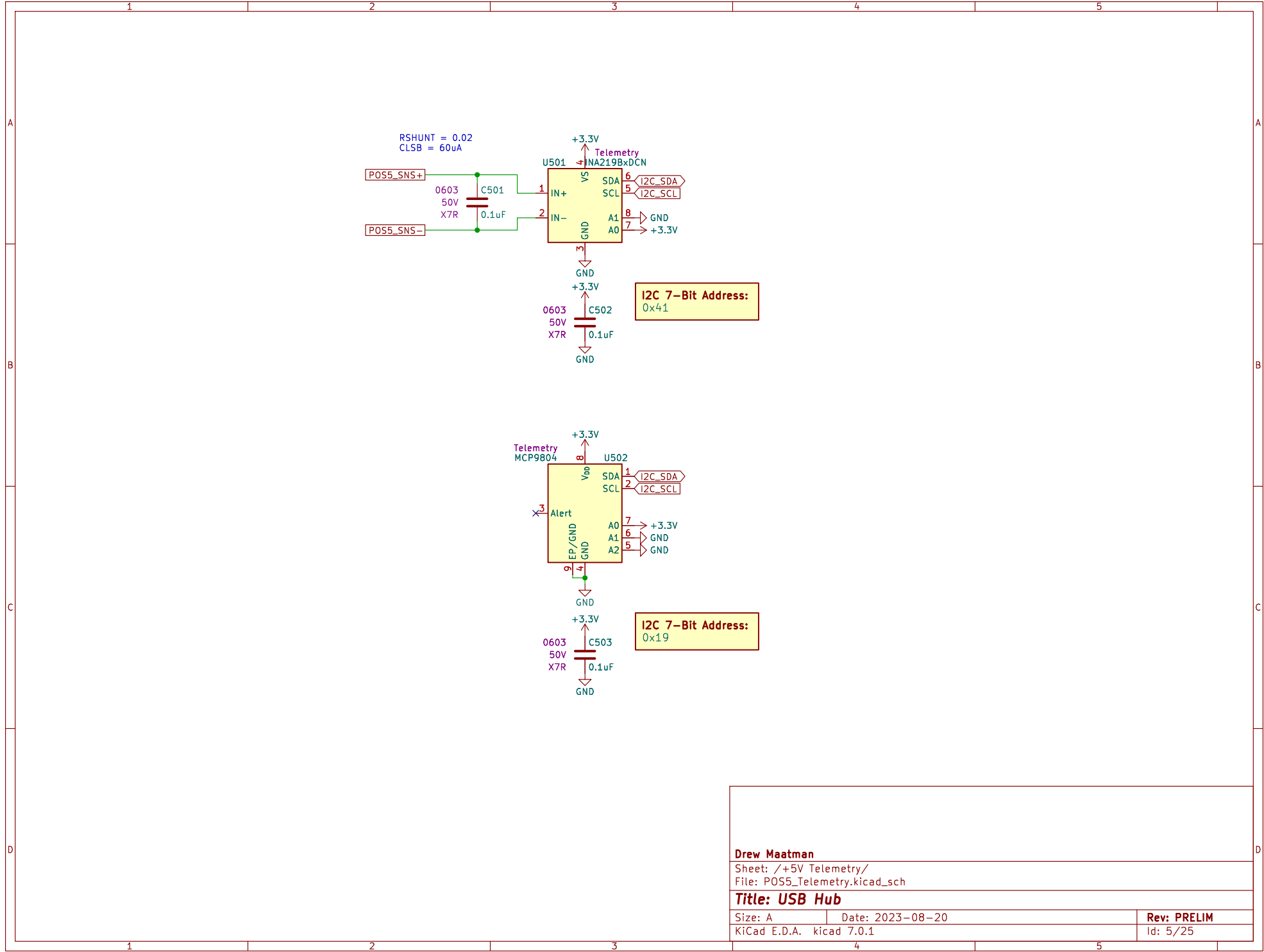
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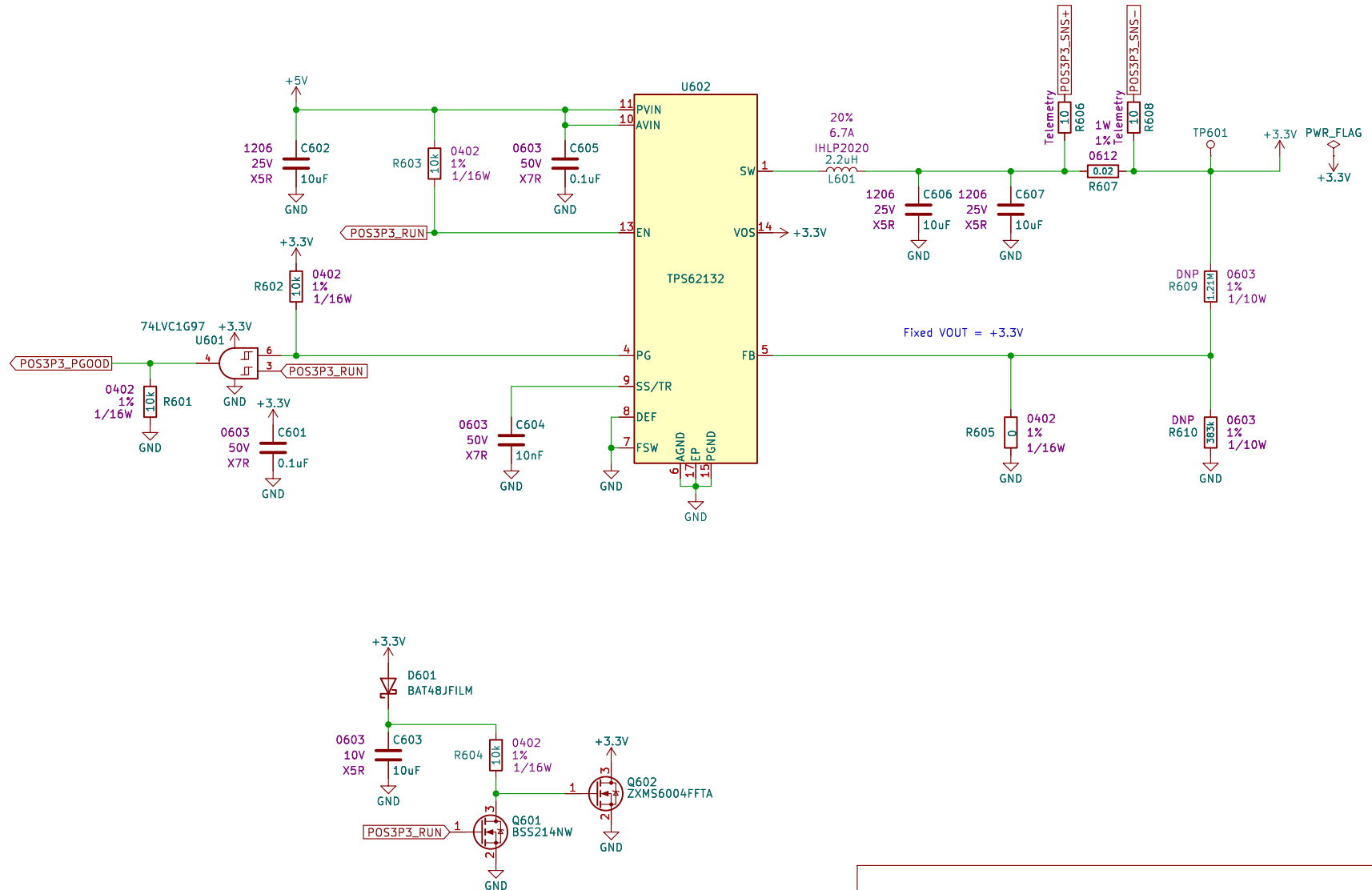
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DOUBLE CHECK POS3P3_RUN VOLTAGE LEVEL TOLERANCE



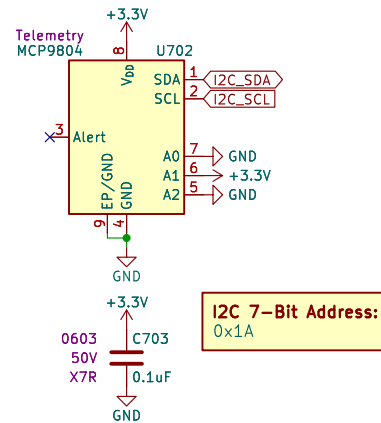
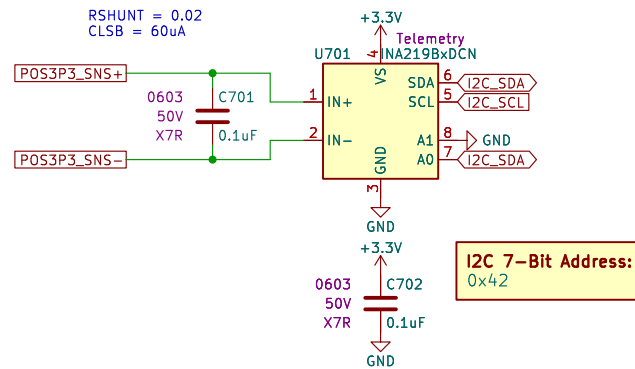
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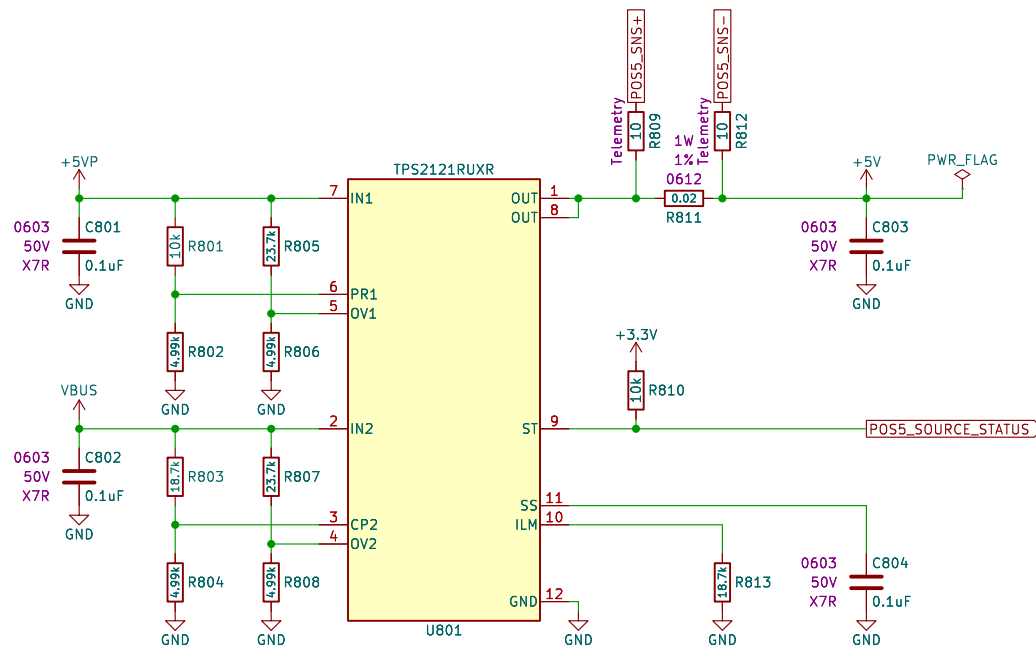
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OVP ~ 6V on IN1, IN2
IN1 is prioritized unless IN1 is under PR1 threshold

Output current limit = 5.2A (4.6A min, 5.8A max)
Output slew rate = 780mV/ms

ST High when wall wart powering output,
low when upstream VBUS powering output.
This should not change during operation

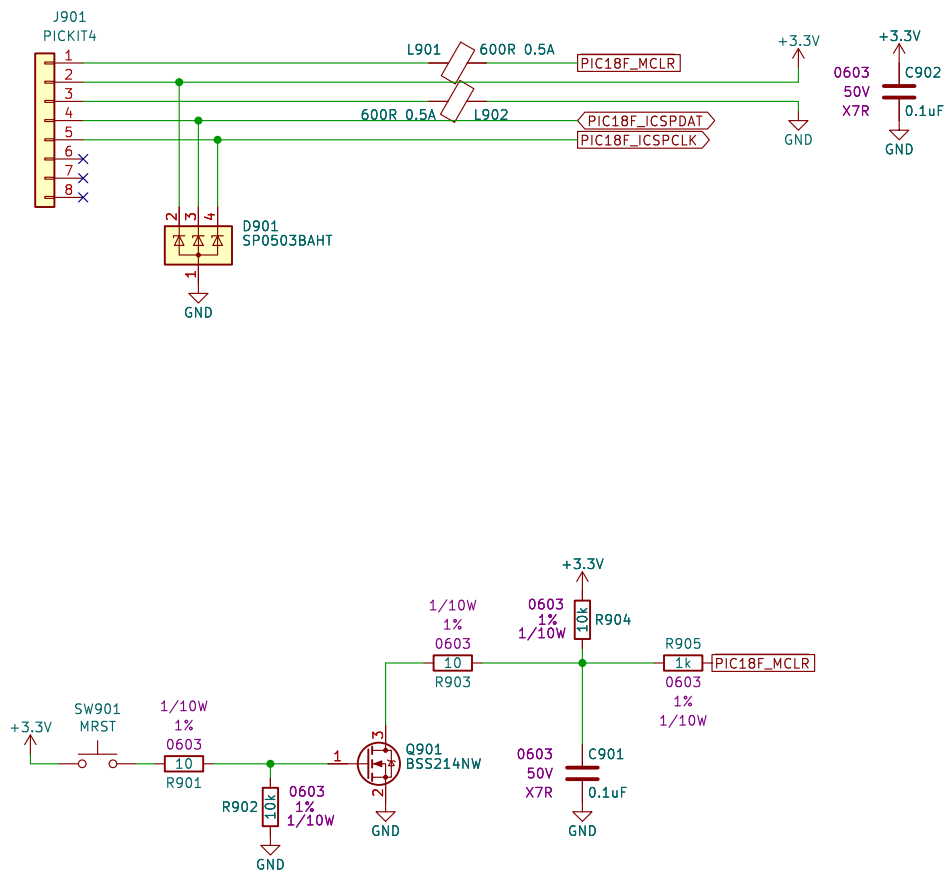
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Sheet: /Power ORing/
File: power_oring.kicad_sch

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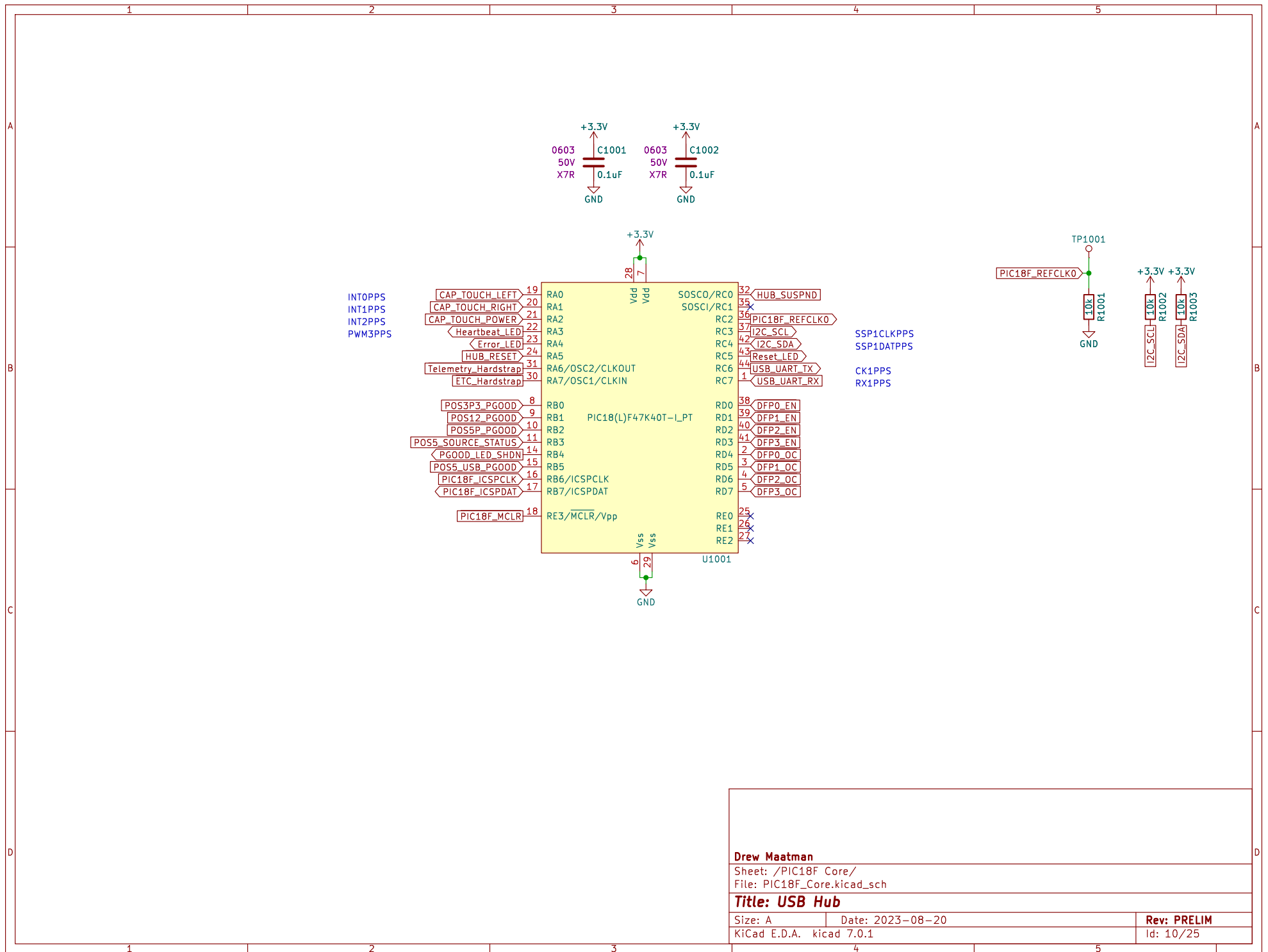
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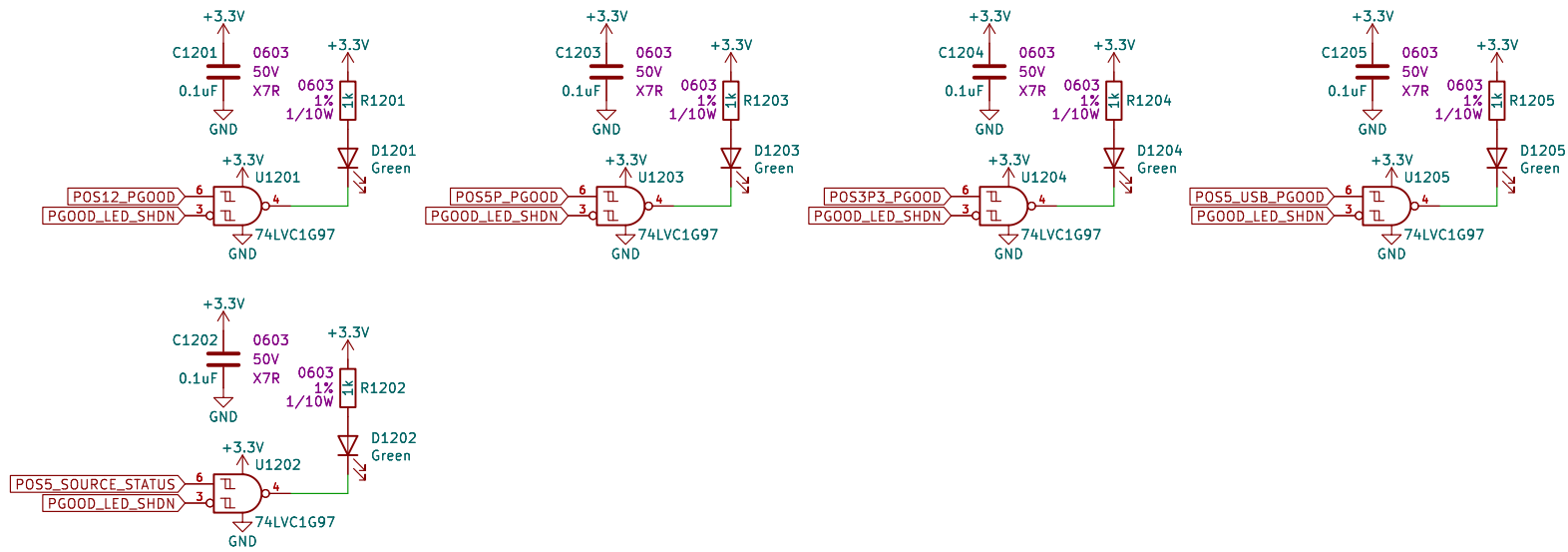
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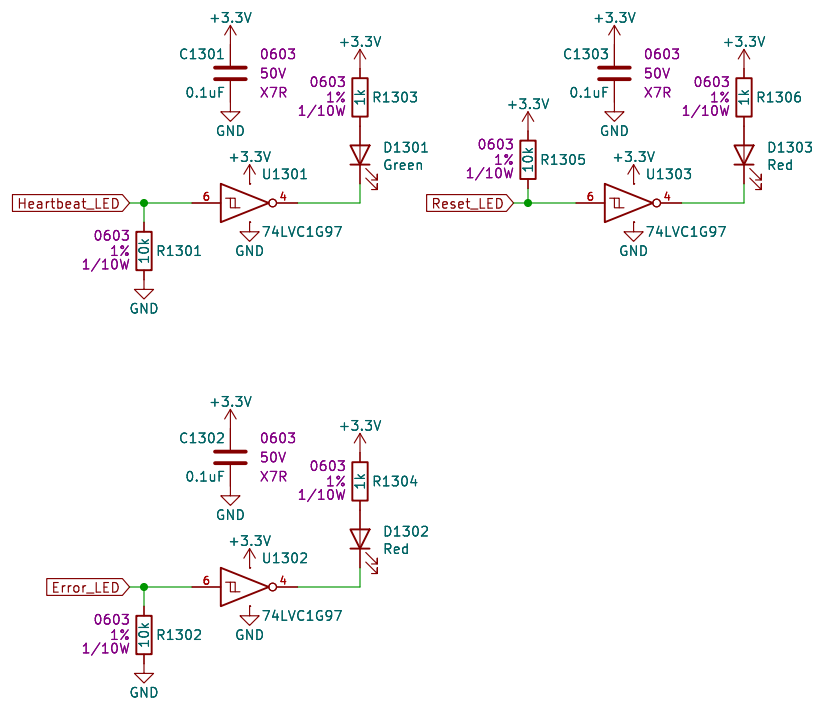
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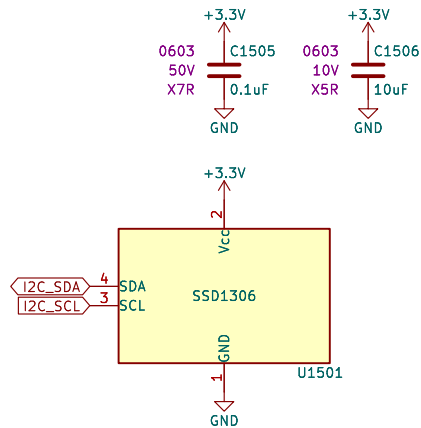
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File: Status_LEDs.kicad_sch

Title: USB Hub

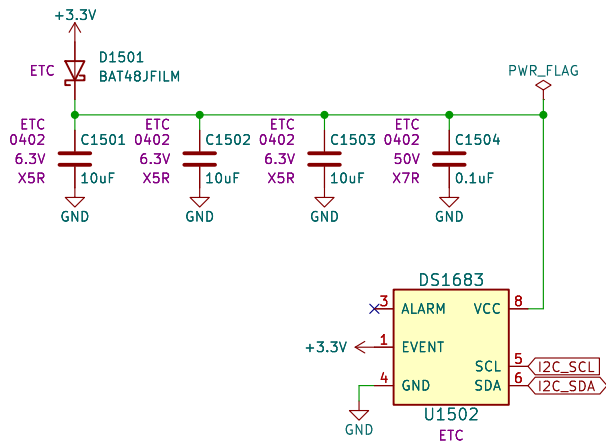
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I2C 7-Bit Address:
0x3C



I2C 7-Bit Address:
0x6B

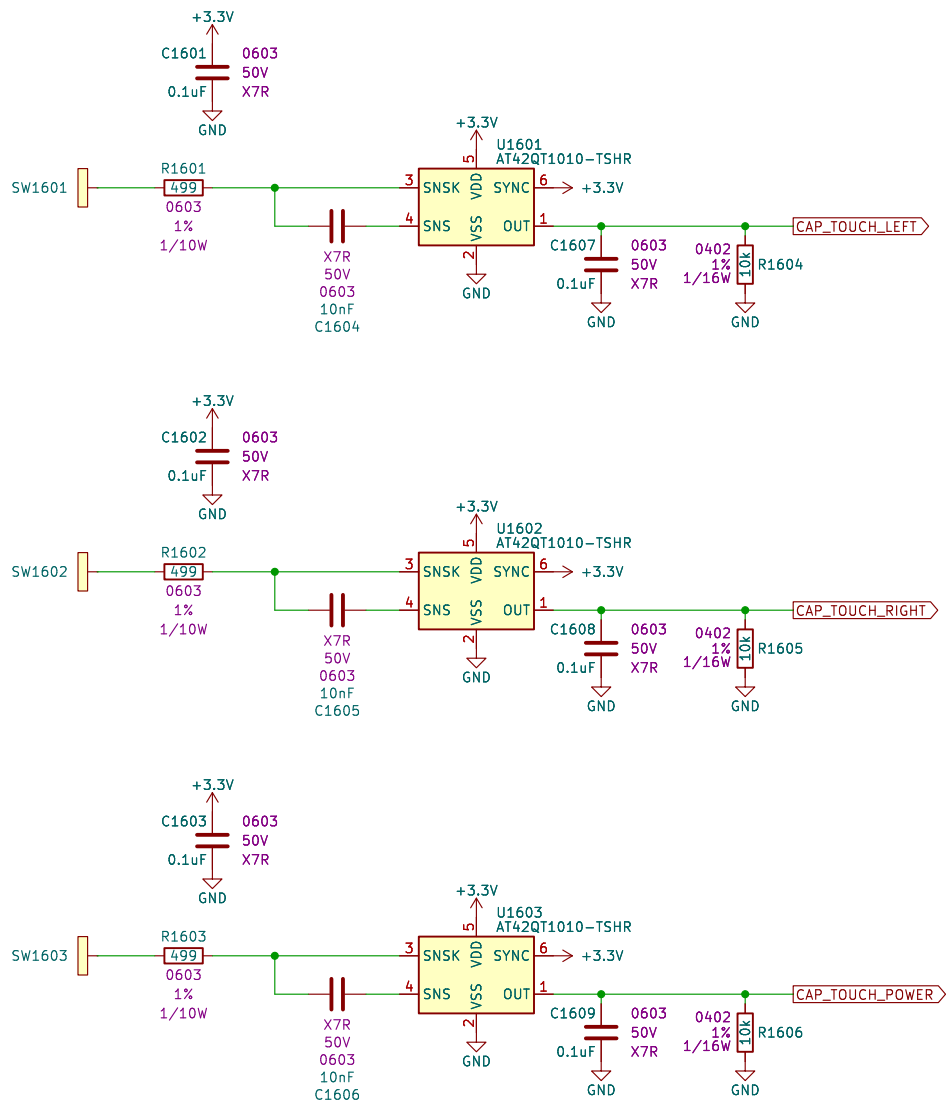
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File: Display.kicad_sch

Title: USB Hub

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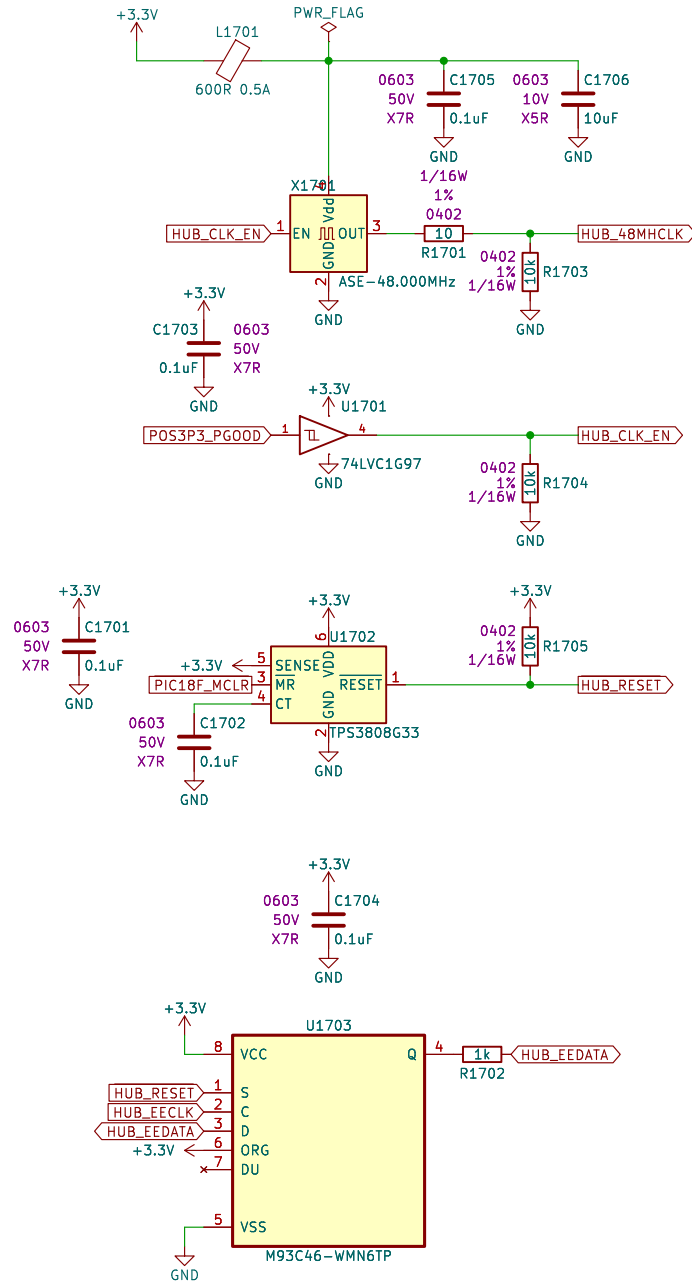
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File: Pushbuttons.kicad_sch

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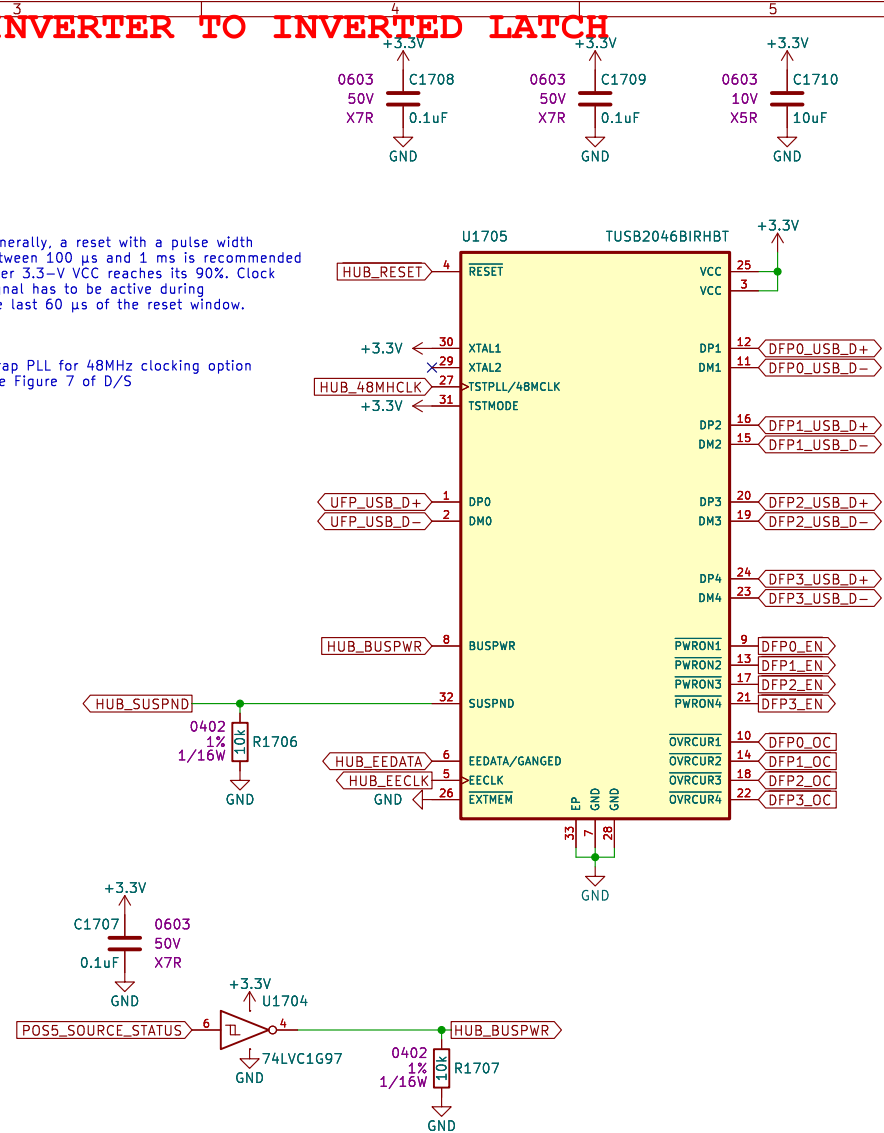
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INVESTIGATE CHANGING INVERTER TO INVERTED LATCH



Generally, a reset with a pulse width between 100 μ s and 1 ms is recommended after 3.3-V VCC reaches its 90%. Clock signal has to be active during the last 60 μ s of the reset window.

Strap PLL for 48MHz clocking option
See Figure 7 of D/S



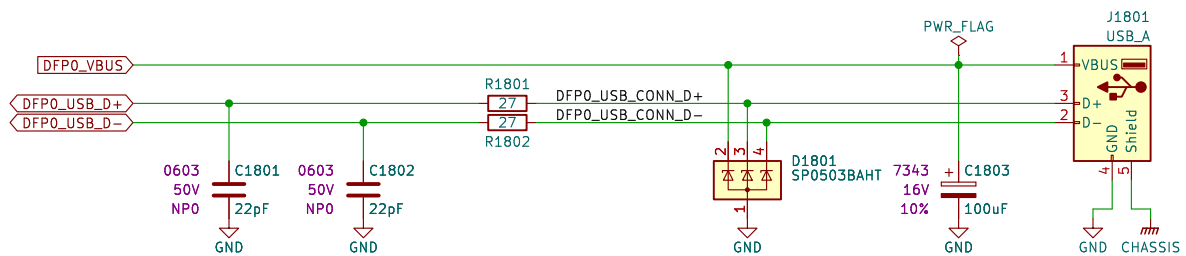
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File: Hub_Controller.kicad_sch

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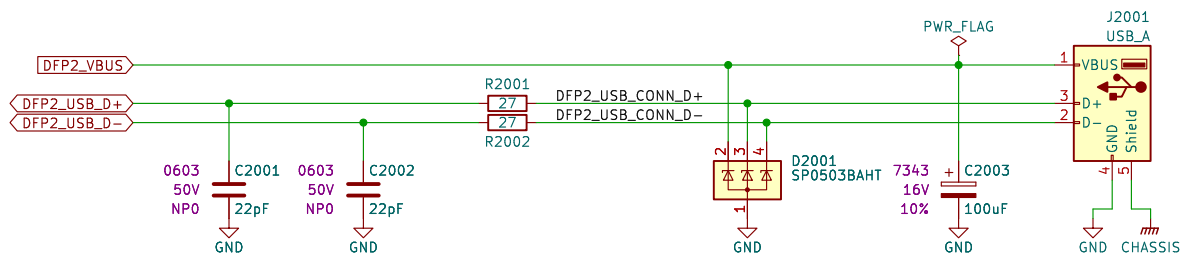
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File: Downstream_Port_0.kicad_sch

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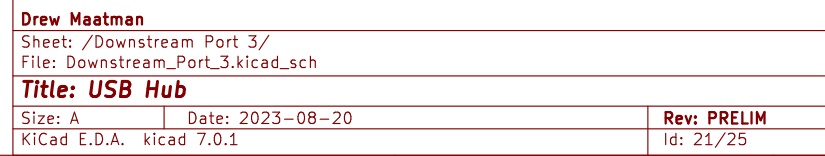
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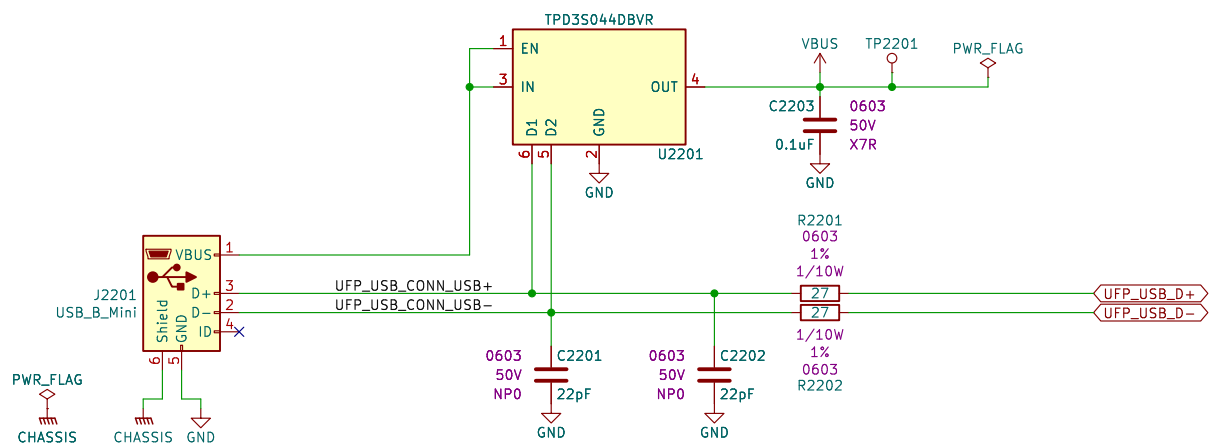
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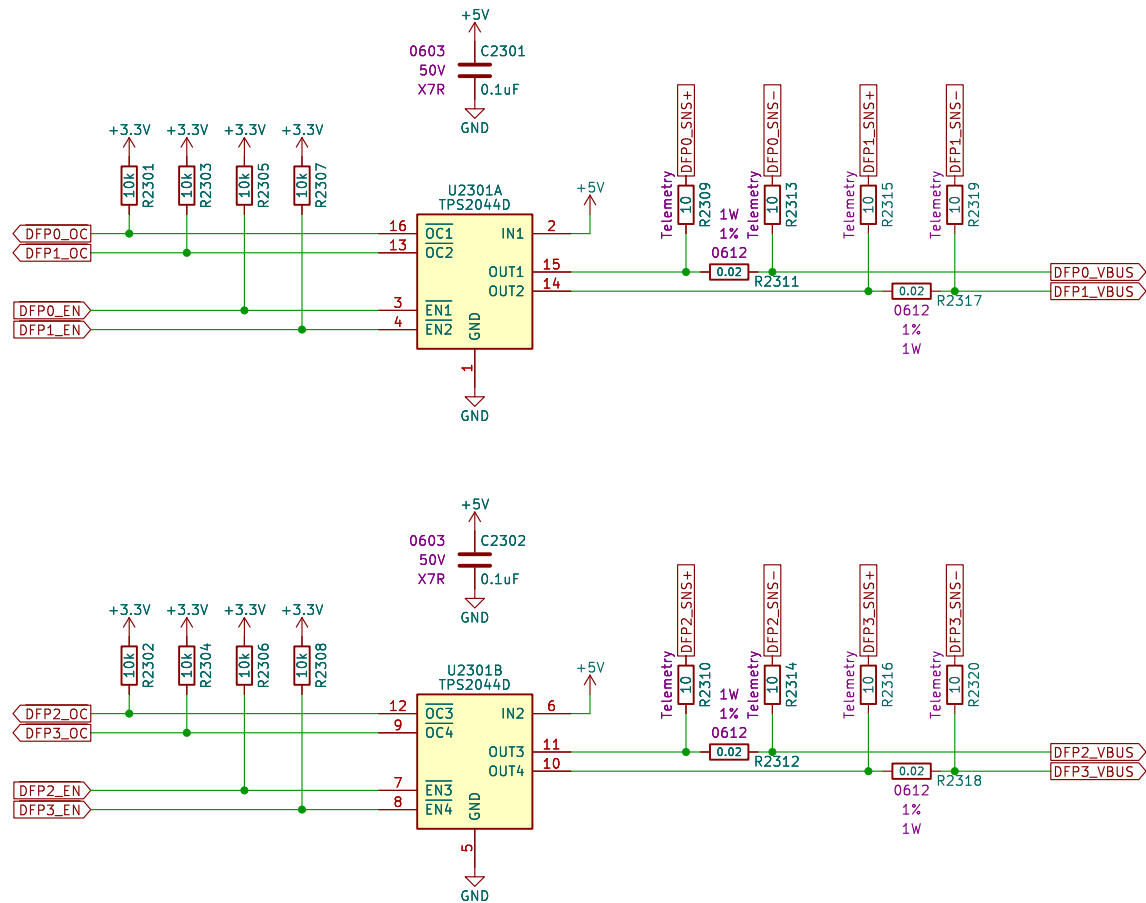
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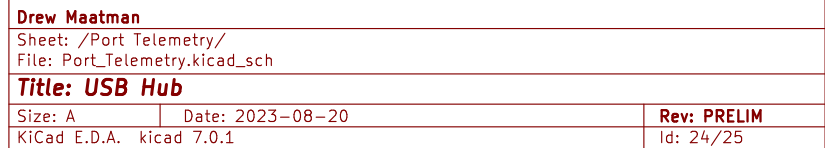
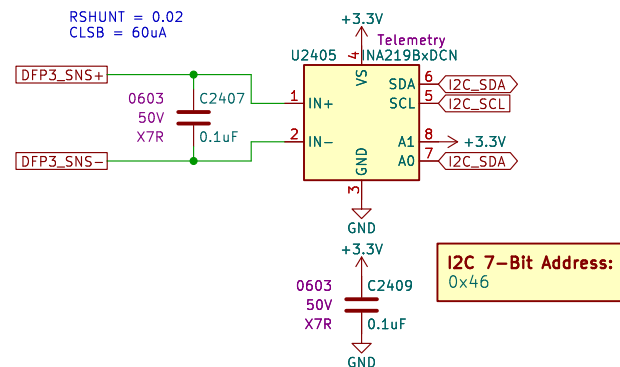
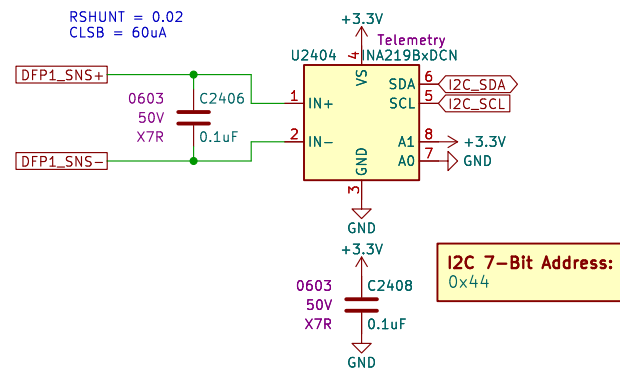
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File: port_power_control.kicad_sch

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Sheet: /Mechanical/
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