

VFD Clock Logic Board

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Consider adding net ties on all current sense shunt resistors for net isolation

Drew Maatman

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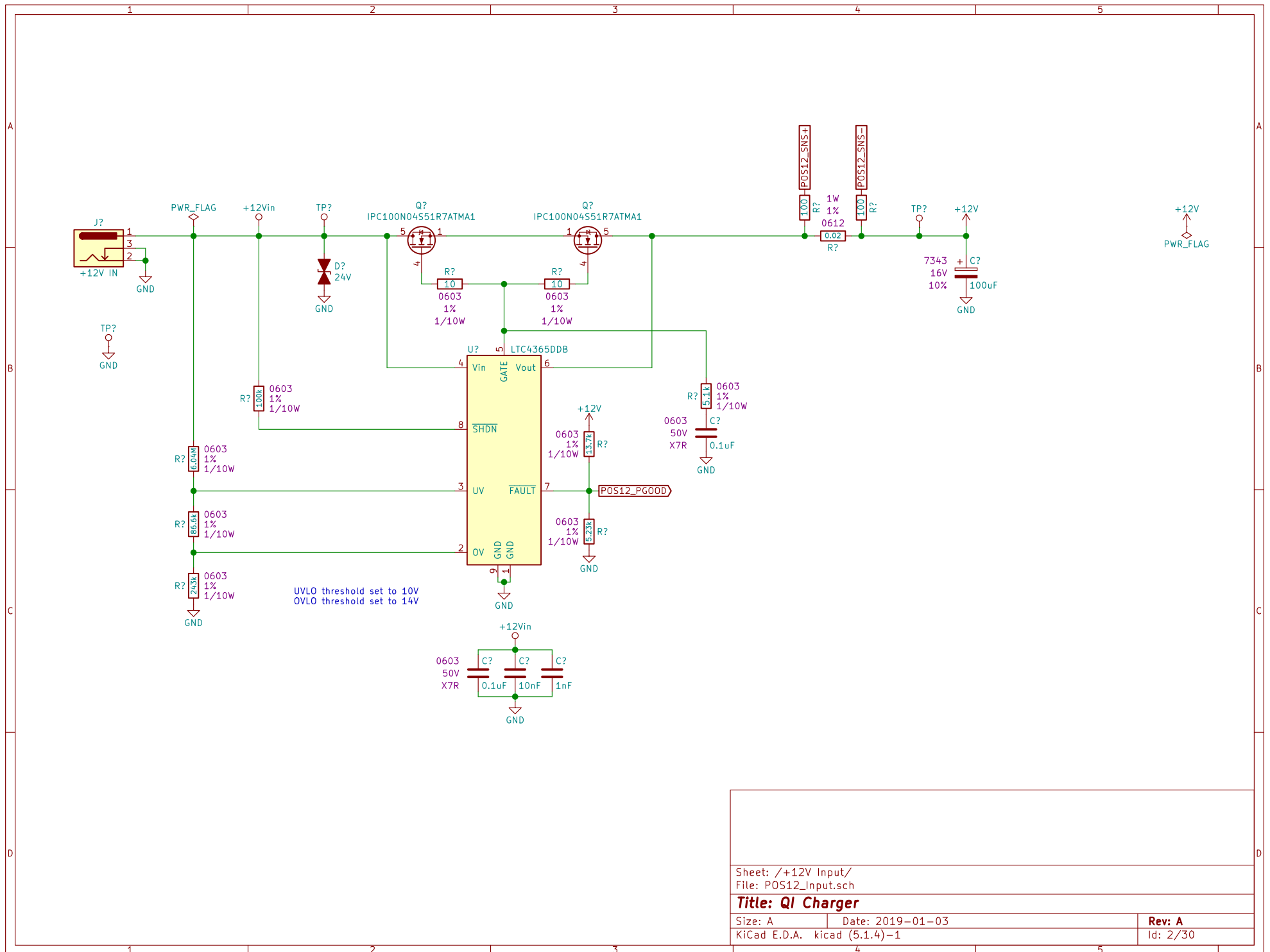
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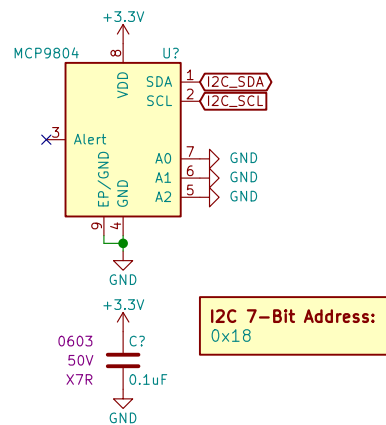
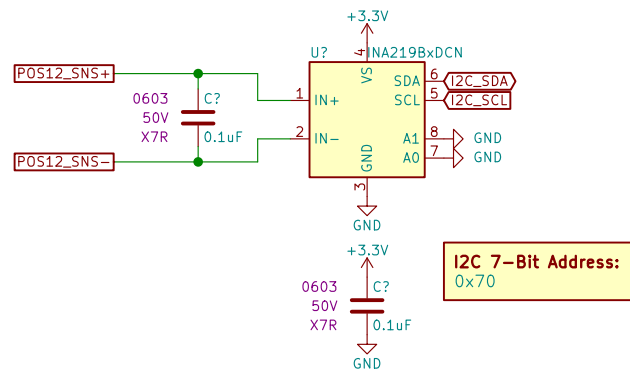
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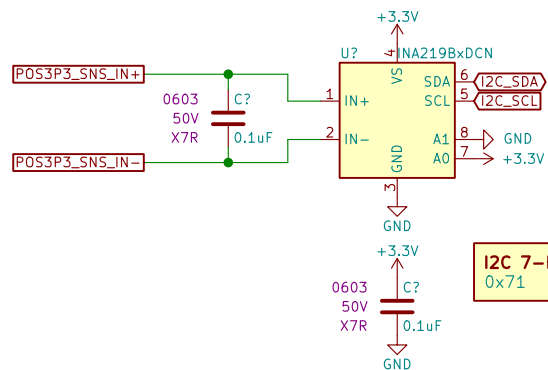
Sheet: /+12V Telemetry/
File: POS12_Telemetry.sch

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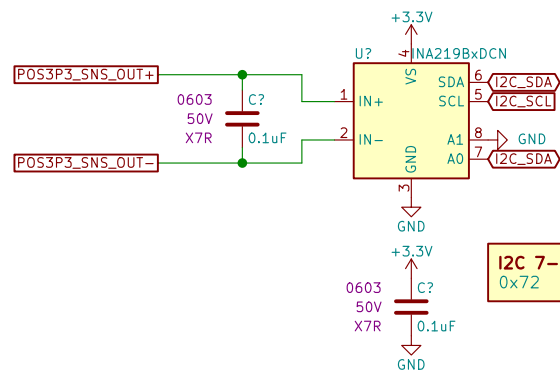
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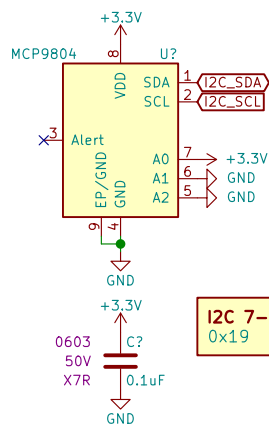
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I2C 7-Bit Address:
0x71



I2C 7-Bit Address:
0x72



I2C 7-Bit Address:
0x19

Sheet: /+3.3V Telemetry/
File: POS3P3_Telemetry.sch

Title:

Size: A

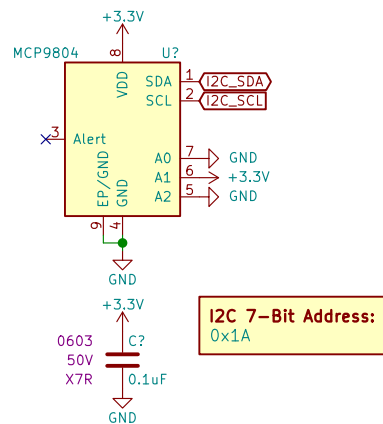
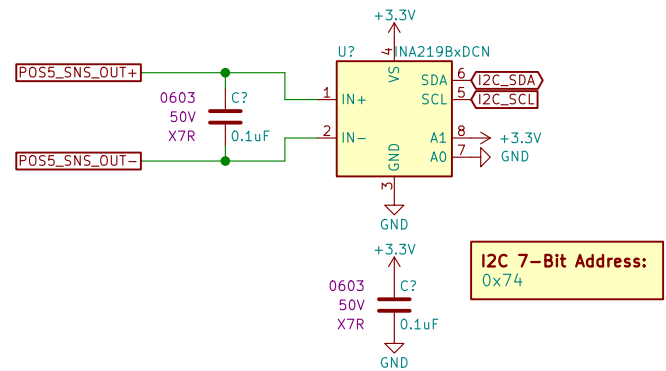
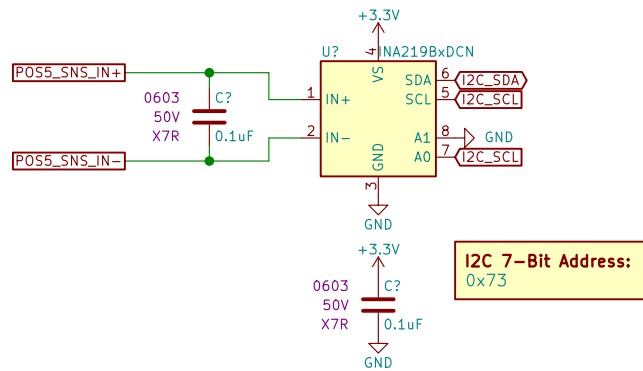
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Rev:

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Sheet: /+5V Telemetry/
File: POS5_Telemetry.sch

Title:

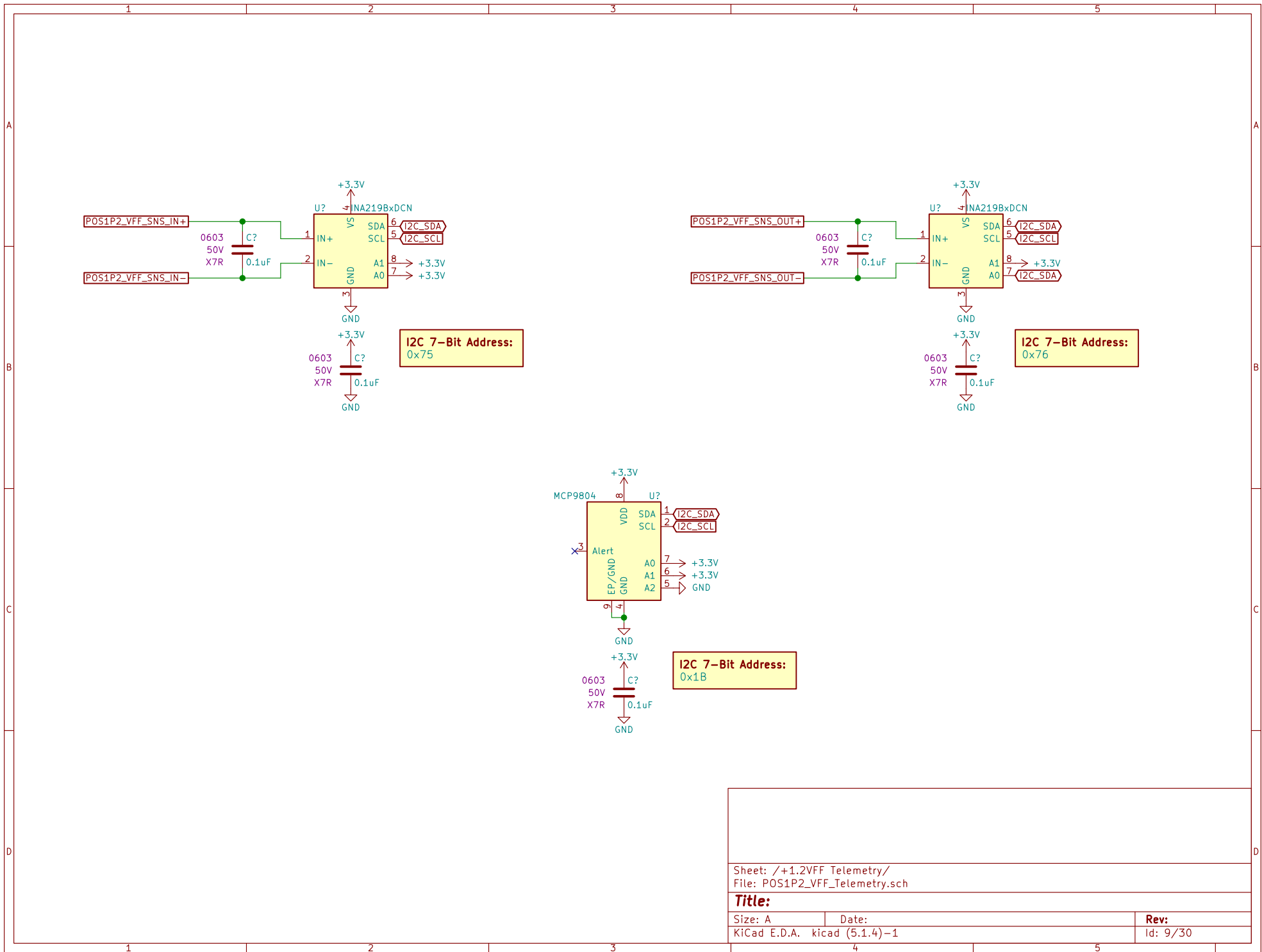
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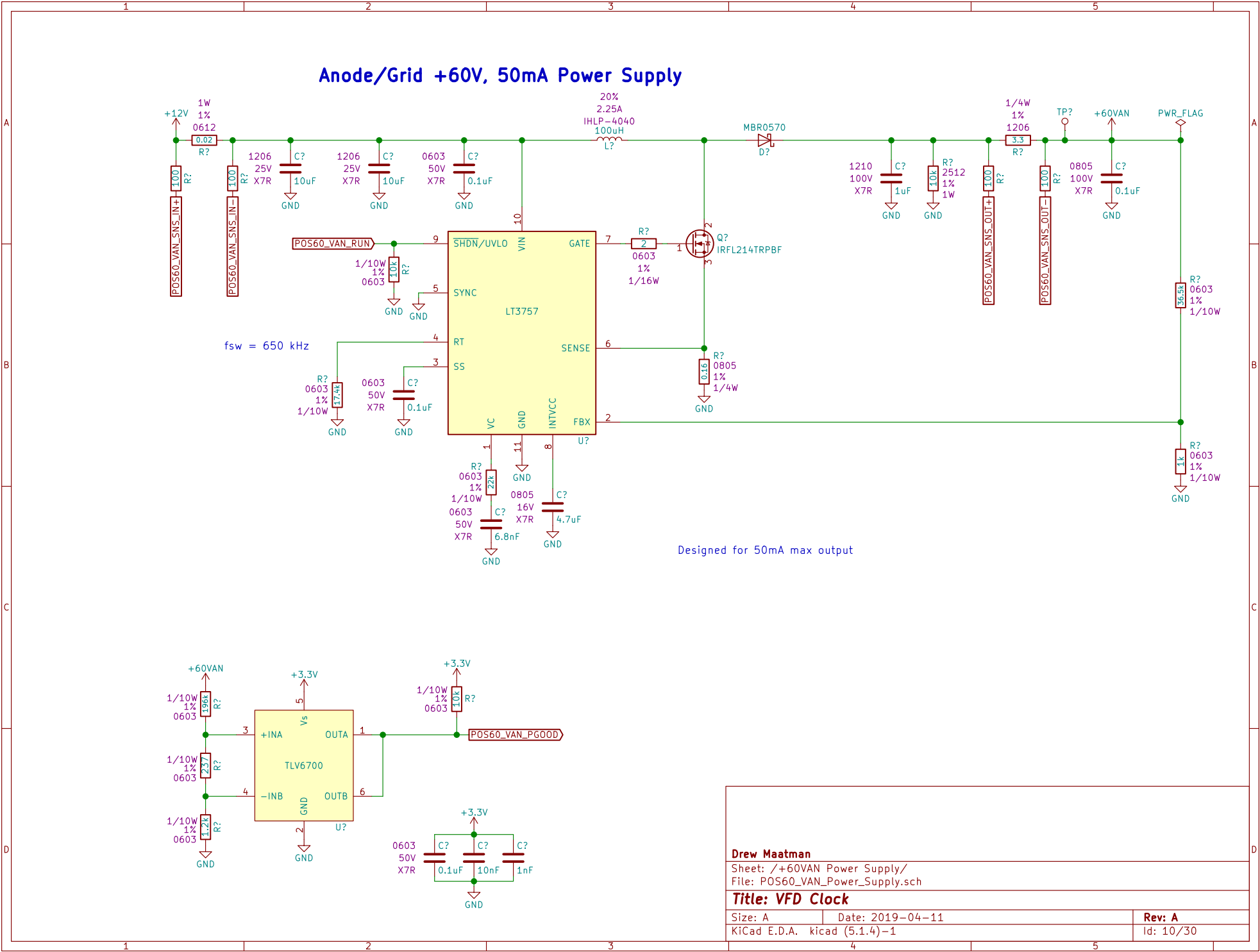
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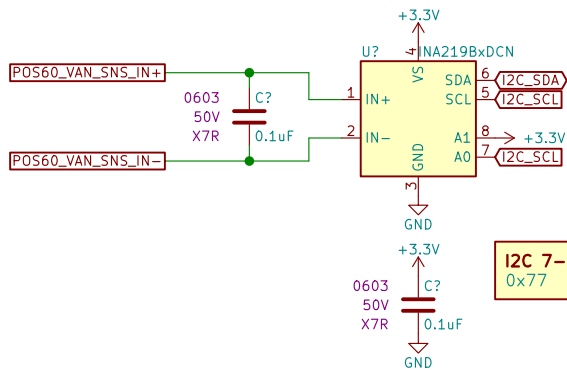
Anode/Grid +60V, 50mA Power Supply

Designed for 50mA max output

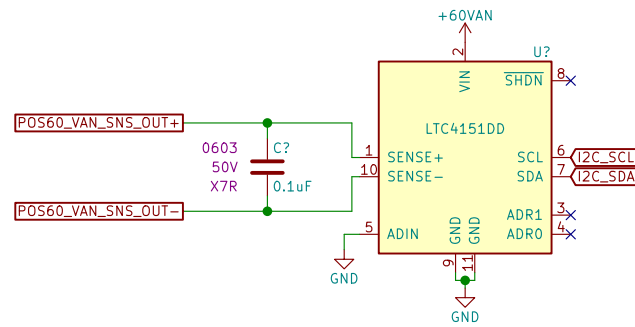
Drew Maatman
 Sheet: //+60VAN Power Supply/
 File: POS60_VAN_Power_Supply.sch
Title: VFD Clock
 Size: A Date: 2019-04-11 Rev: A
 KiCad E.D.A. kicad (5.1.4)-1 Id: 10/30

[illegible]

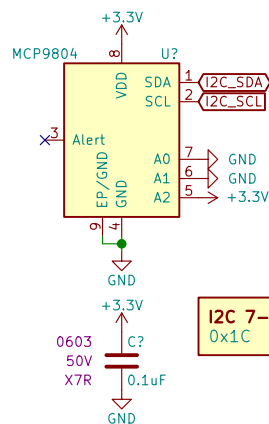
Rev: A
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I2C 7-Bit Address:
0x77



I2C 7-Bit Address:
0x6A



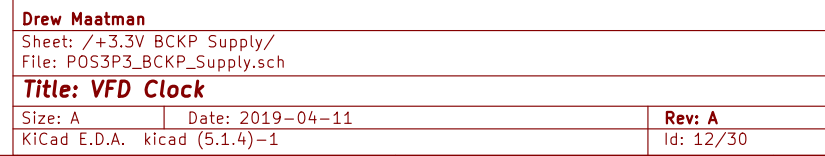
I2C 7-Bit Address:
0x1C

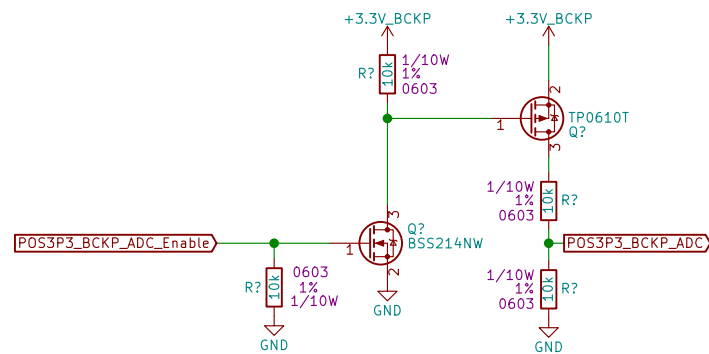
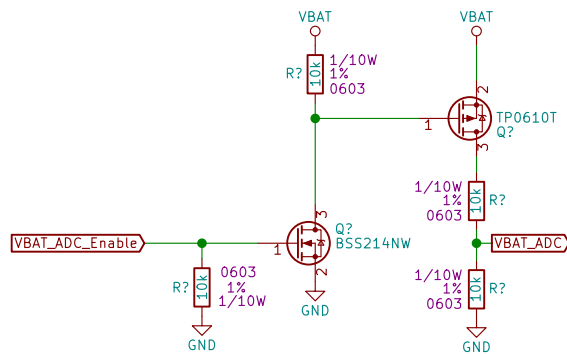
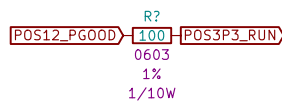
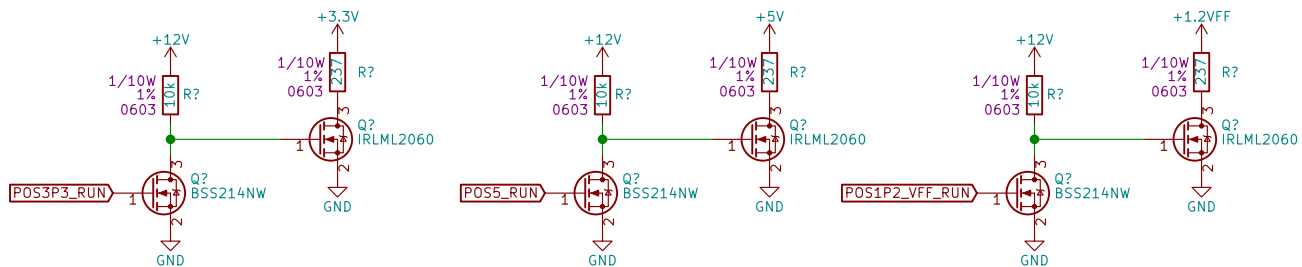
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Title:

Size: A Date:
KiCad E.D.A. kicad (5.1.4)-1

Rev:
Id: 11/30





Sheet: /Misc Power/
File: Misc_Power.sch

Title:

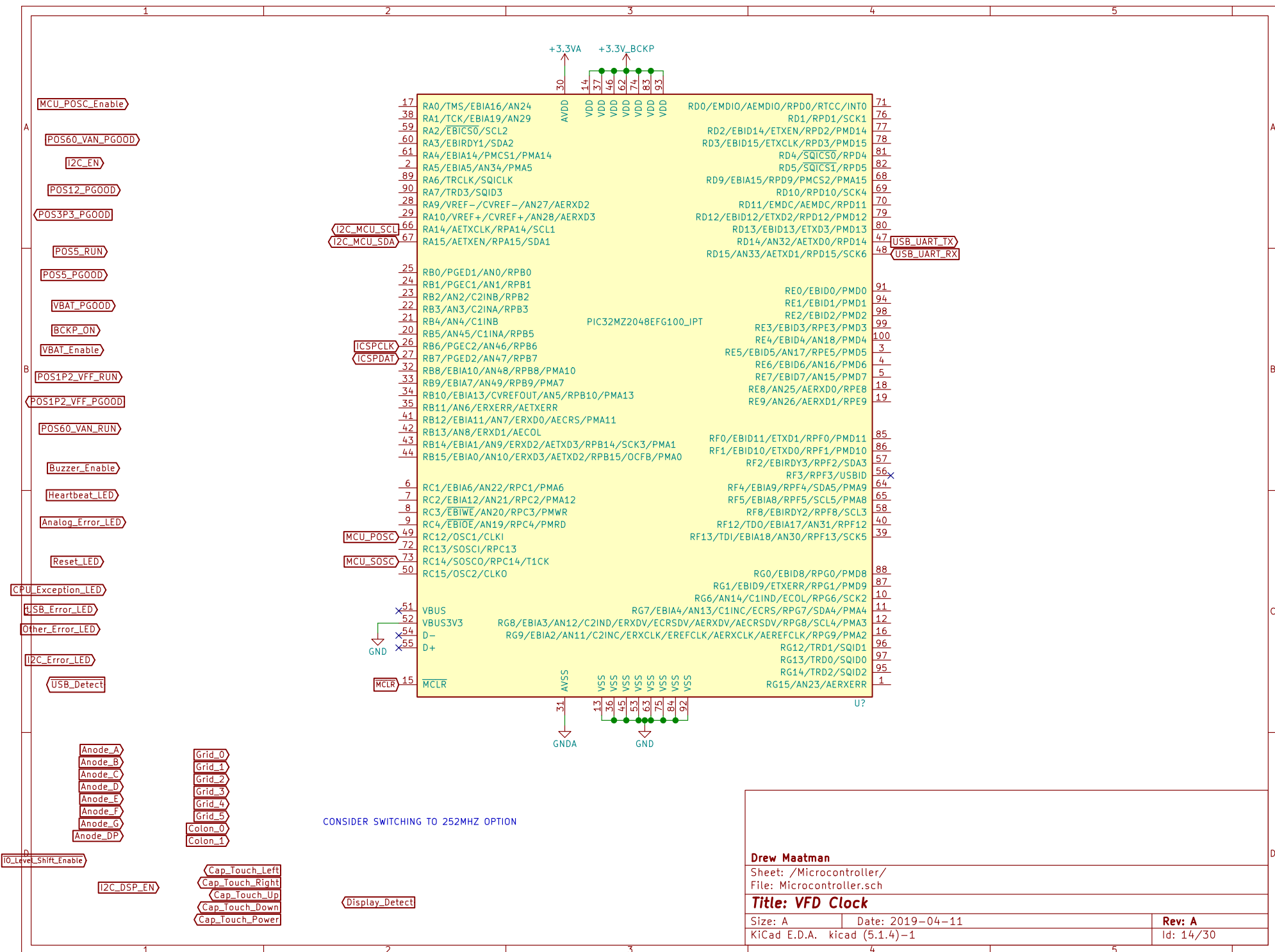
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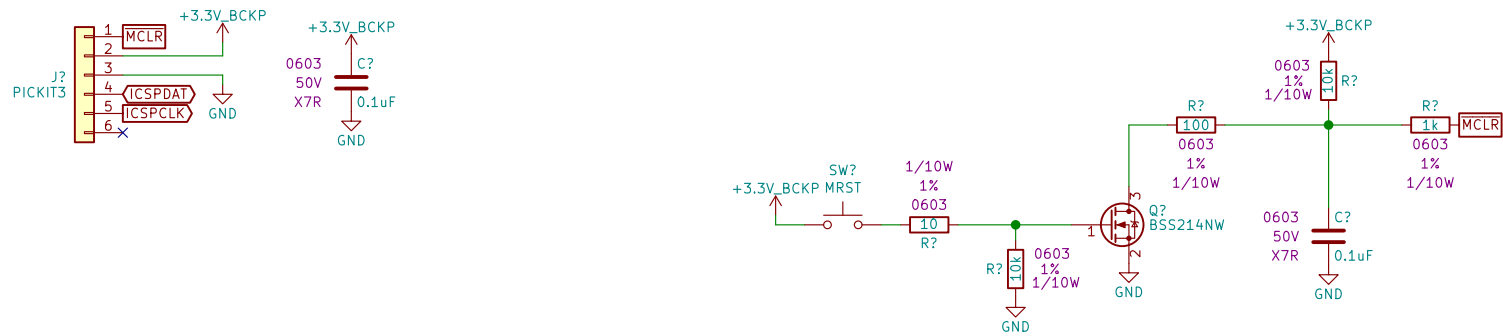
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Rev:

Id: 13/30



Microcontroller Programming



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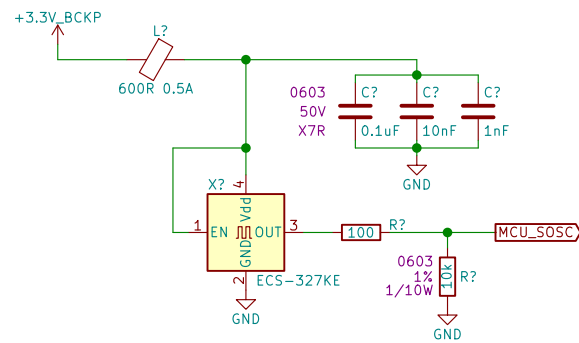
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Title: VFD Clock

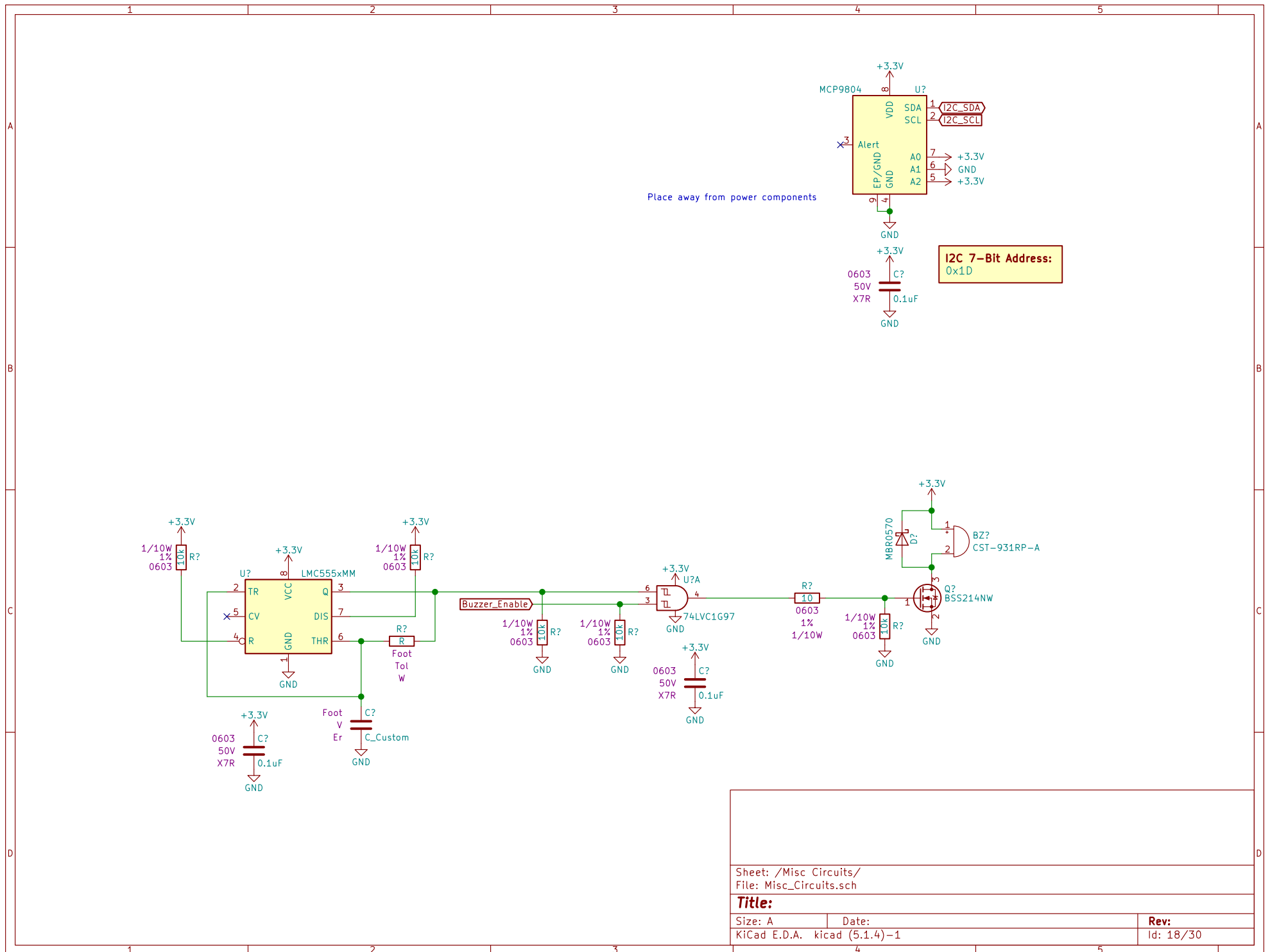
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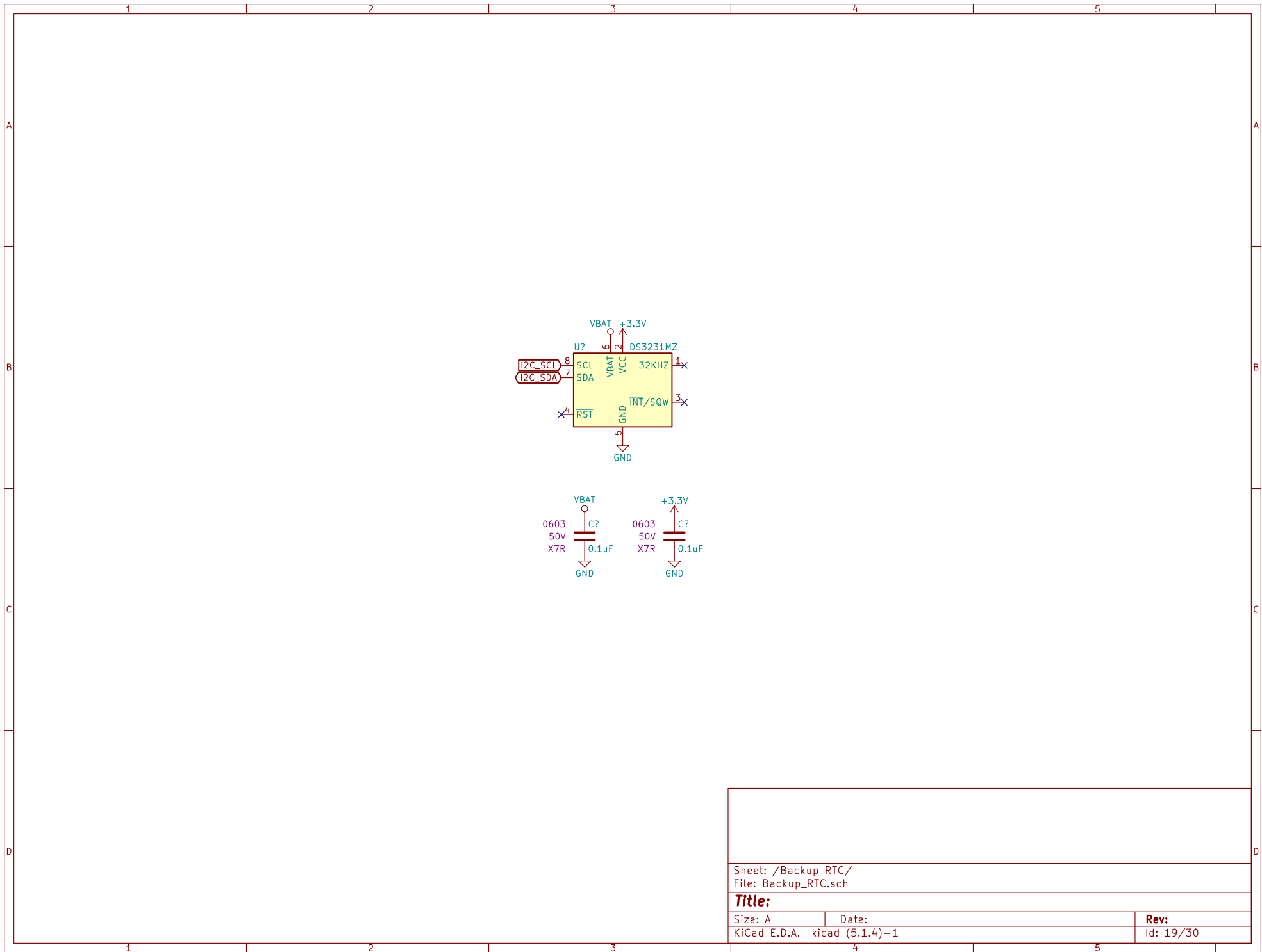
Rev: A
Id: 15/30



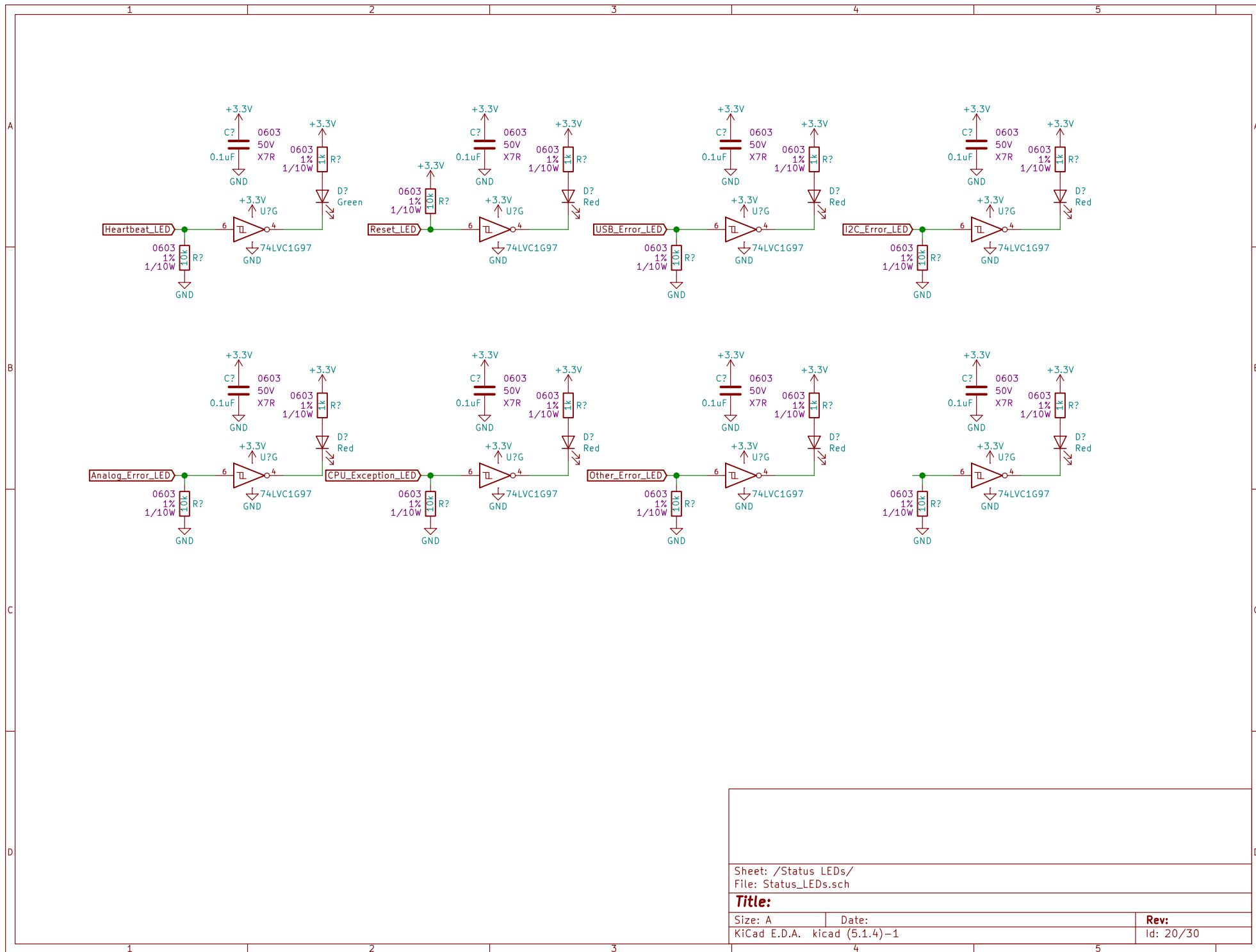


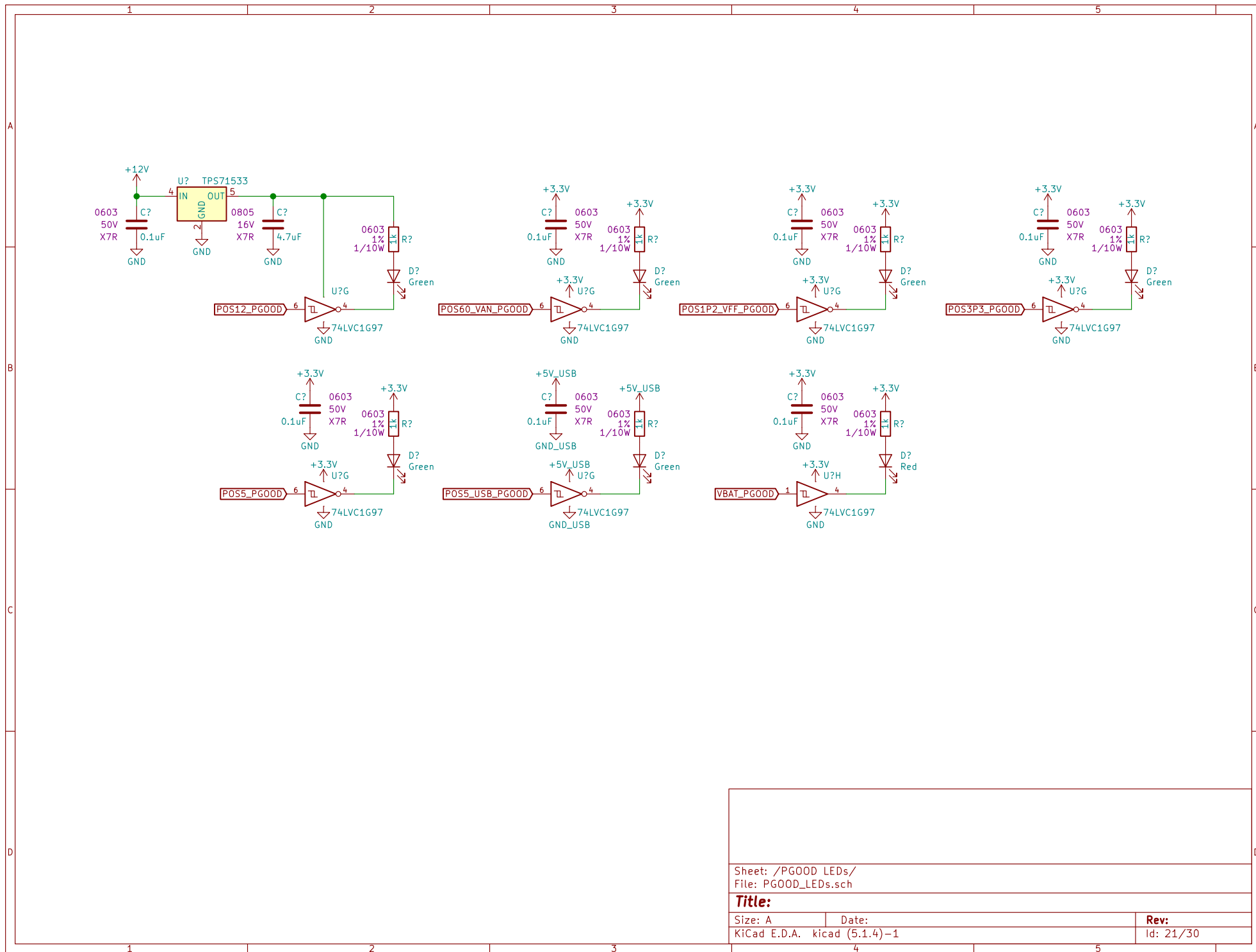
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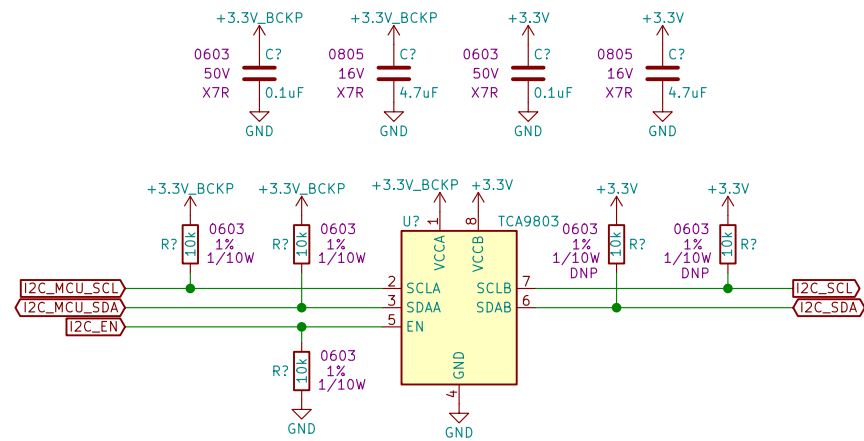




Sheet: /Backup_RTC/ File: Backup_RTC.sch		
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Size: A	Date:	Rev:
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Sheet: /I2C Buffer/
File: I2C_Buffer.sch

Title:

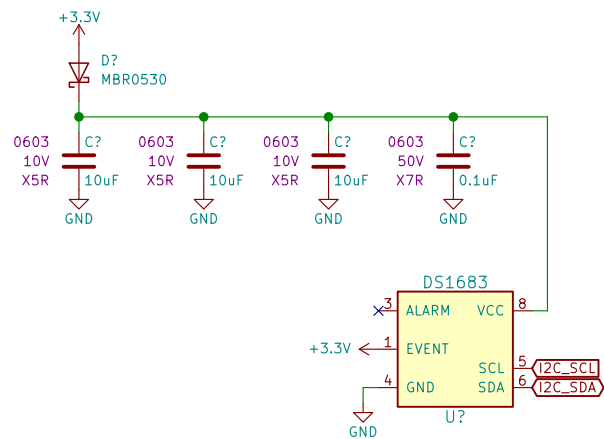
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Rev:

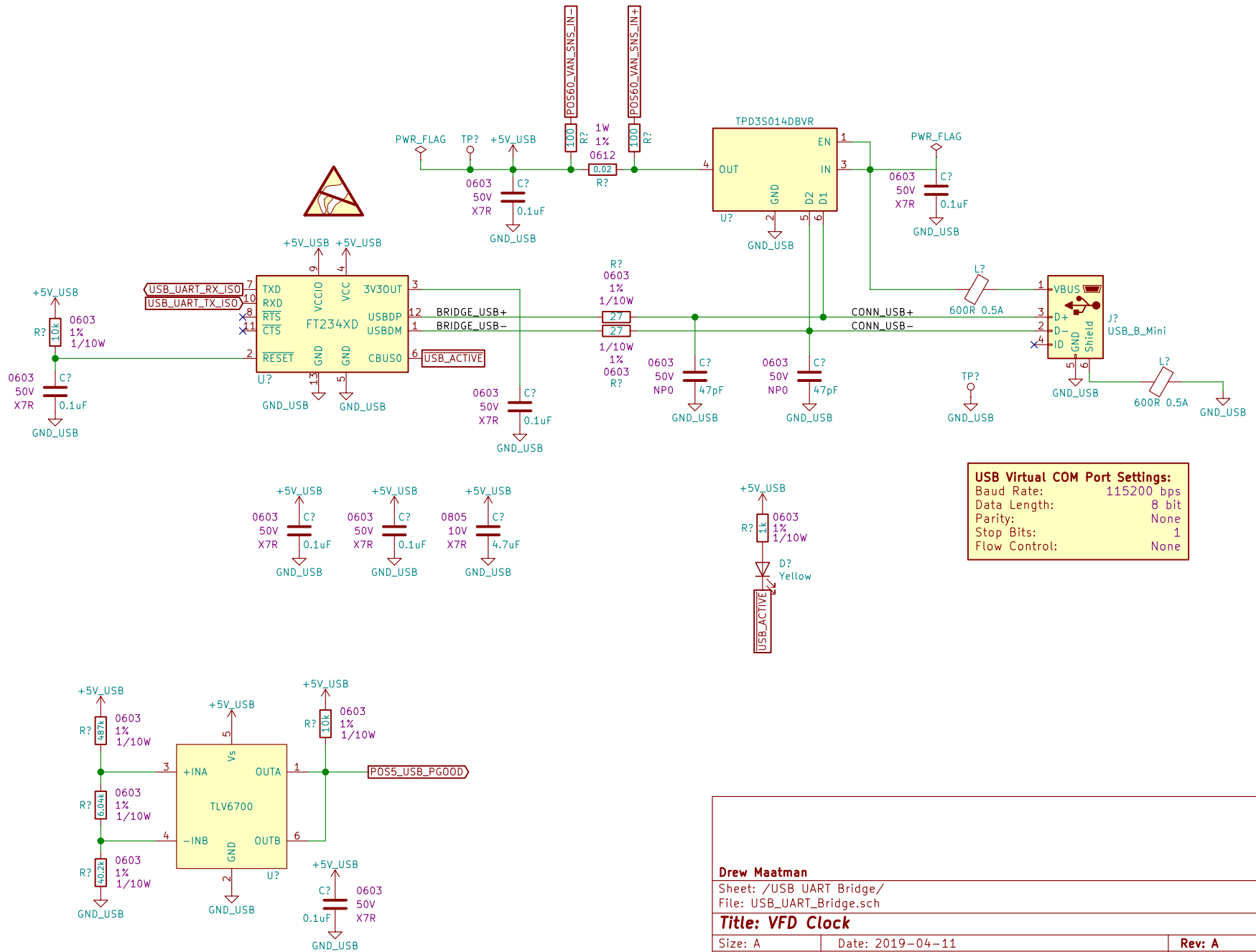
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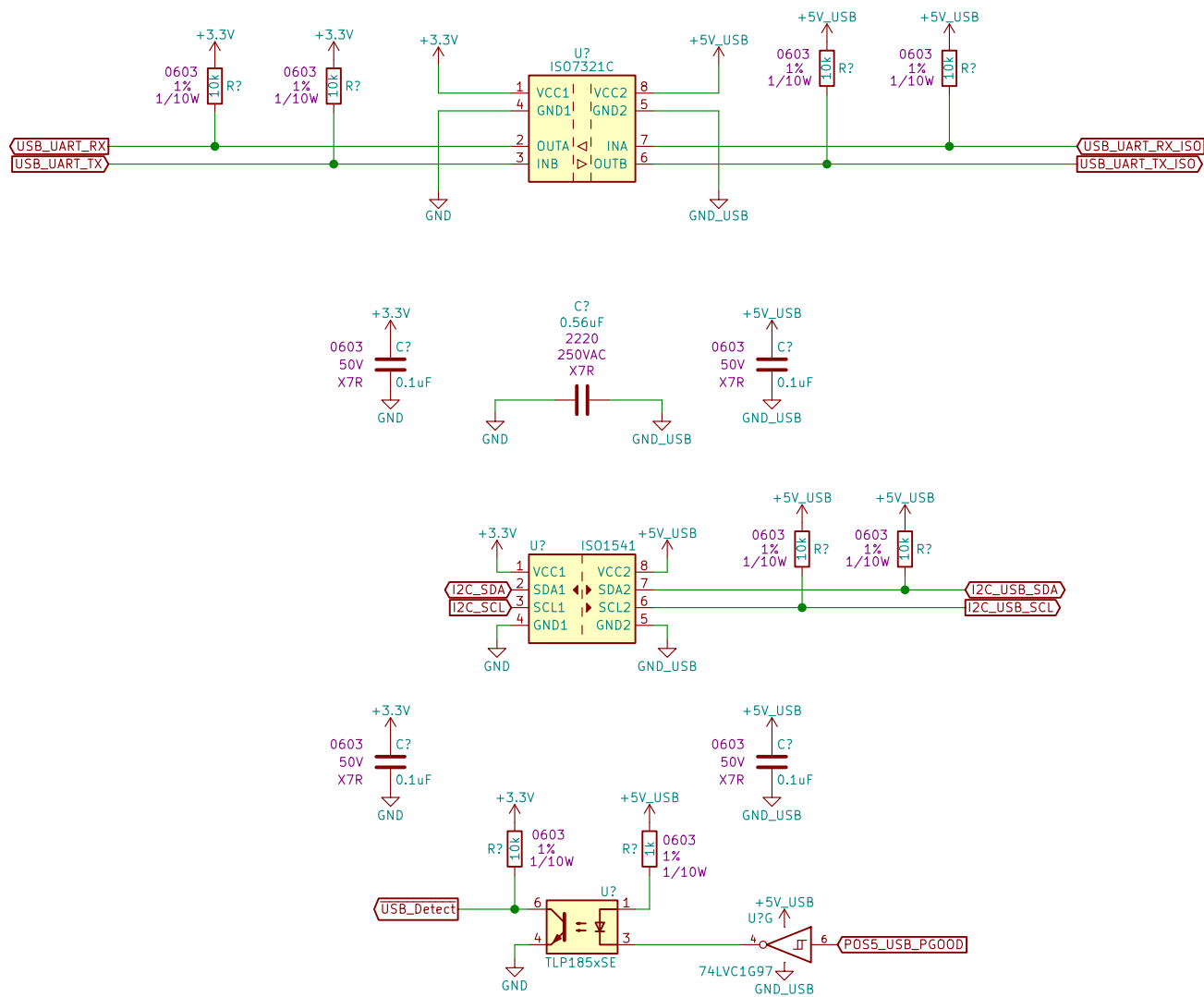


I2C 7-Bit Address:
0x3B

Sheet: /Time of Flight/ File: Time_of_Flight.sch		
Title:		
Size: A	Date:	Rev:
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11. USB UART Bridge





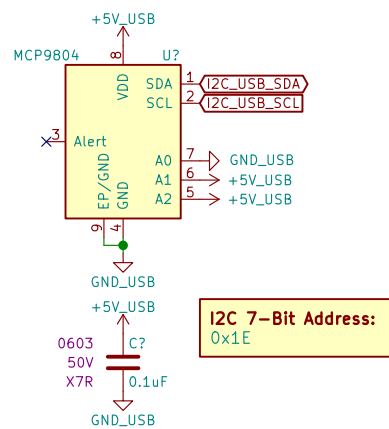
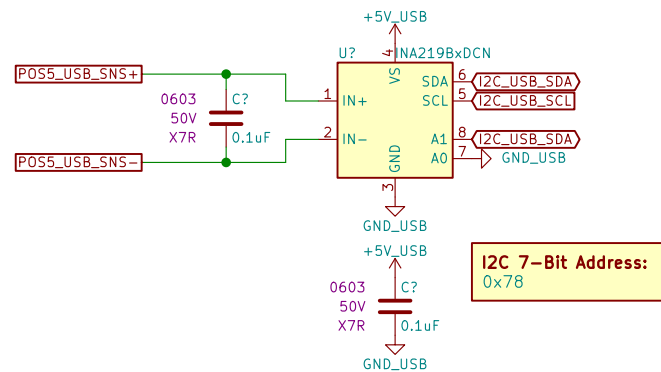
Drew Maatman

Sheet: /USB UART Isolation/
File: USB_UART_Isolation.sch

Title: VFD Clock

Size: A | Date: 2019-04-11
KiCad E.D.A. | kicad (5.1.4)-1

Rev: A
Id: 25/30



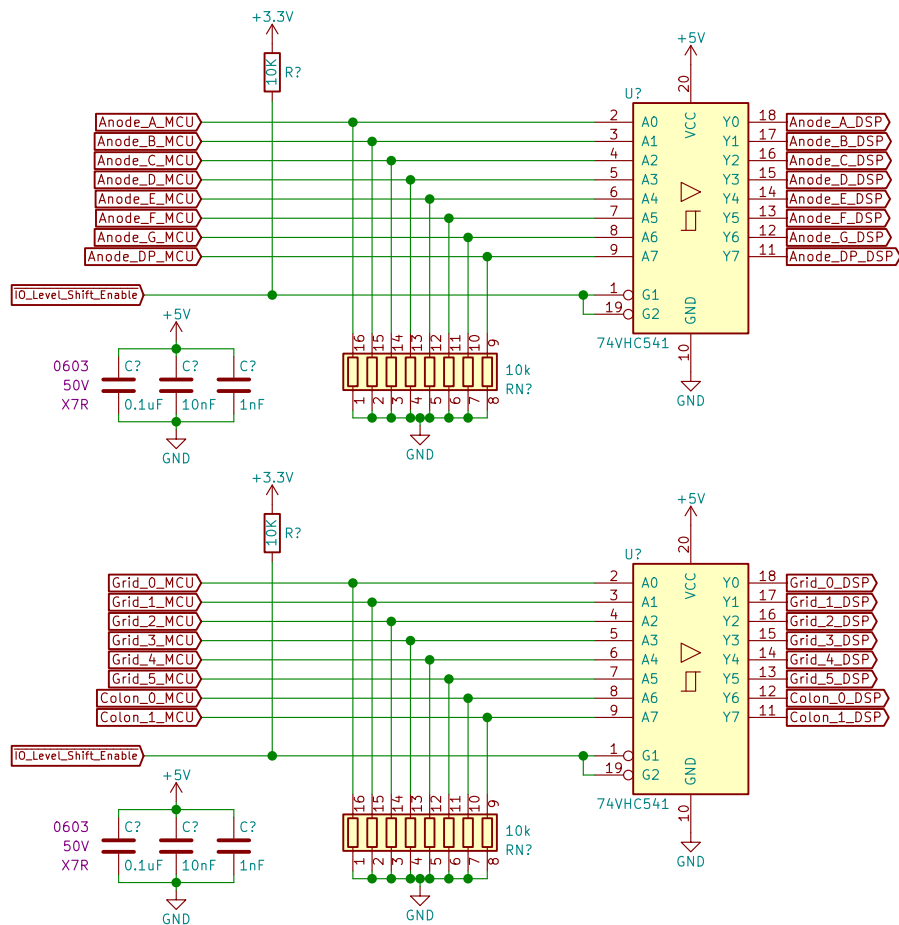
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Size: A Date:
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Rev:
Id: 26/30

I/O Buffers



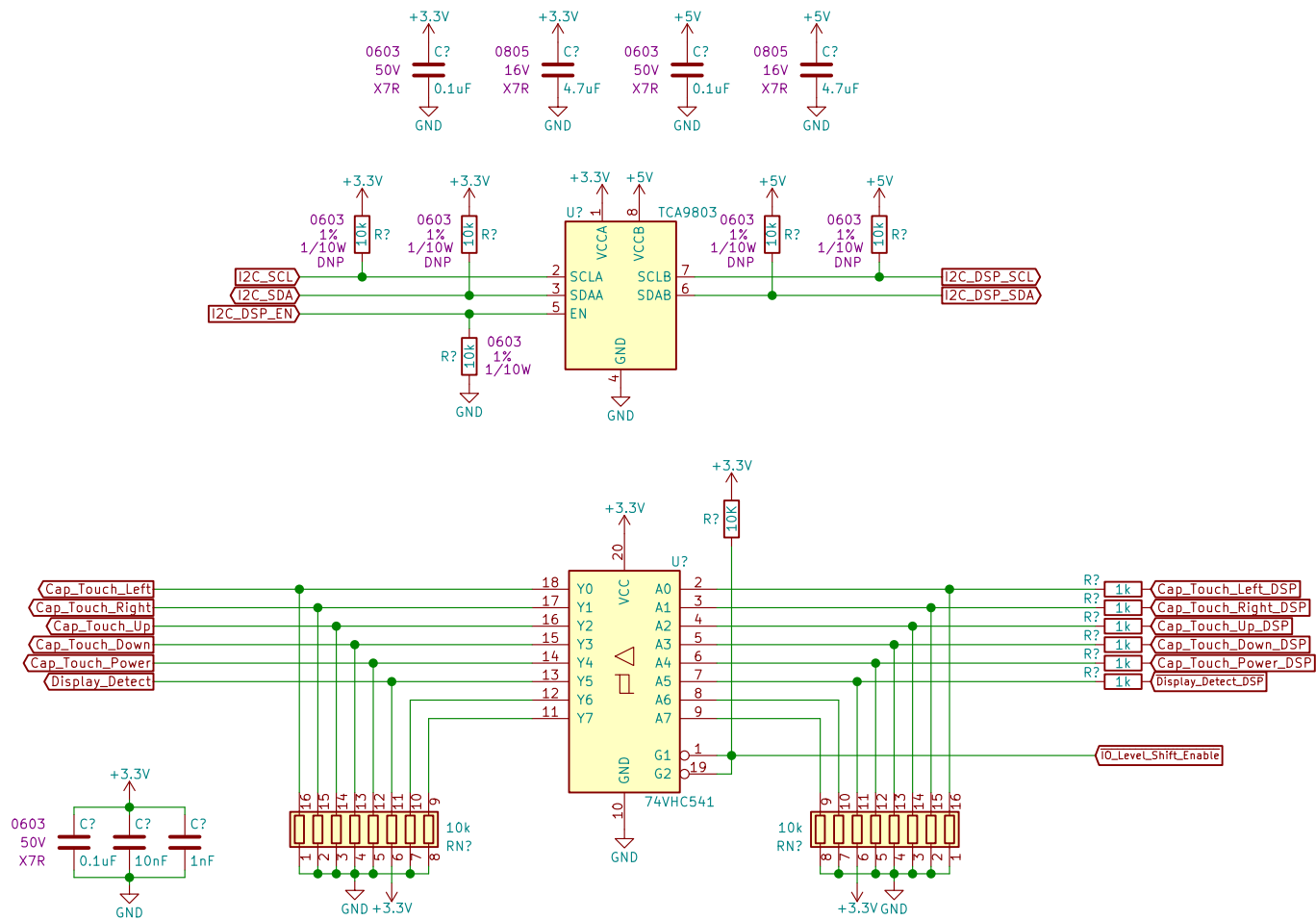
Marquette University Senior Design 2018/2019 Group E44

Sheet: /IO Buffers 1/
File: IO_Buffers_1.sch

Title: Electronic Display Logic Board

Size: A Date: 2018-12-15
KiCad E.D.A. kicad (5.1.4)-1

Rev: A
Id: 27/30



Sheet: /IO Buffers 2/
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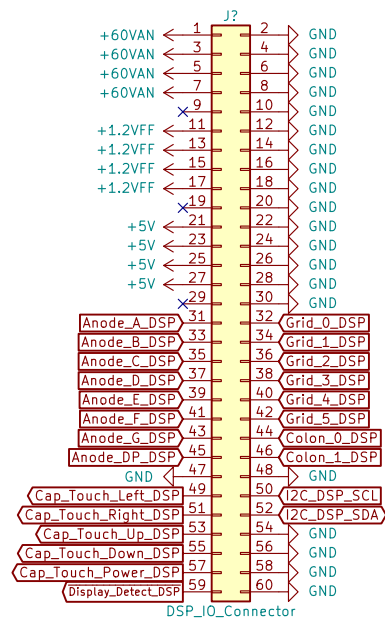
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Rev:

Id: 28/30



Sheet: /IO Connectors/
File: IO_Connectors.sch

Title:

Size: A Date:
KiCad E.D.A. kicad (5.1.4)-1

Rev:
Id: 29/30

