

VFD Clock Logic Board

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Drew Maatman

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Title: VFD Clock

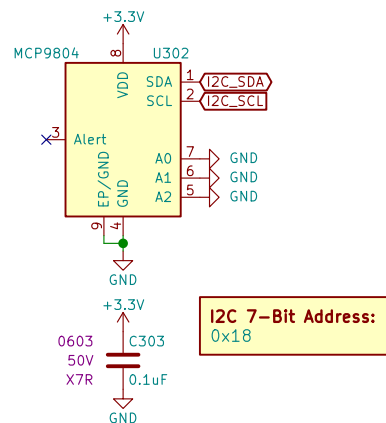
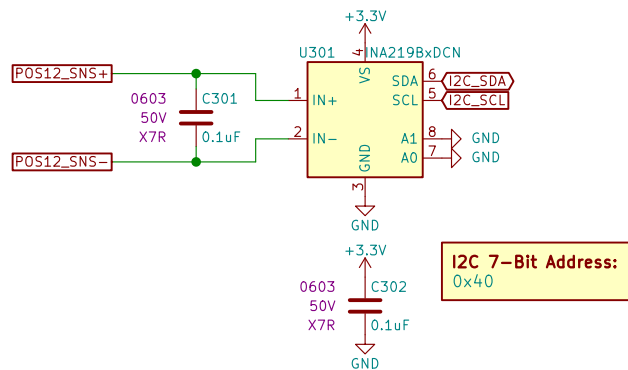
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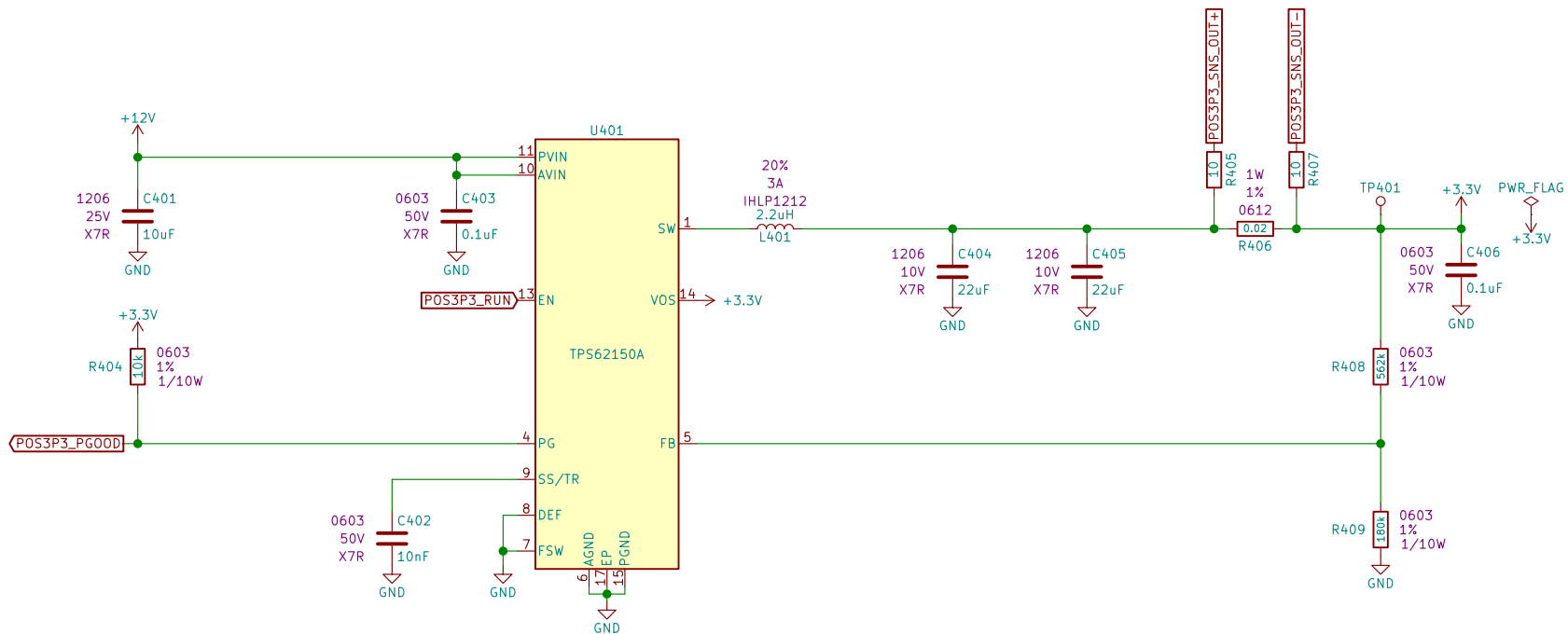
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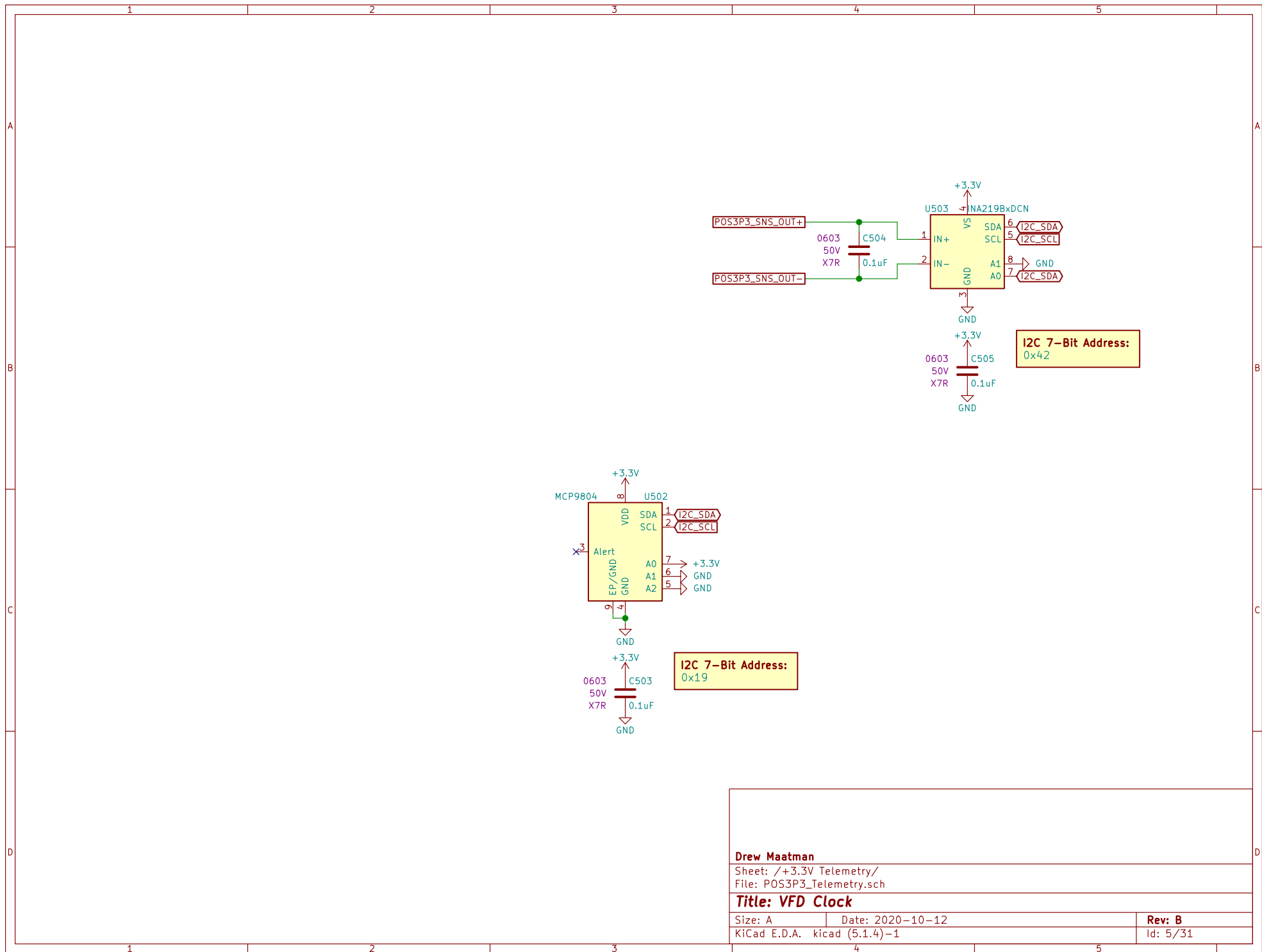
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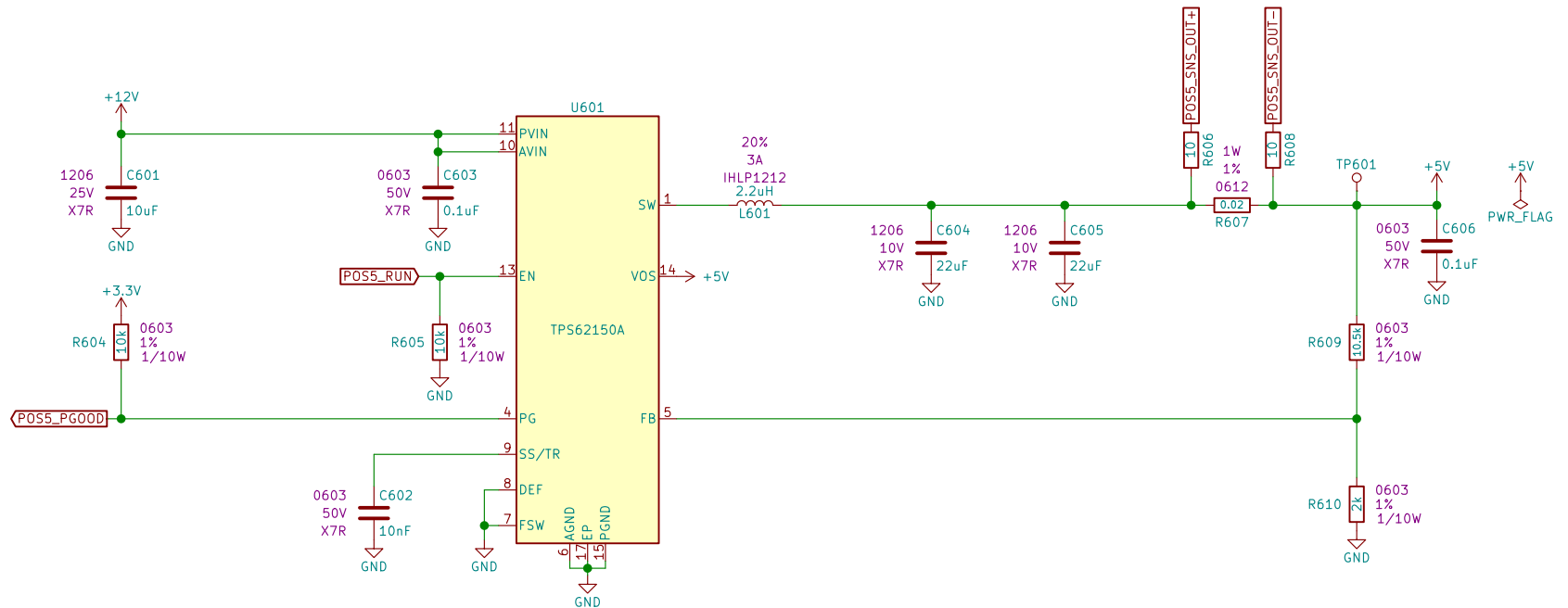
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Sheet: /+5V Power Supply/

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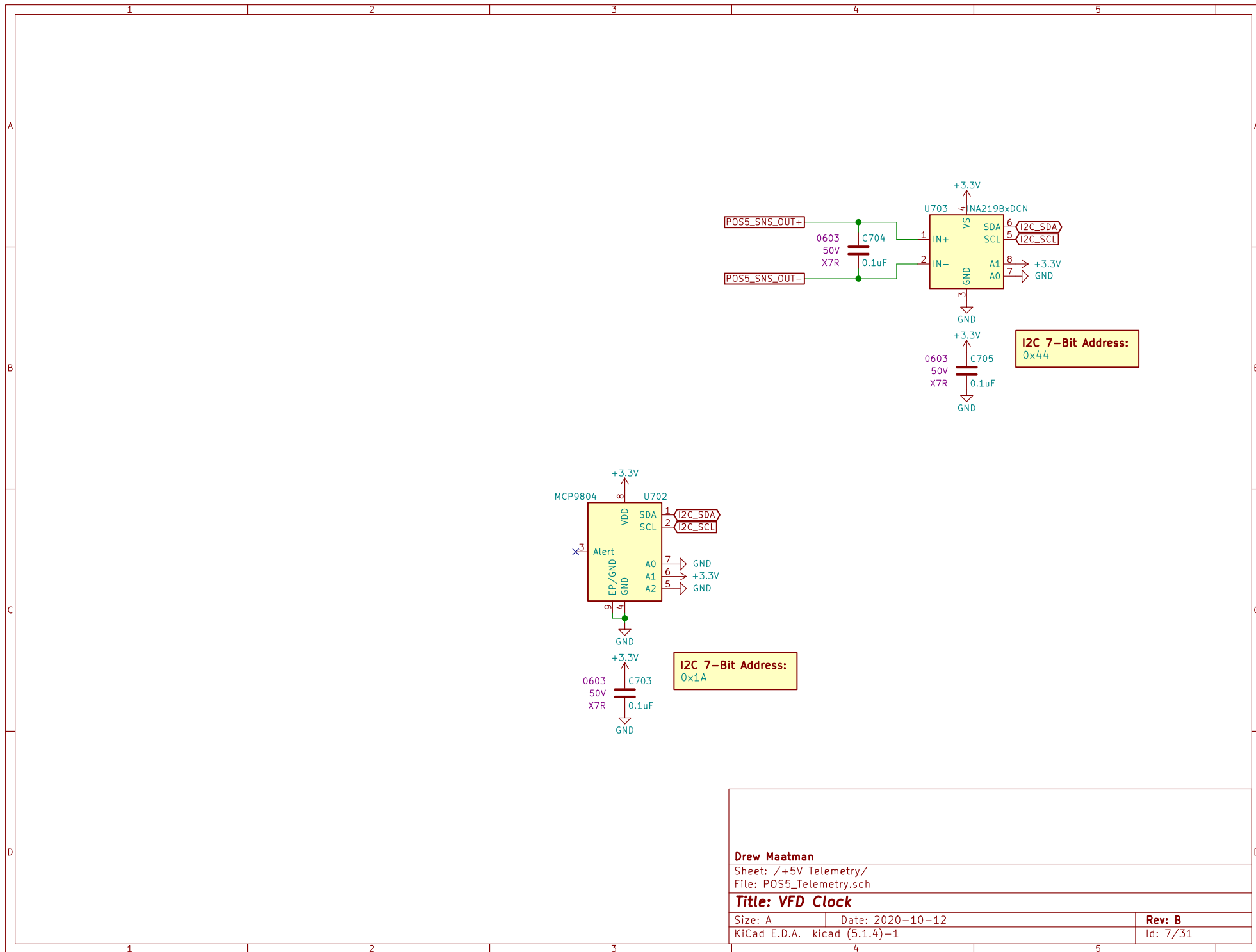
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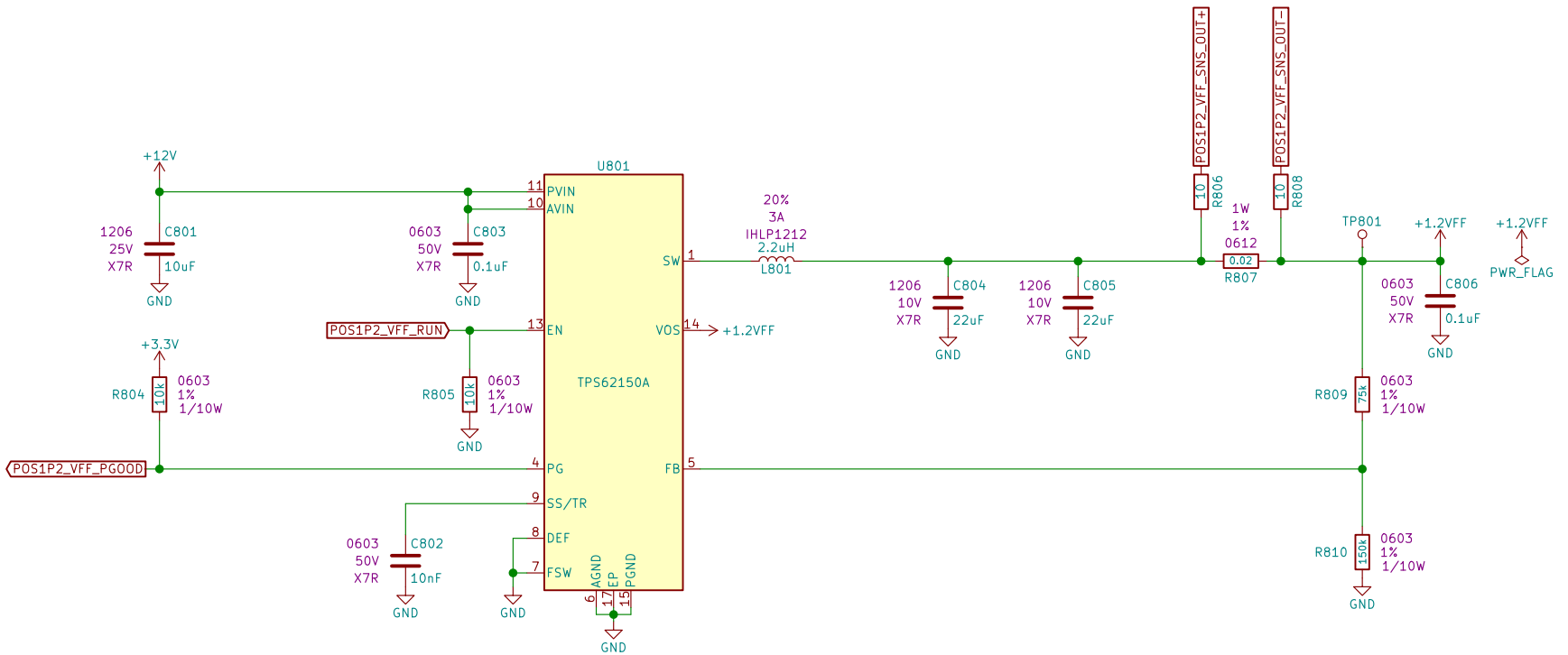
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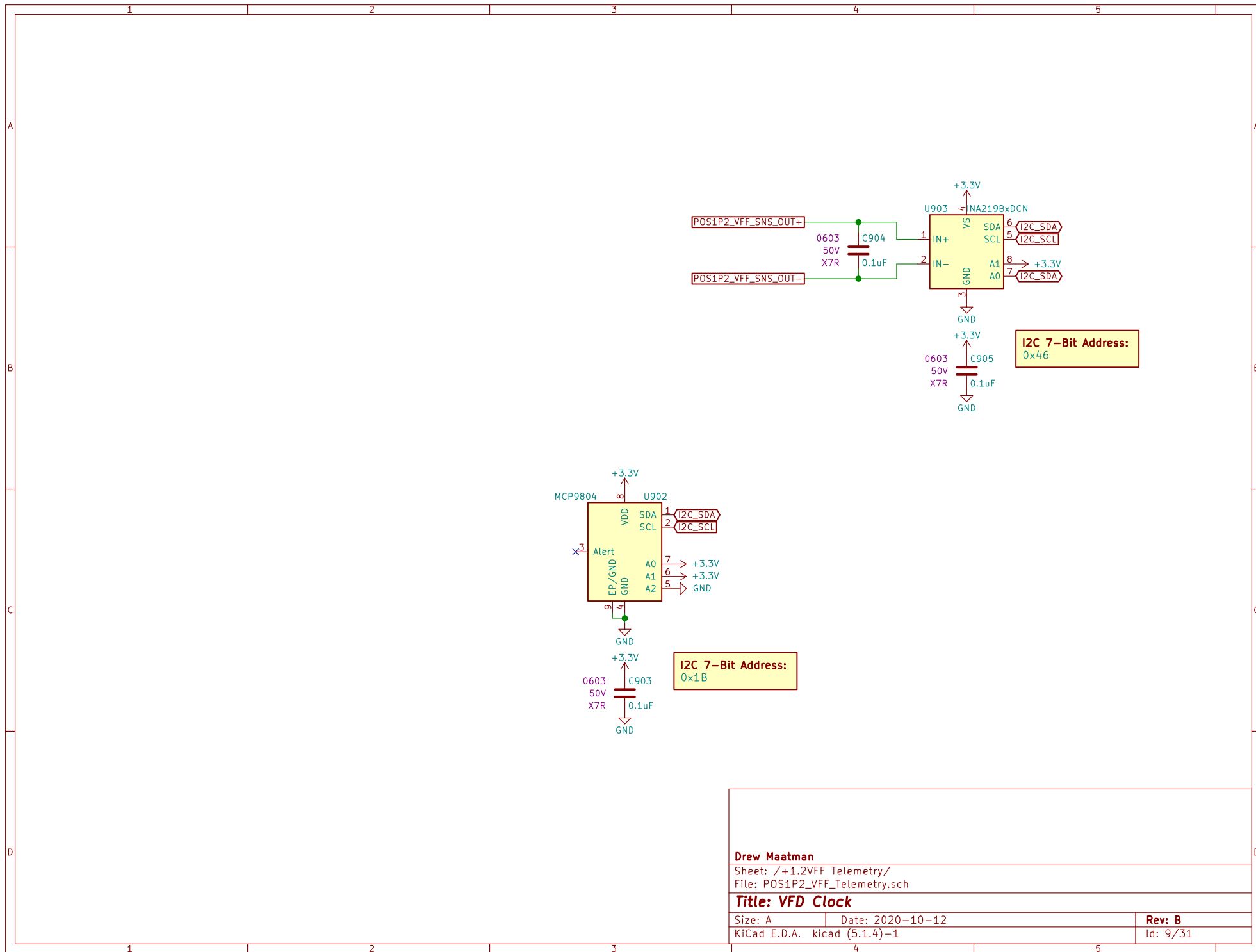
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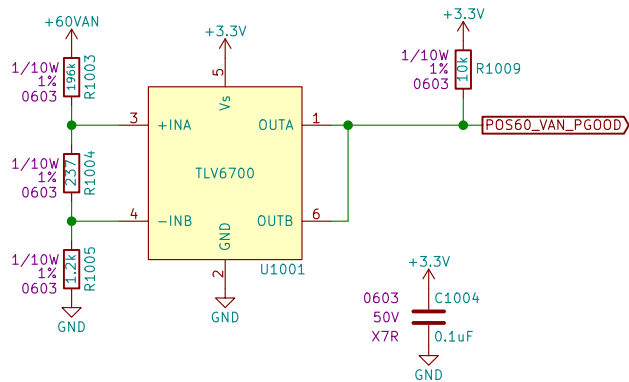
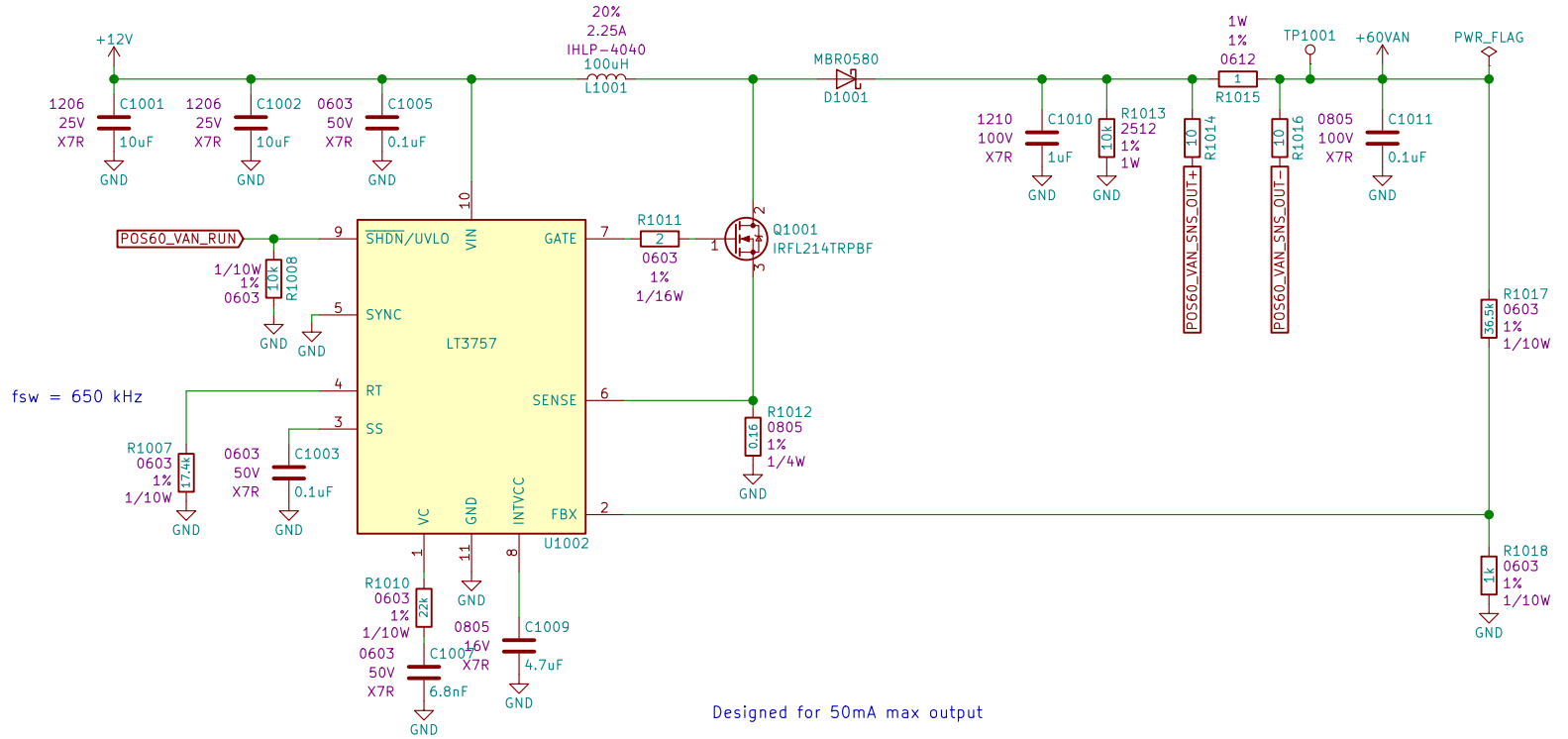
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Anode/Grid +60V, 50mA Power Supply



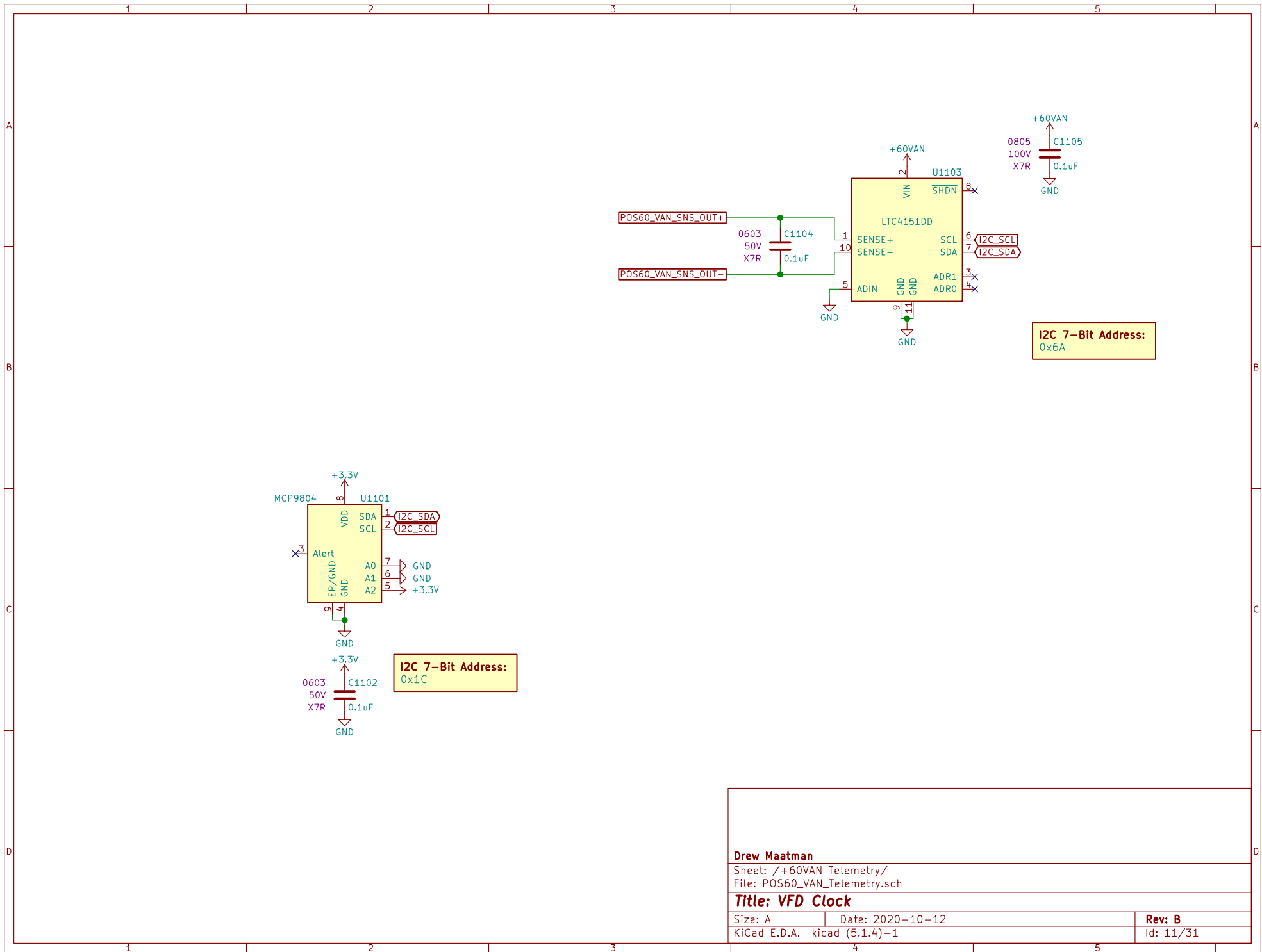
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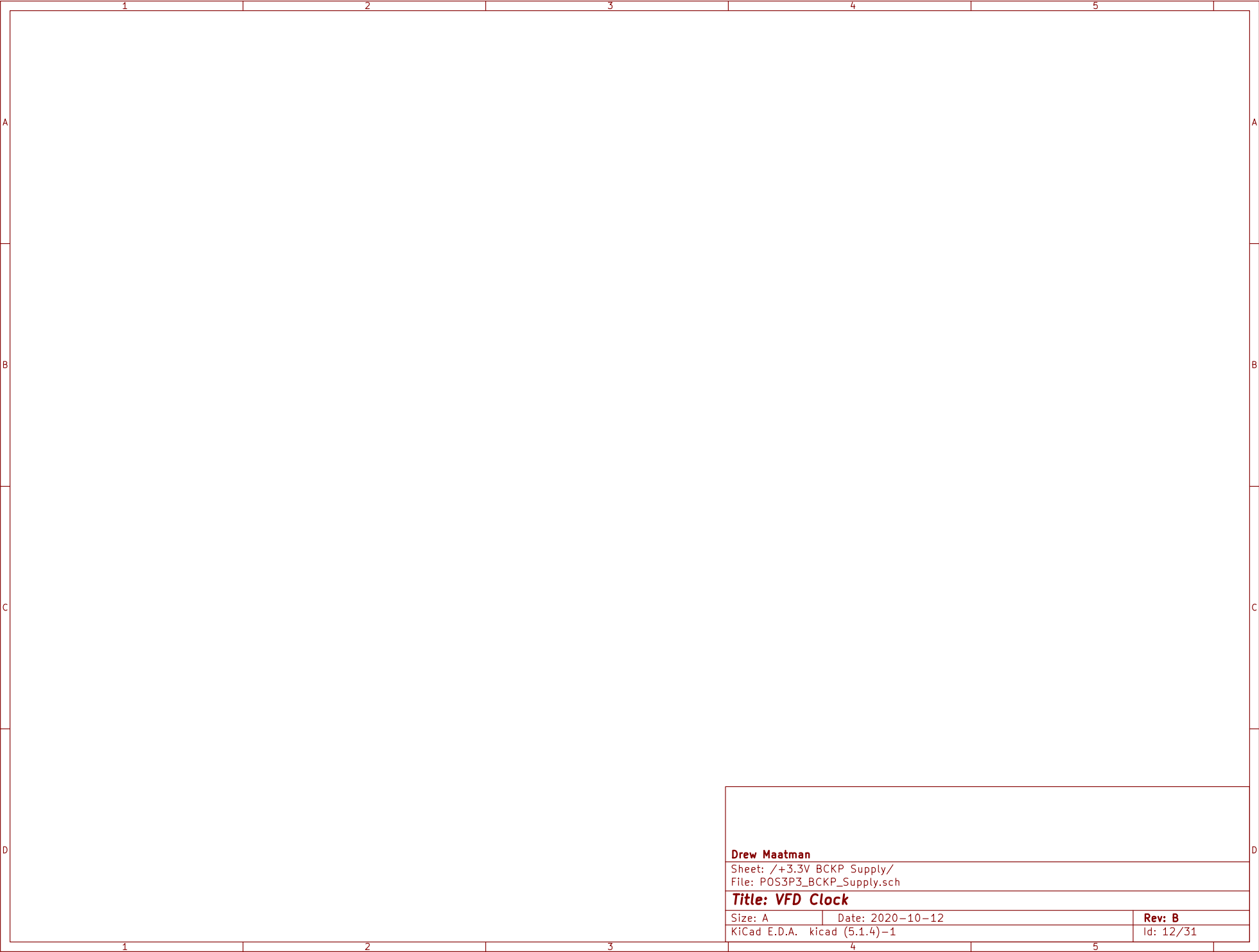
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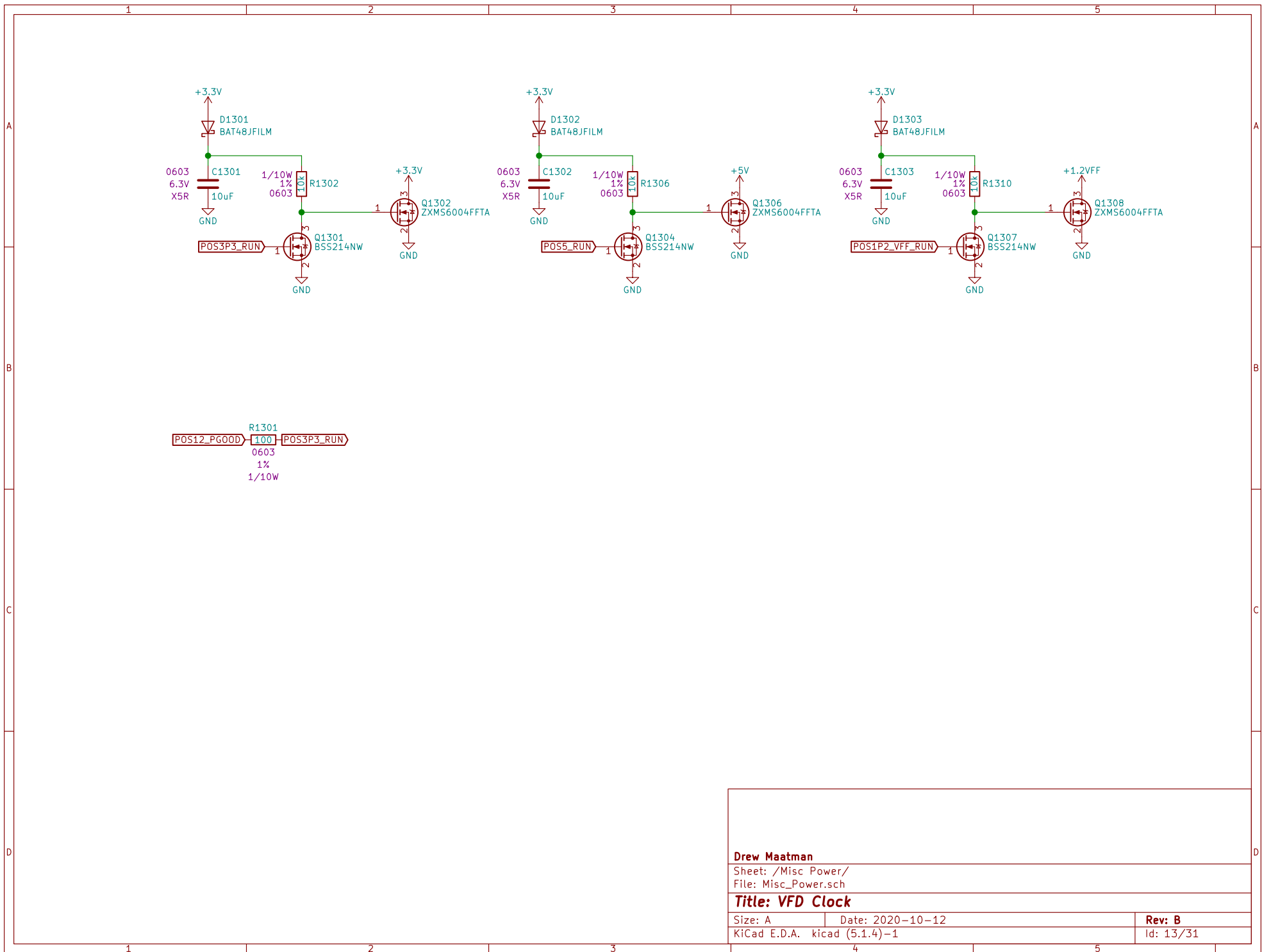
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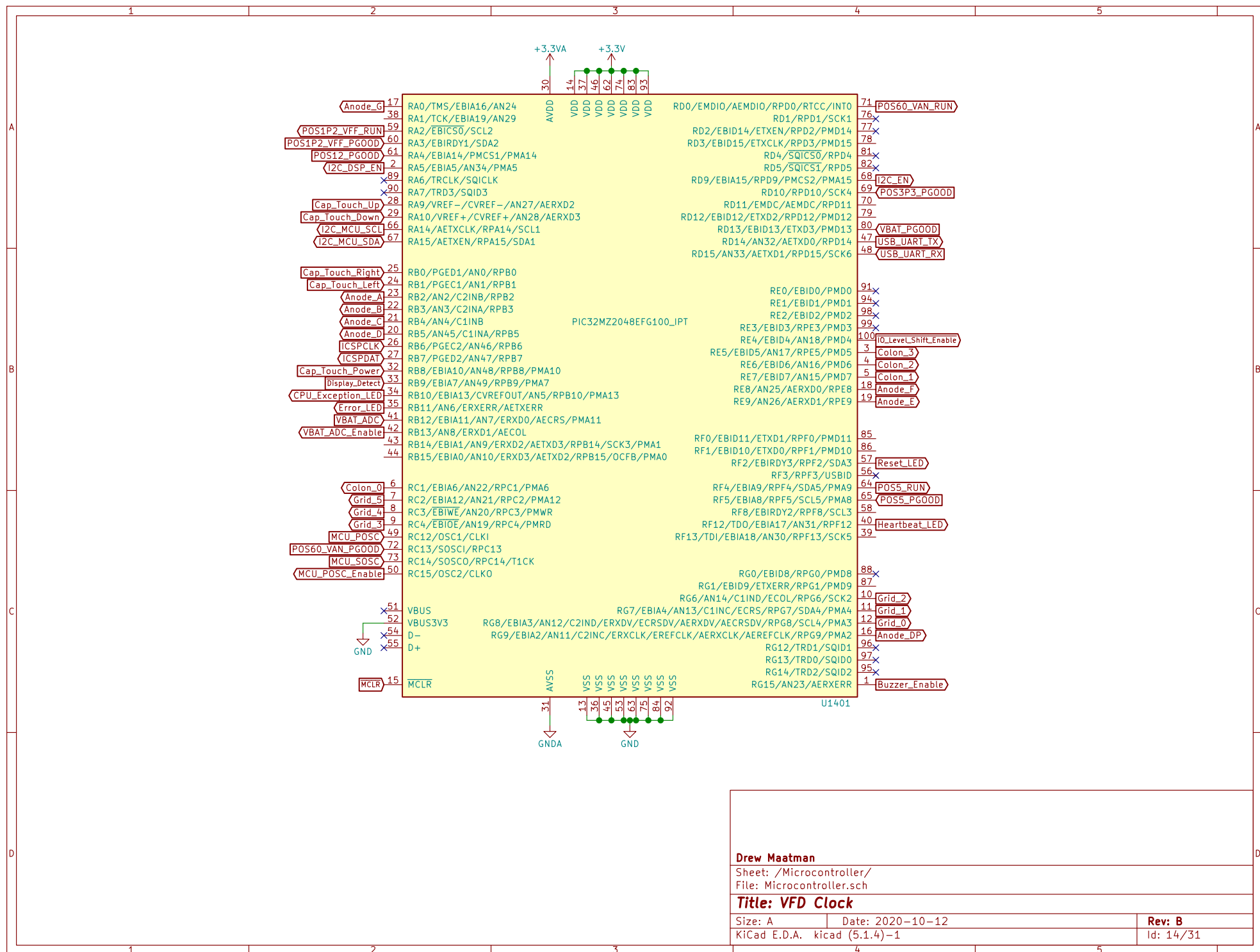
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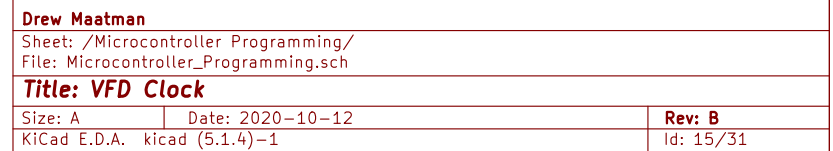
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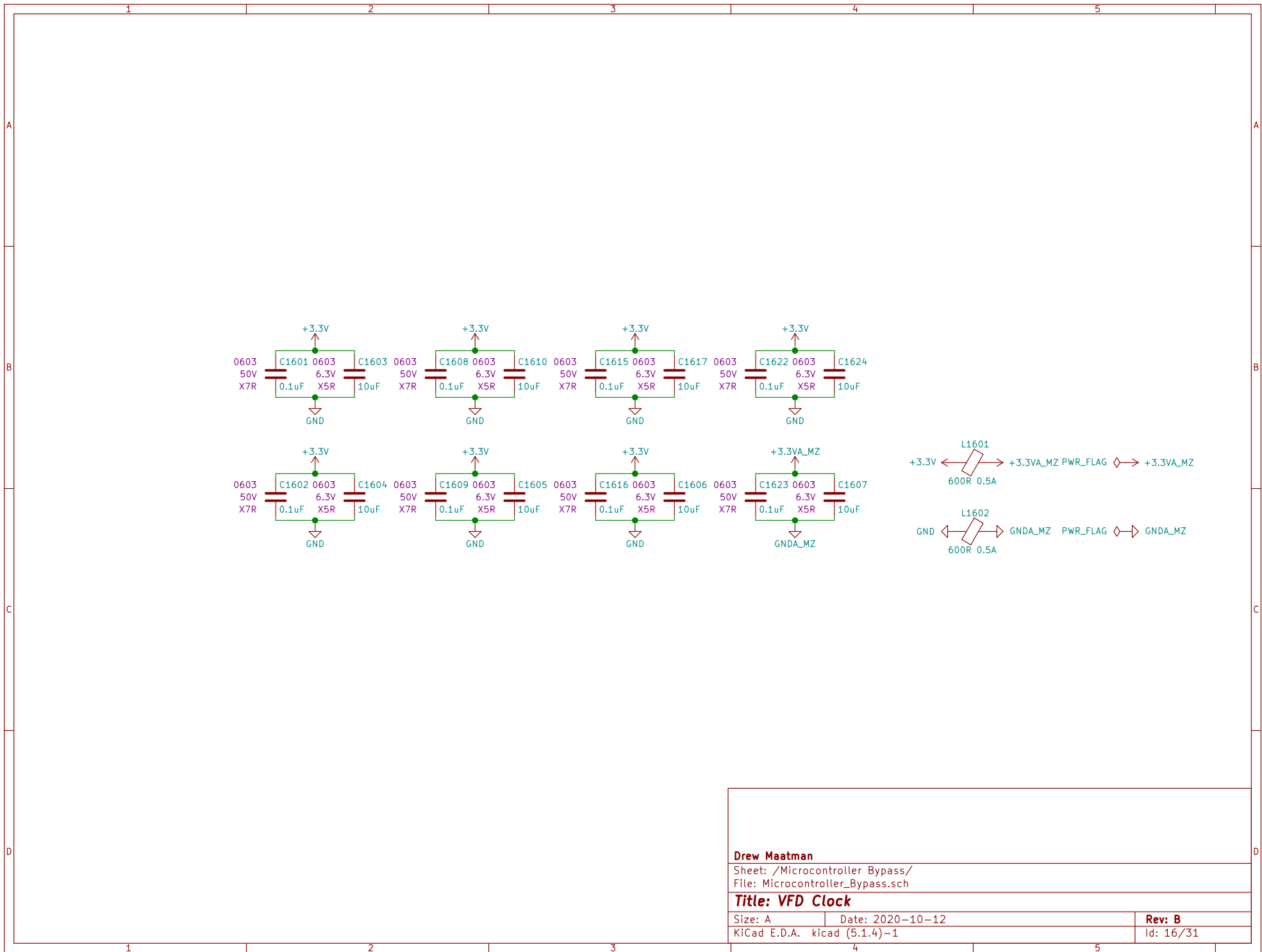
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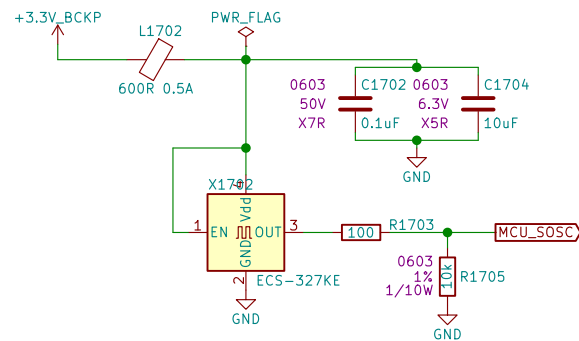
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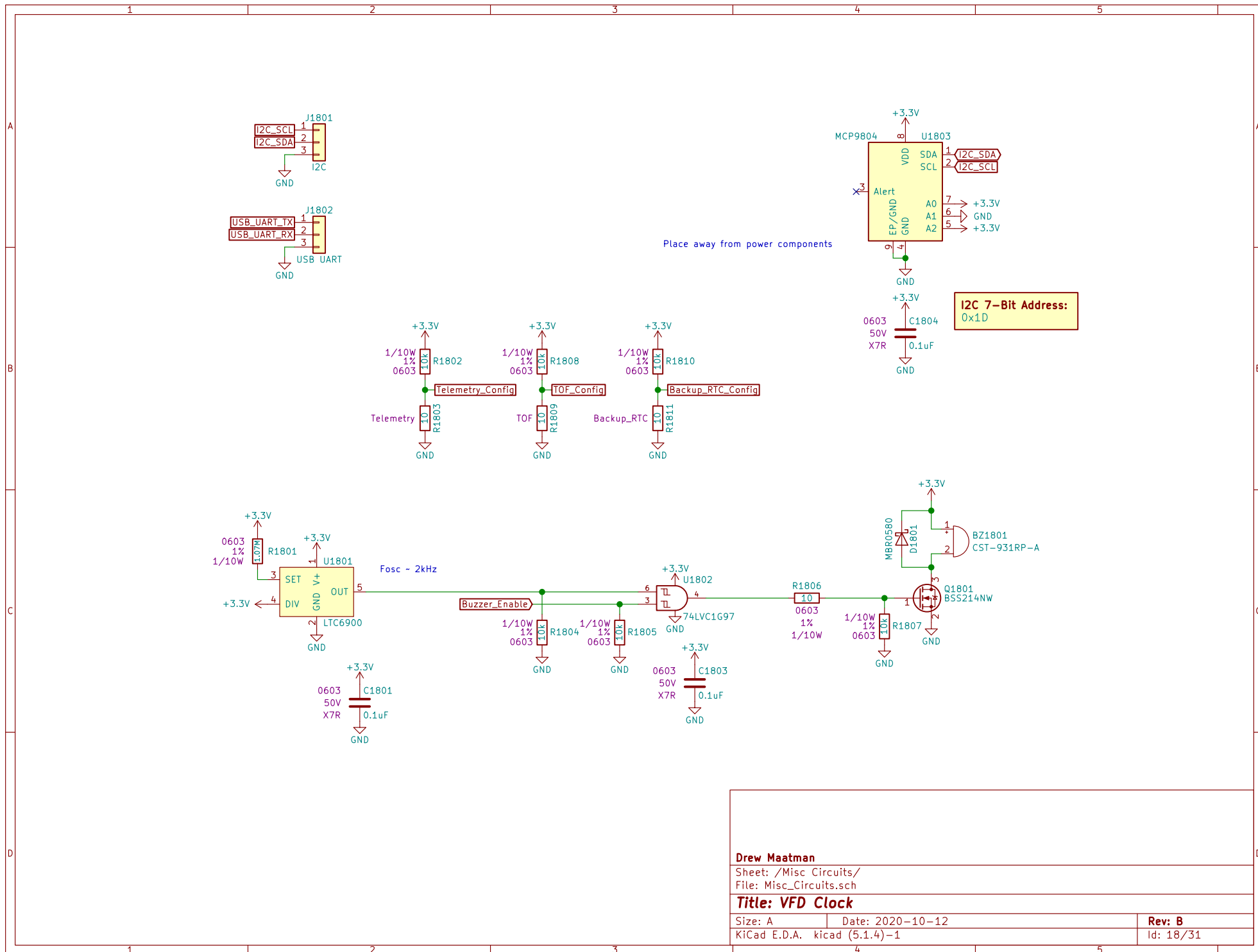








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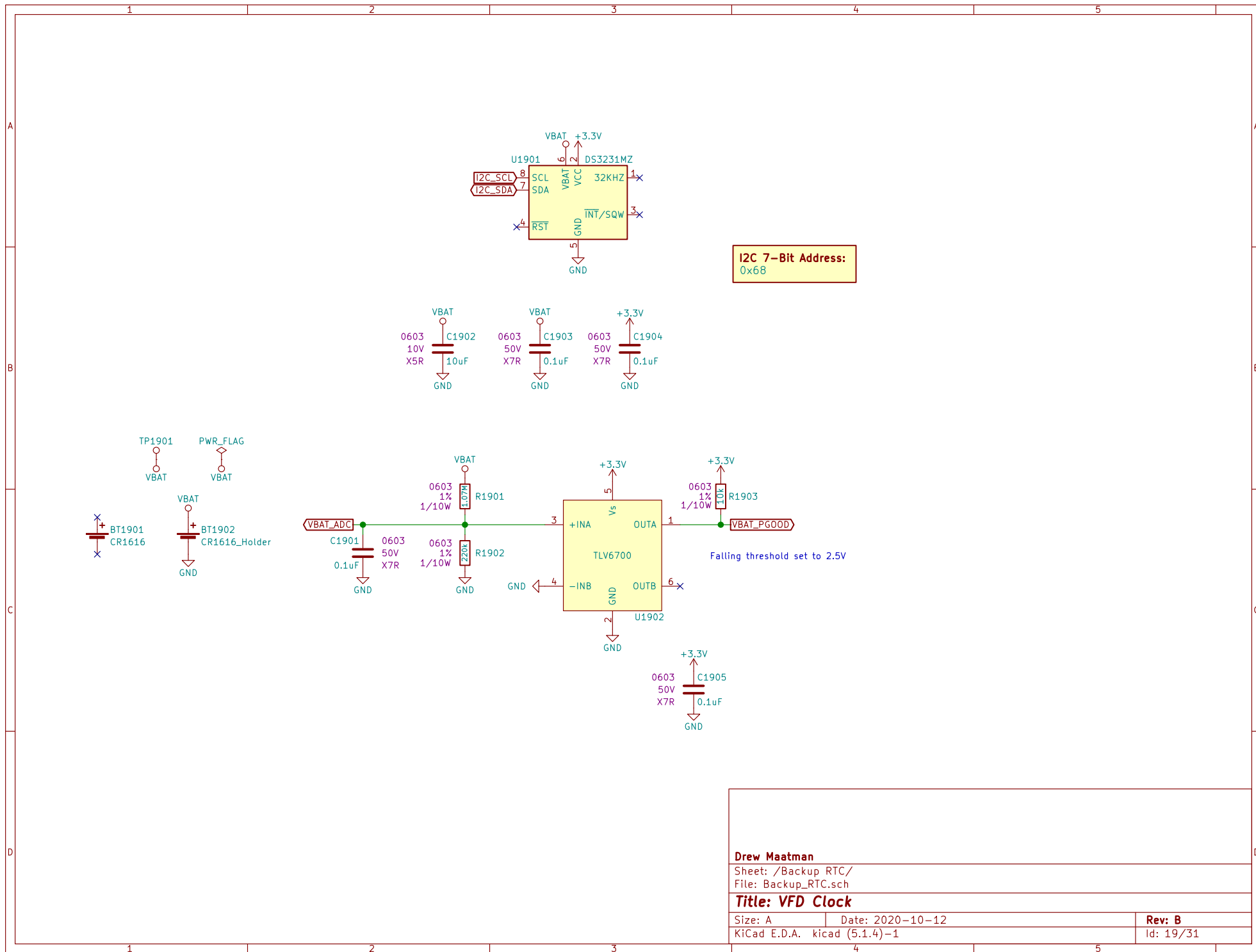
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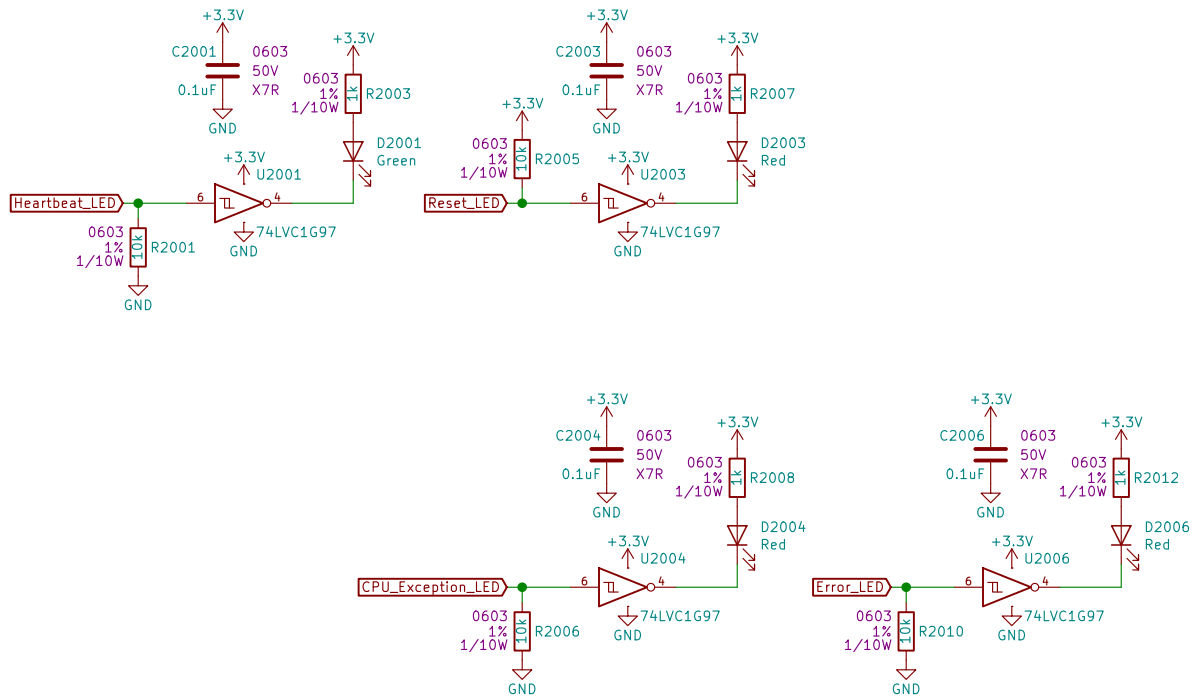
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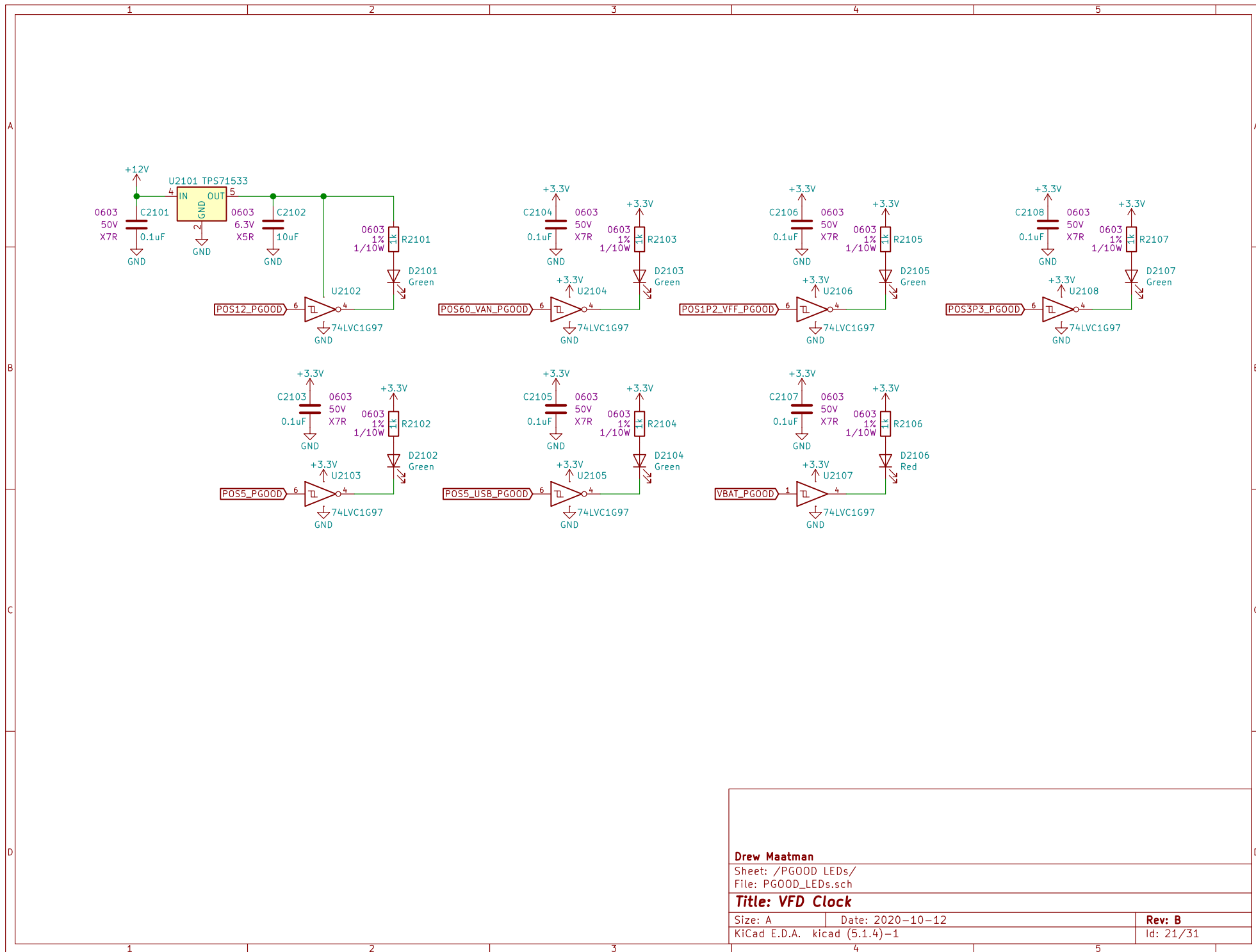
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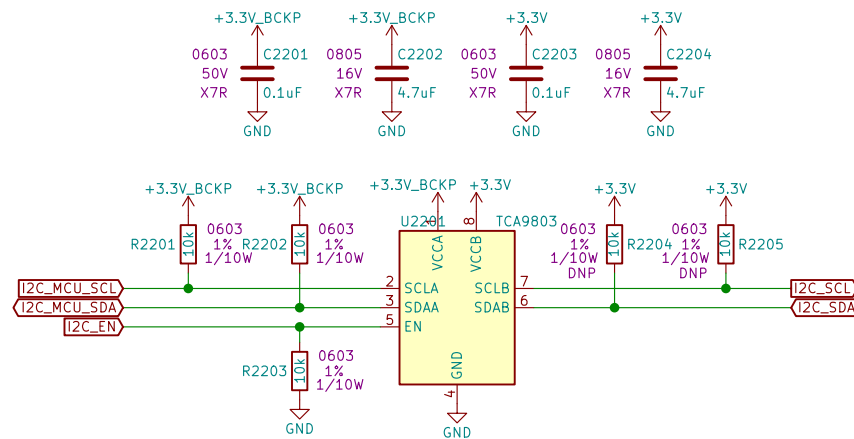
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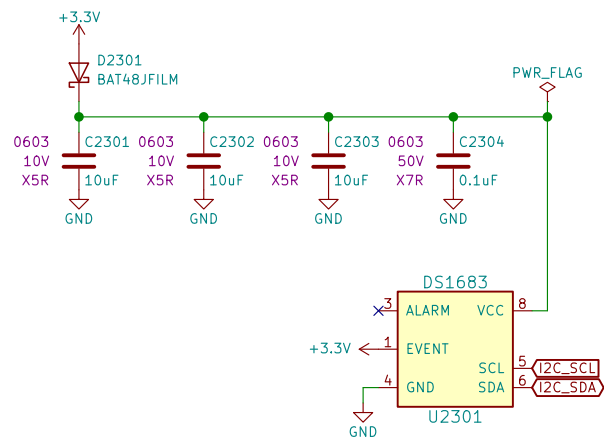
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Sheet: /I2C Buffer/
File: I2C_Buffer.sch

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I2C 7-Bit Address:
0x6B

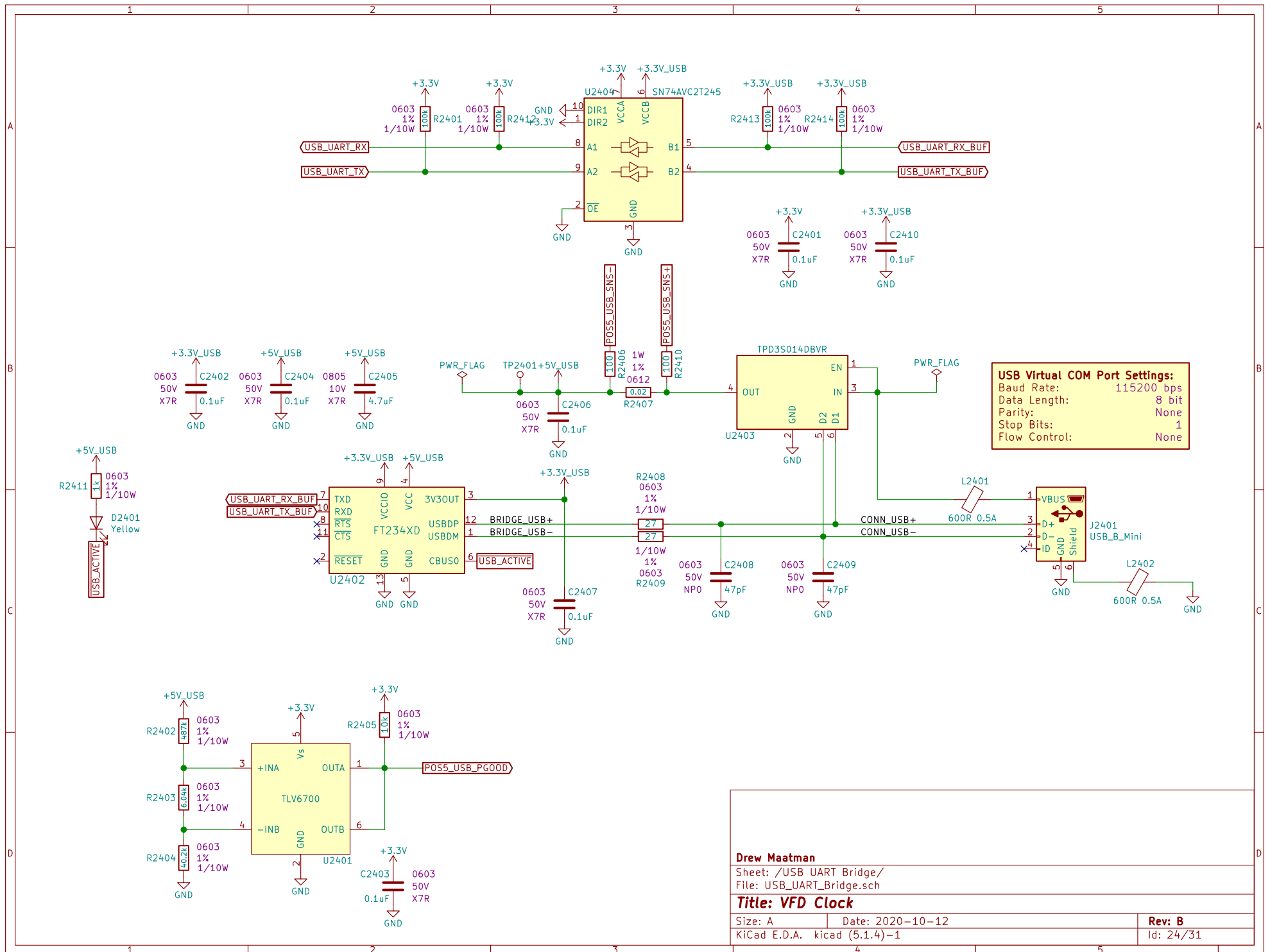
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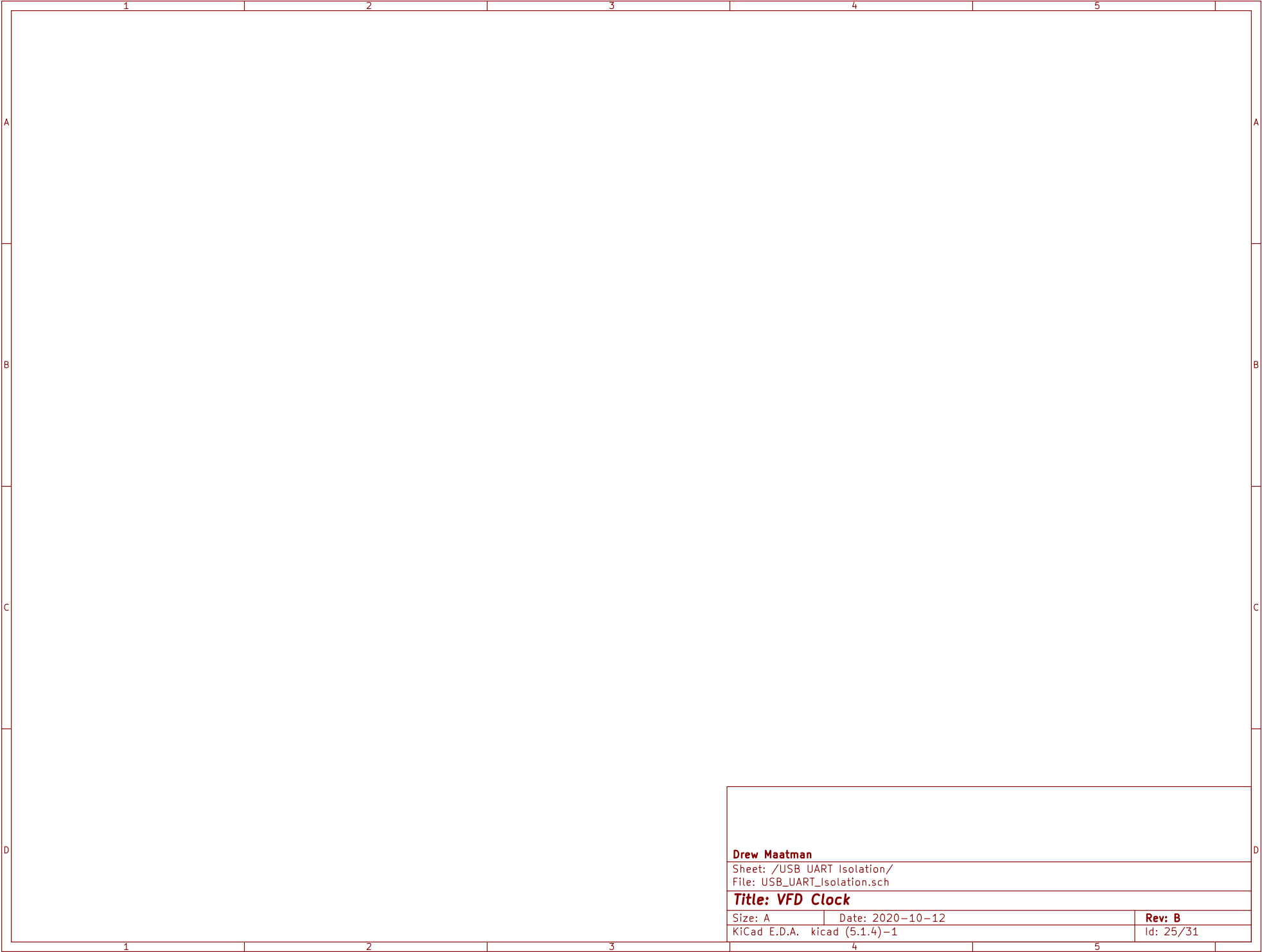
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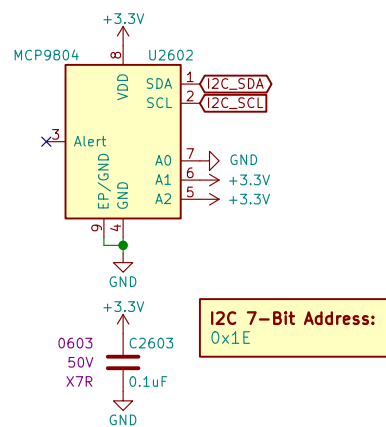
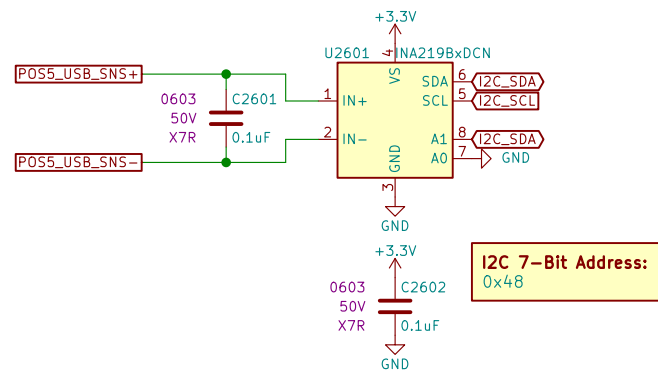
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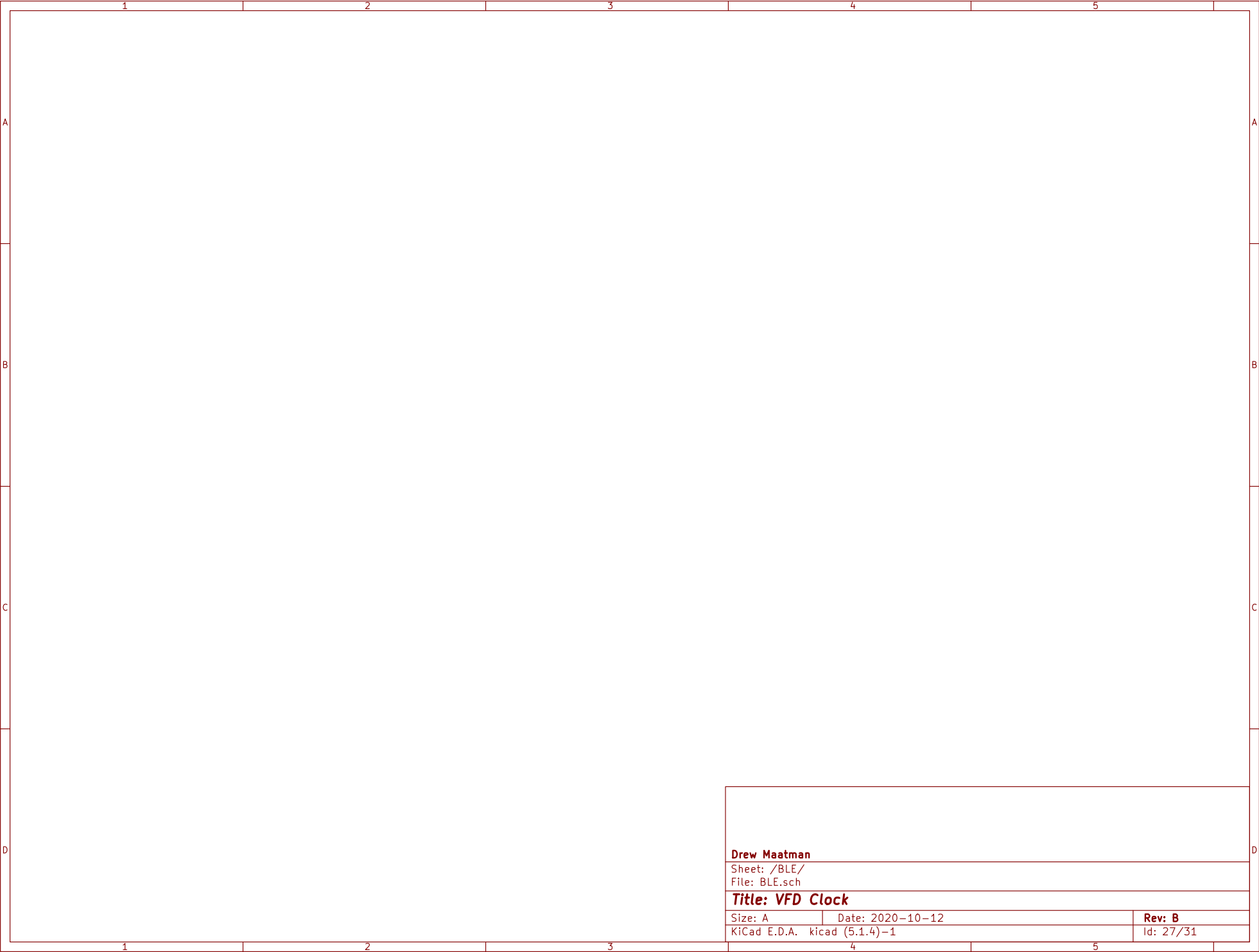
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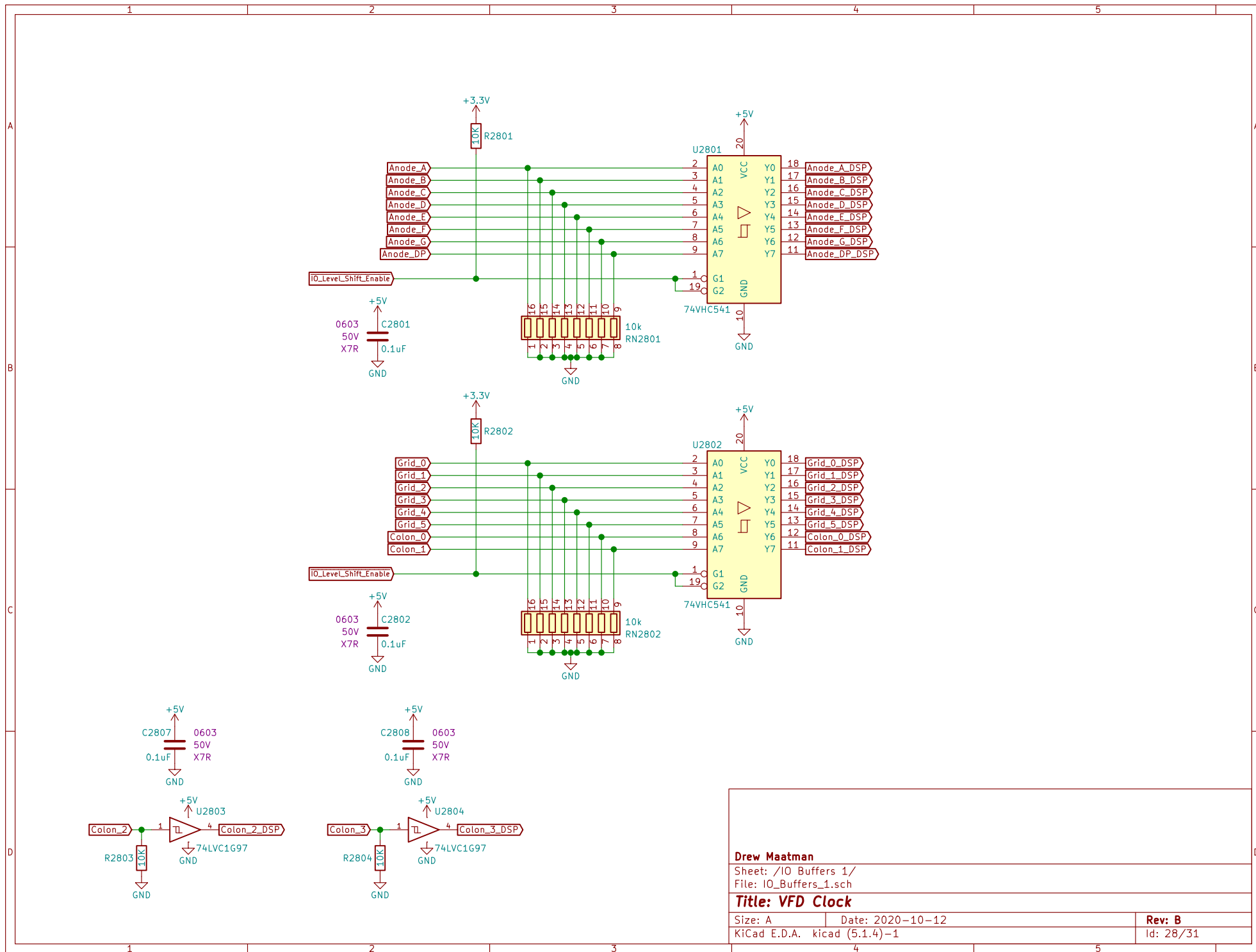
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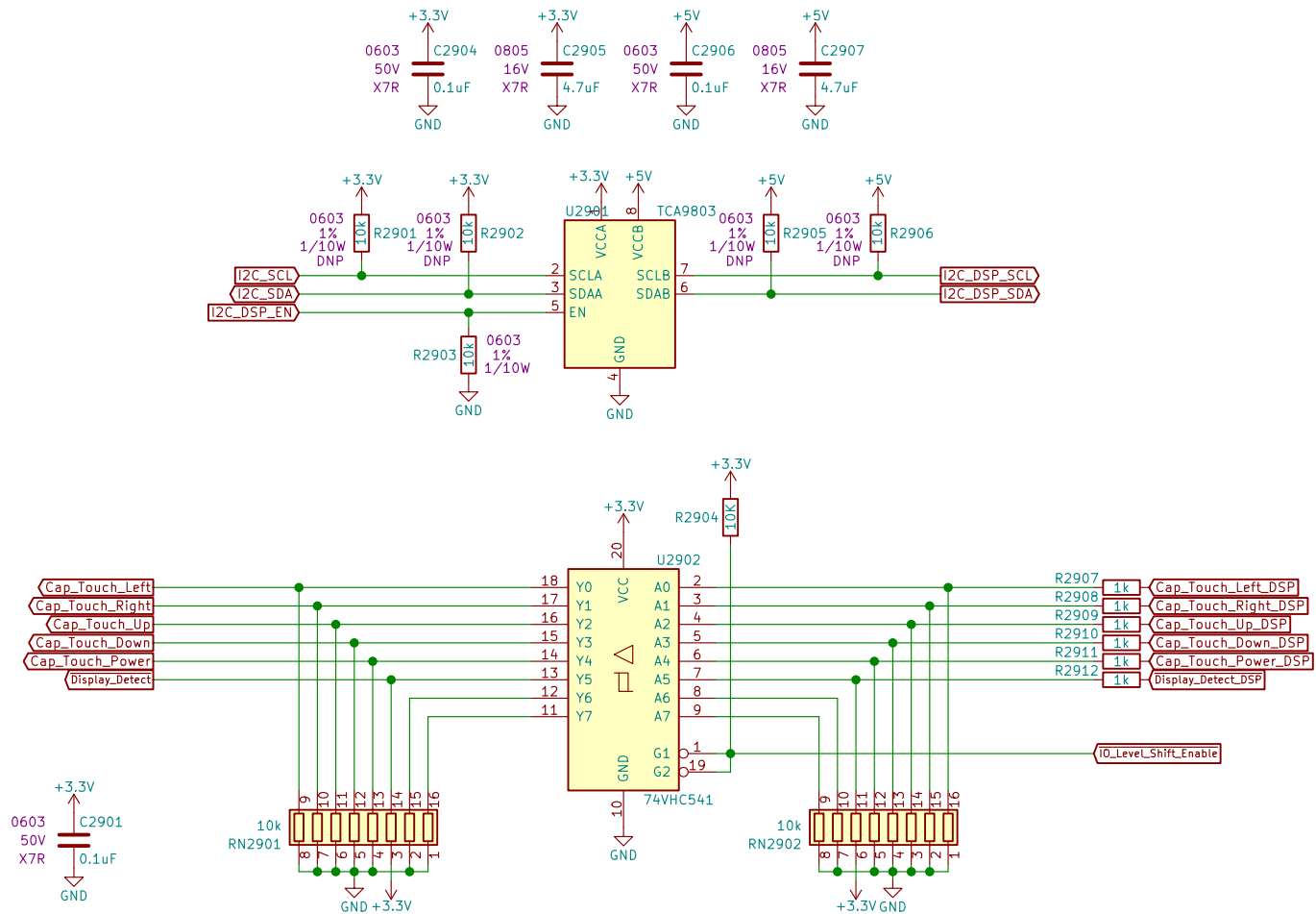
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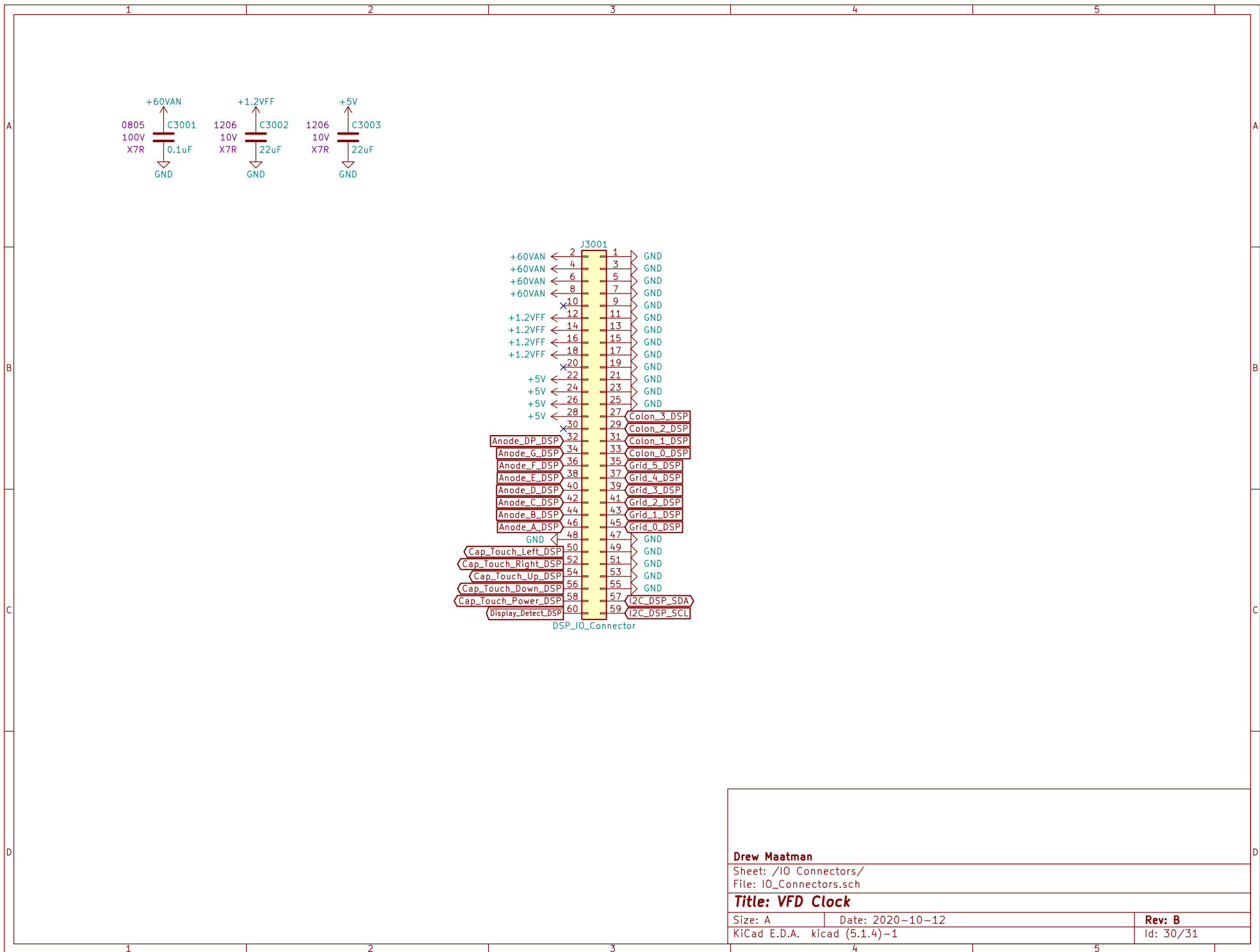
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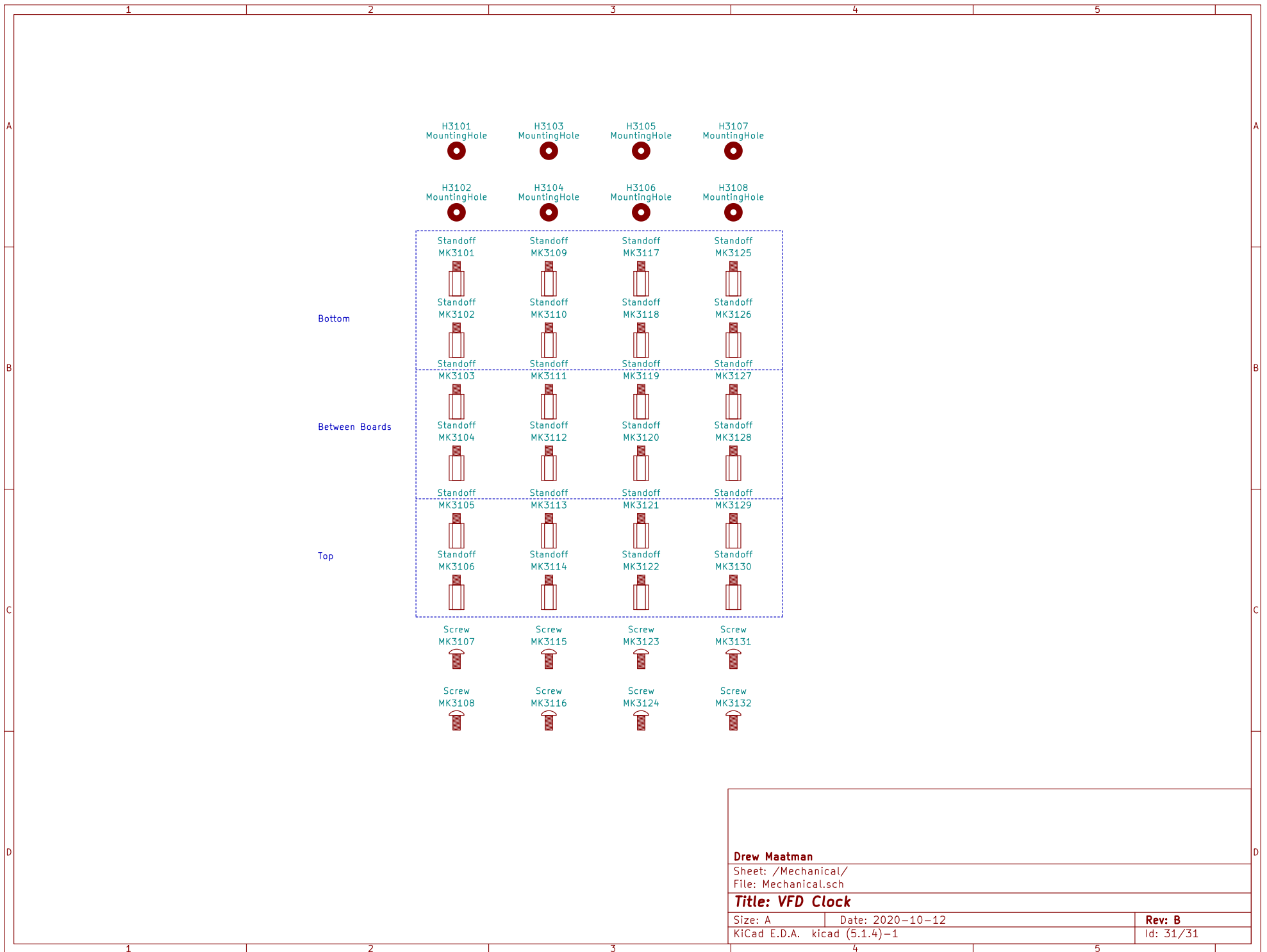
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Title: VFD Clock

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