VFD Clock Logic Board

0	1.	Tat	ole	of	Con	ten	ts
_			•••	•	••••	••••	••

02. +12V Input

03. +12V Telemetry

04. +3.3V Power Supply

05. +3.3V Telemetry

06. +5V Power Supply

07. +5V Telemetry

08. +1.2VFF Power Supply

09. +1.2VFF Telemetry

10. +60VAN Power Supply

11. +60VAN Telemetry

12. +3.3V BCKP Power Supply

13. Misc Power

14. Microcontroller

15. Microcontroller Programming

16. Microcontroller Bypass

17. Microcontroller Clocking

18. Misc Circuits

19. Backup RTC

Sheet: +12V Input

File: POS12 Input.sch

Sheet: +12V Telemetry

File: POS12_Telemetry.sch

Sheet: +3.3V Power Supply

File: POS3P3_Power_Supply.sch

Sheet: +3.3V Telemetry

File: POS3P3_Telemetry.sch

Sheet: +5V Power Supply

File: POS5_Power_Supply.sch

Sheet: +5V Telemetry

File: POS5_Telemetry.sch

Sheet: +1.2VFF Power Supply

File: POS1P2_VFF_Power_Supply.sch

Sheet: +1.2VFF Telemetry

File: POS1P2_VFF_Telemetry.sch

Sheet: +60VAN Power Supply

File: POS60_VAN_Power_Supply.sch

Sheet: +60VAN Telemetry

File: POS60_VAN_Telemetry.sch

Sheet: +3.3V BCKP Supply

File: POS3P3_BCKP_Supply.sch

Sheet: Misc Power

File: Misc_Power.sch

Sheet: Microcontroller

File: Microcontroller.sch

Sheet: Microcontroller Programming

File: Microcontroller_Programming.sch

Sheet: Microcontroller Bypass

File: Microcontroller_Bypass.sch

Sheet: Microcontroller Clocking

File: Microcontroller_Clocking.sch

Sheet: Misc Circuits

File: Misc_Circuits.sch

Sheet: Backup RTC

File: Backup_RTC.sch

20. Status LEDs

21. PGOOD LEDs

22. I2C Buffer

23. Time of Flight

24. USB UART Bridge

25. USB UART Isolation

26. USB Telemetry

27. 10 Buffers 1

28. 10 Buffers 2

29. 10 Connectors

30. Mechanical

Sheet: Status LEDs

File: Status LEDs.sch Sheet: PGOOD LEDs

File: PGOOD_LEDs.sch

Sheet: I2C Buffer

File: I2C_Buffer.sch

Sheet: Time of Flight

File: Time_of_Flight.sch

Sheet: USB UART Bridge

File: USB_UART_Bridge.sch

Sheet: USB UART Isolation

File: USB UART Isolation.sch

Sheet: USB Telemetry

File: USB_Telemetry.sch

Sheet: 10 Buffers 1

File: IO_Buffers_1.sch Sheet: 10 Buffers 2

File: 10 Buffers 2.sch Sheet: 10 Connectors

File: 10_Connectors.sch Sheet: Mechanical

File: Mechanical.sch

Consider adding net ties on all current sense shunt resistors for net isolation

Drew Maatman

Sheet: /

File: VFD_Clock.sch

Title: VFD Clock

Date: 2019-04-11 KiCad E.D.A. kicad (5.1.4)-1

Rev: A ld: 1/30

























































