

Vacuum Fluorescent Display Clock

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Input Overvoltage/Undervoltage/Reverse Polarity Lockout

VIN:
MIN: 22V,
NOM: 24V,
MAX: 26V

J201
1
3
2
Power Input
GND

F201
C1Q 2
1206
2A

Q201
Si2319CDS

MMSZ5240BS-7-F
SOD323
10V
Z201

1/10W 1% 0603 R202
GND

+3.3V
0603 50V X7R C203
0.1uF
GND

ACS722xLCTR-05AB
U201
IP+
VOUT
IP-
BW_SEL
GND
5
6
7
8
+3.3V

PWR_FLAG

PMT560ENEA
Q202
4
2
1
3
+24V

0603 50V X7R C209
10nF
GND

+3.3V
1/10W 1% 0603 R209
10k

POS24_PG00D

11
6
5
A
BYP
UV
OV
RSET
RSVD
GND
14
4
3
PG
FLTB
STAT
C
VDD
10
1
TPS2411PWR
U203
1/10W 1% 0603 R205
15k
1/10W 1% 0603 R206
15k
1/10W 1% 0603 R207
3.7k
1/10W 1% 0603 R208
1k
GND

+3.3V_MNG
0603 50V X7R C201
0.1uF
0603 50V X7R C202
10nF
0603 50V X7R C204
1nF
GND

U202
NCS325
3
4
15
V+
V-
2
GND

1/10W 1% 0603 R201
10k
GND

0603 1% 1/16W R203
0
GND

1k R204
0603 1% 1/16W
GND

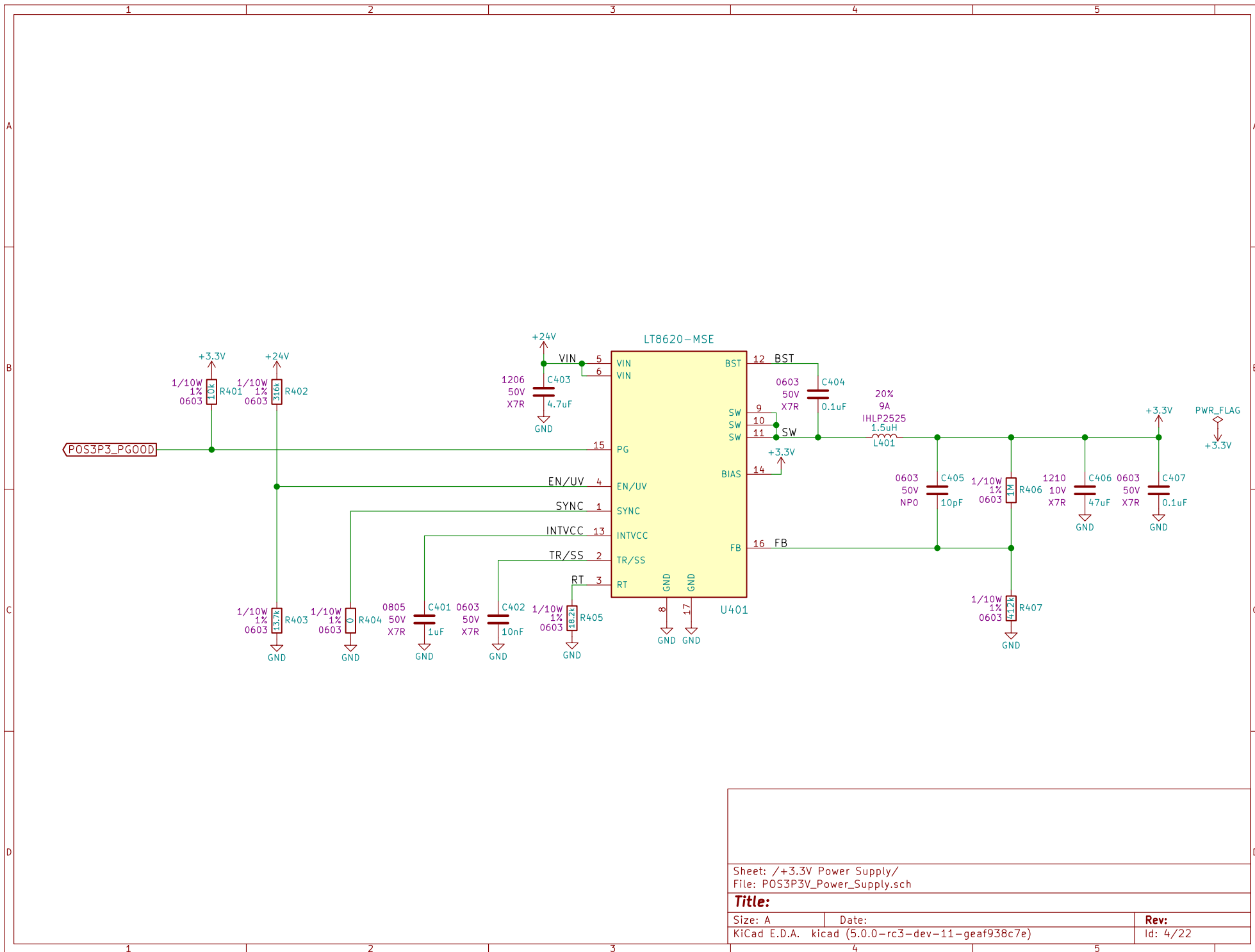
POS24_CS_ADC

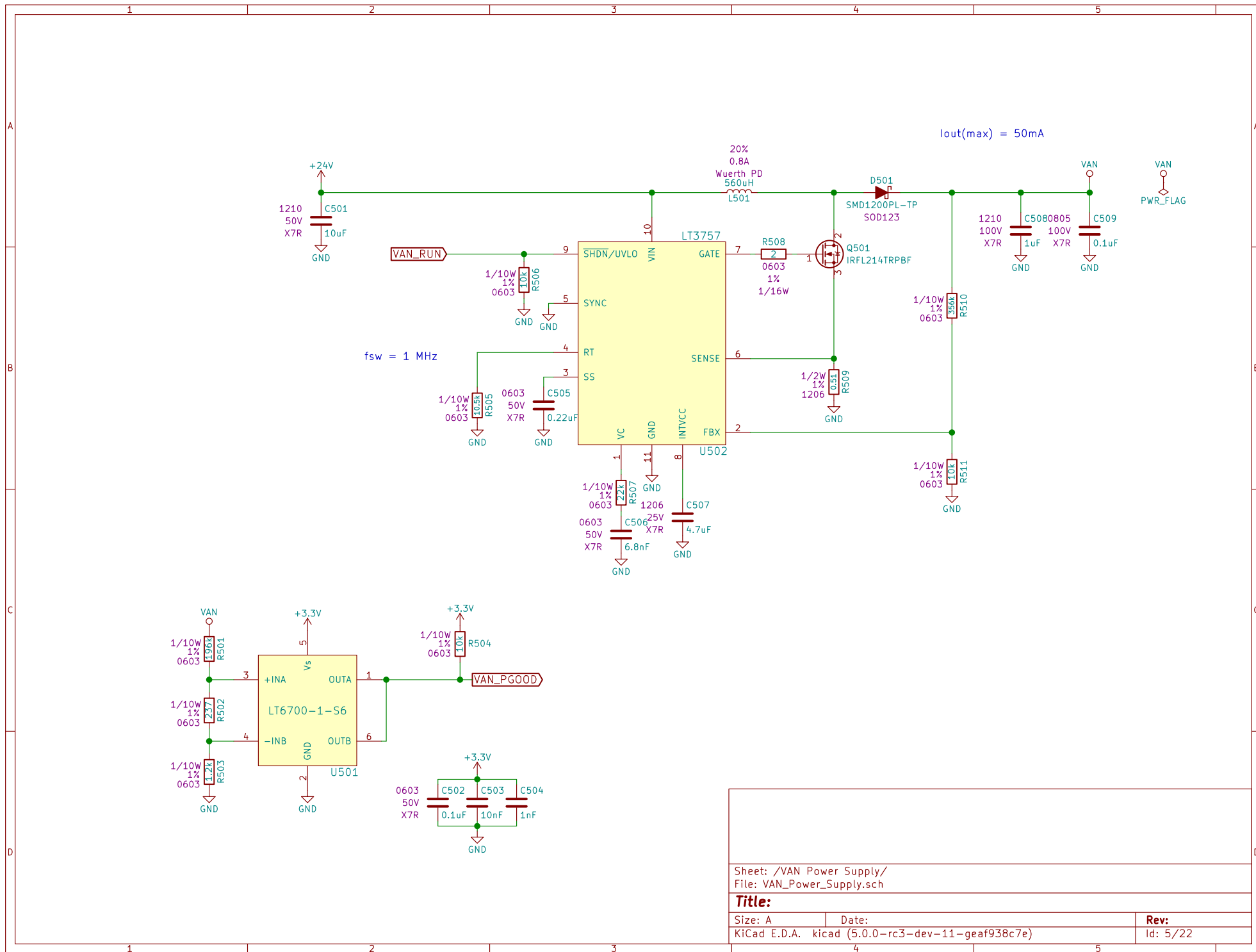
0603 50V X7R C206
0.1uF
GND

PWR_FLAG
+24V

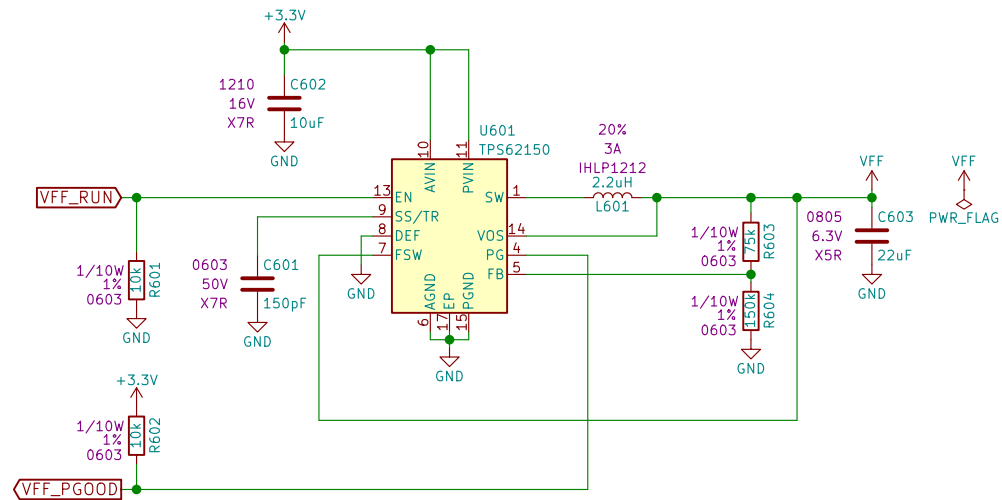
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+1.2V Filament Voltage Power Supply



Sheet: /VFF Power Supply/
File: VFF_Power_Supply.sch

Title:

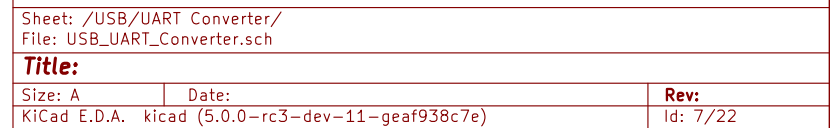
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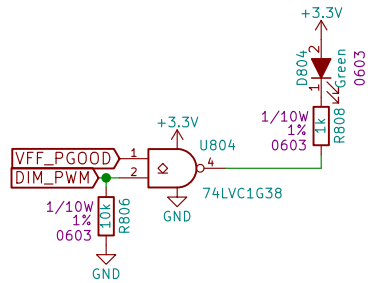
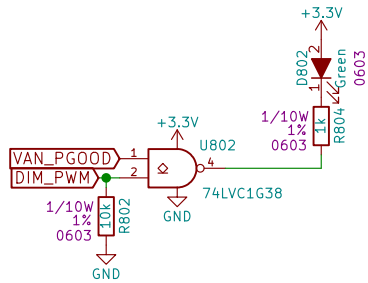
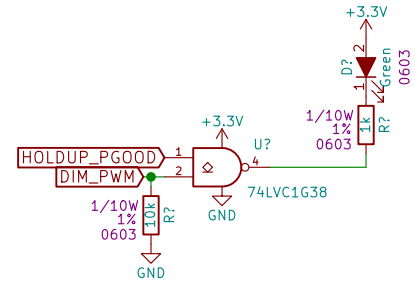
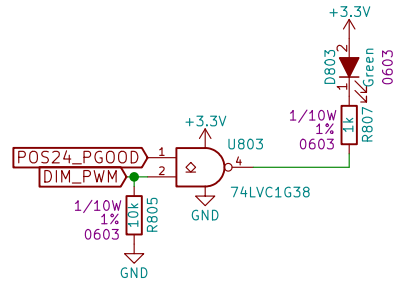
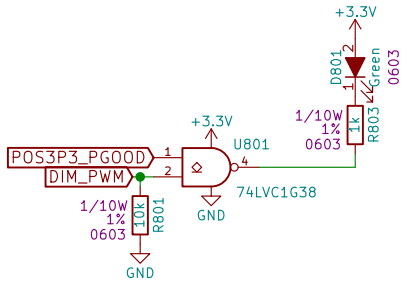
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Sheet: /PGOOD Indicators/
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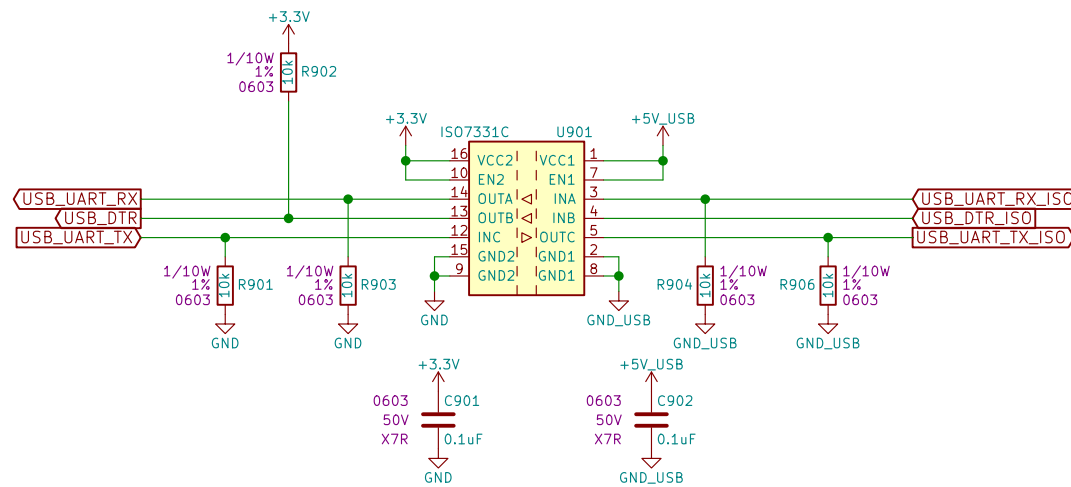
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Sheet: /UART Isolation/
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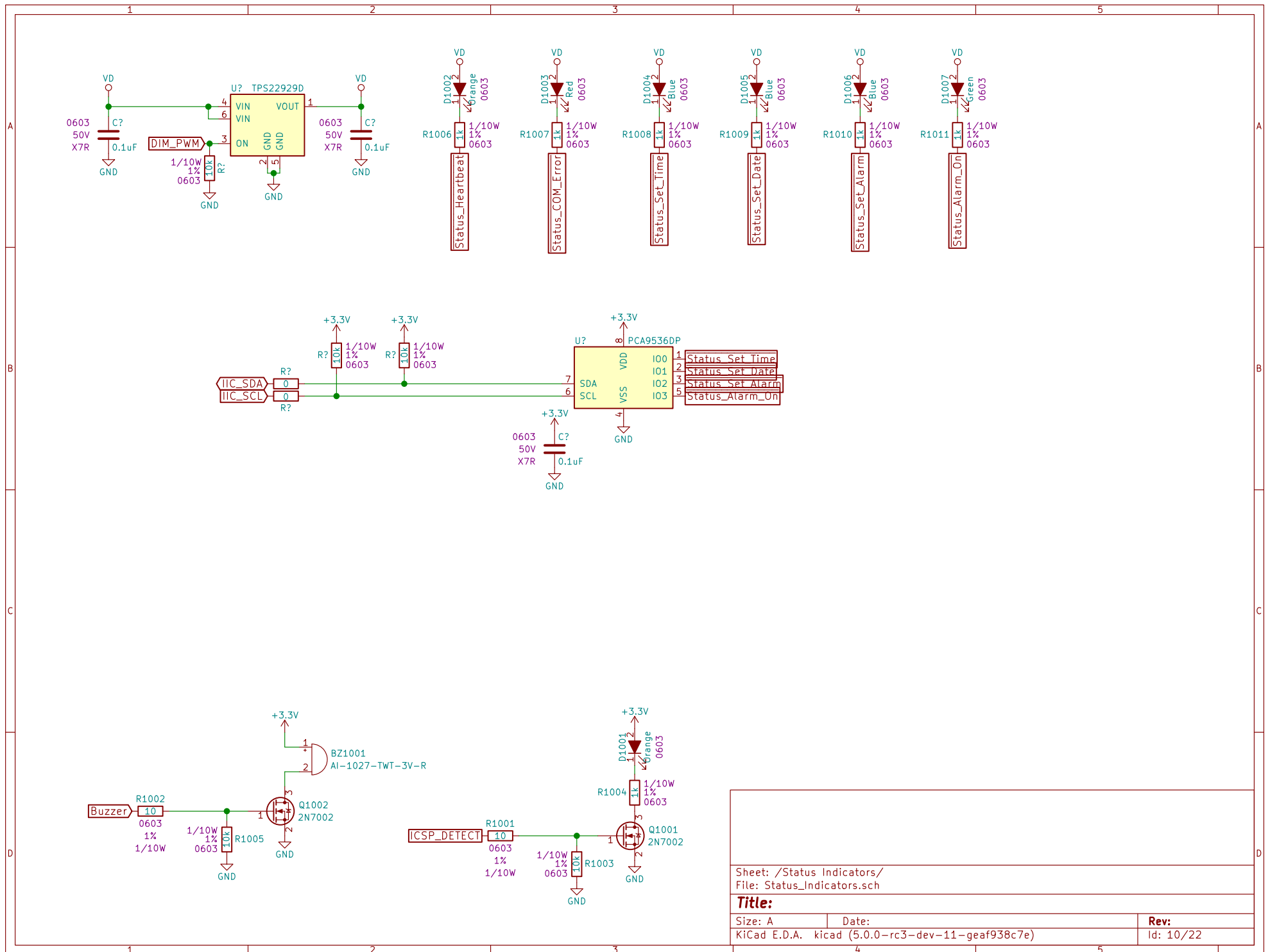
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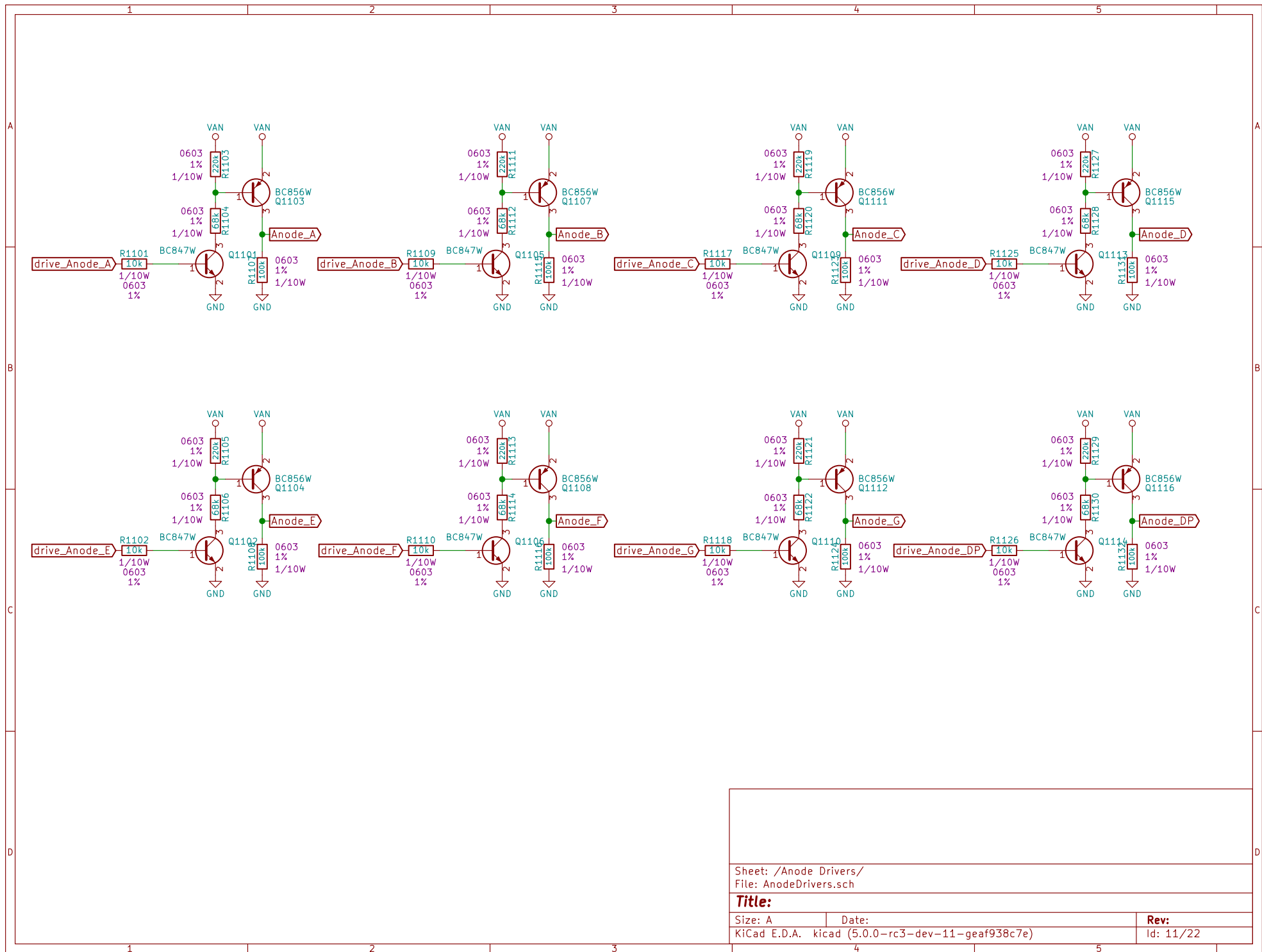
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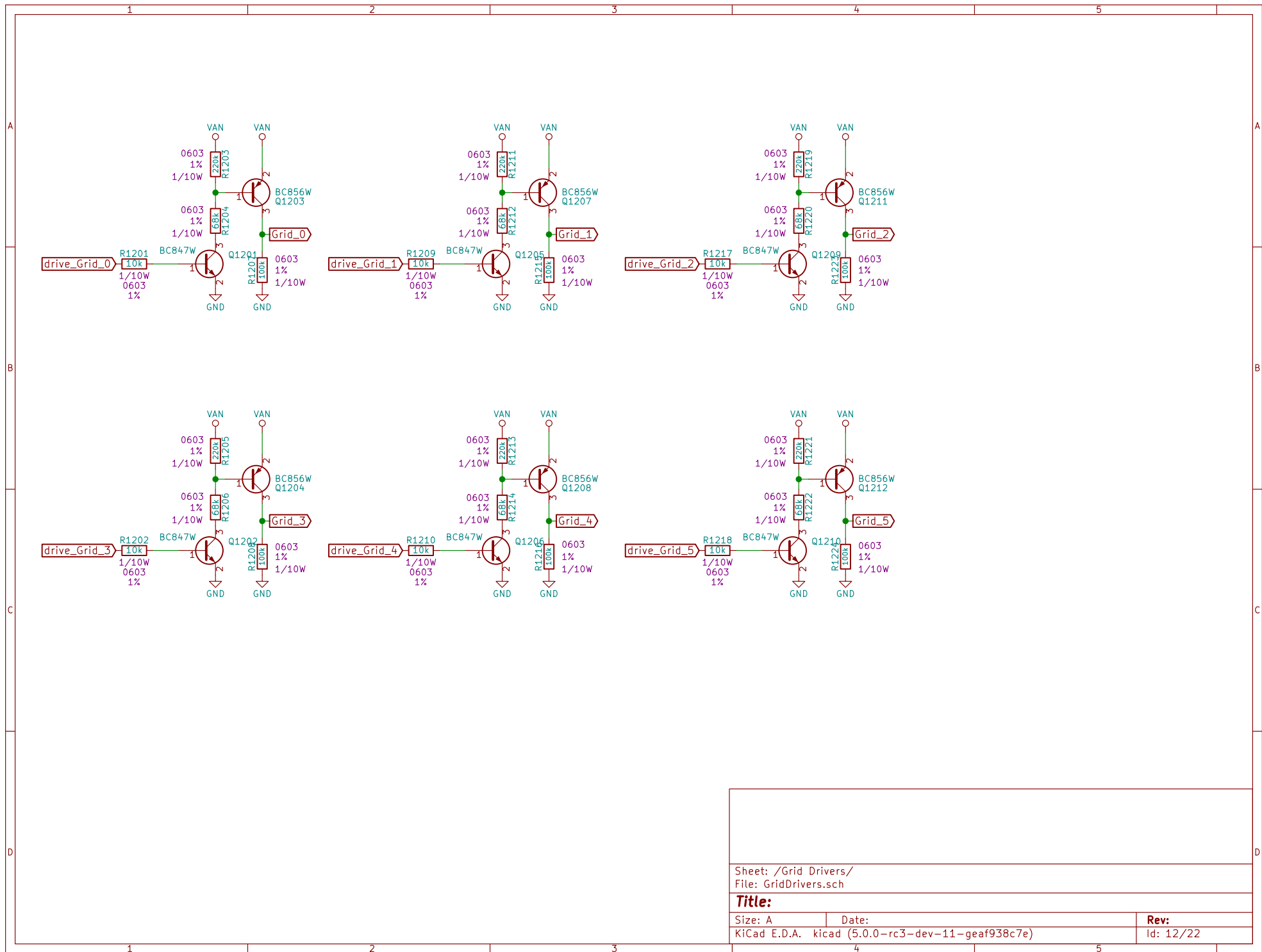
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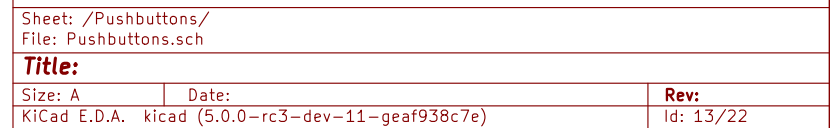
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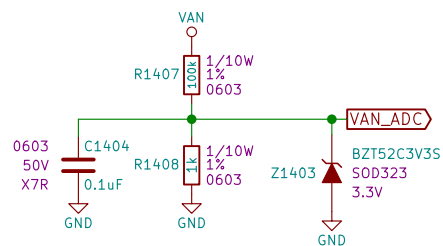
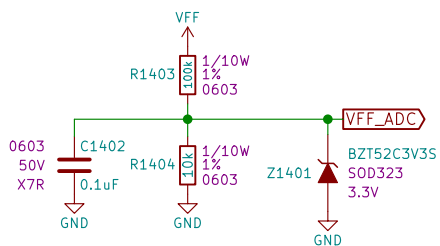
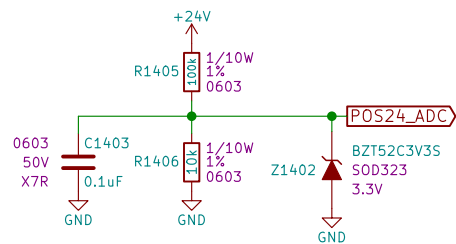
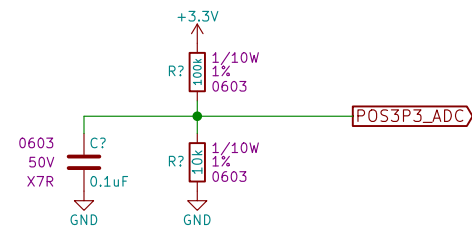


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Sheet: /Analog Inputs/
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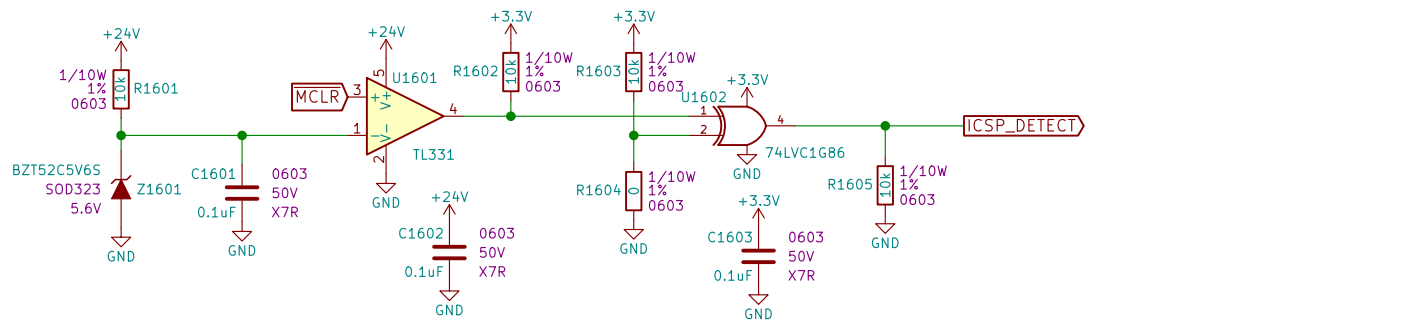
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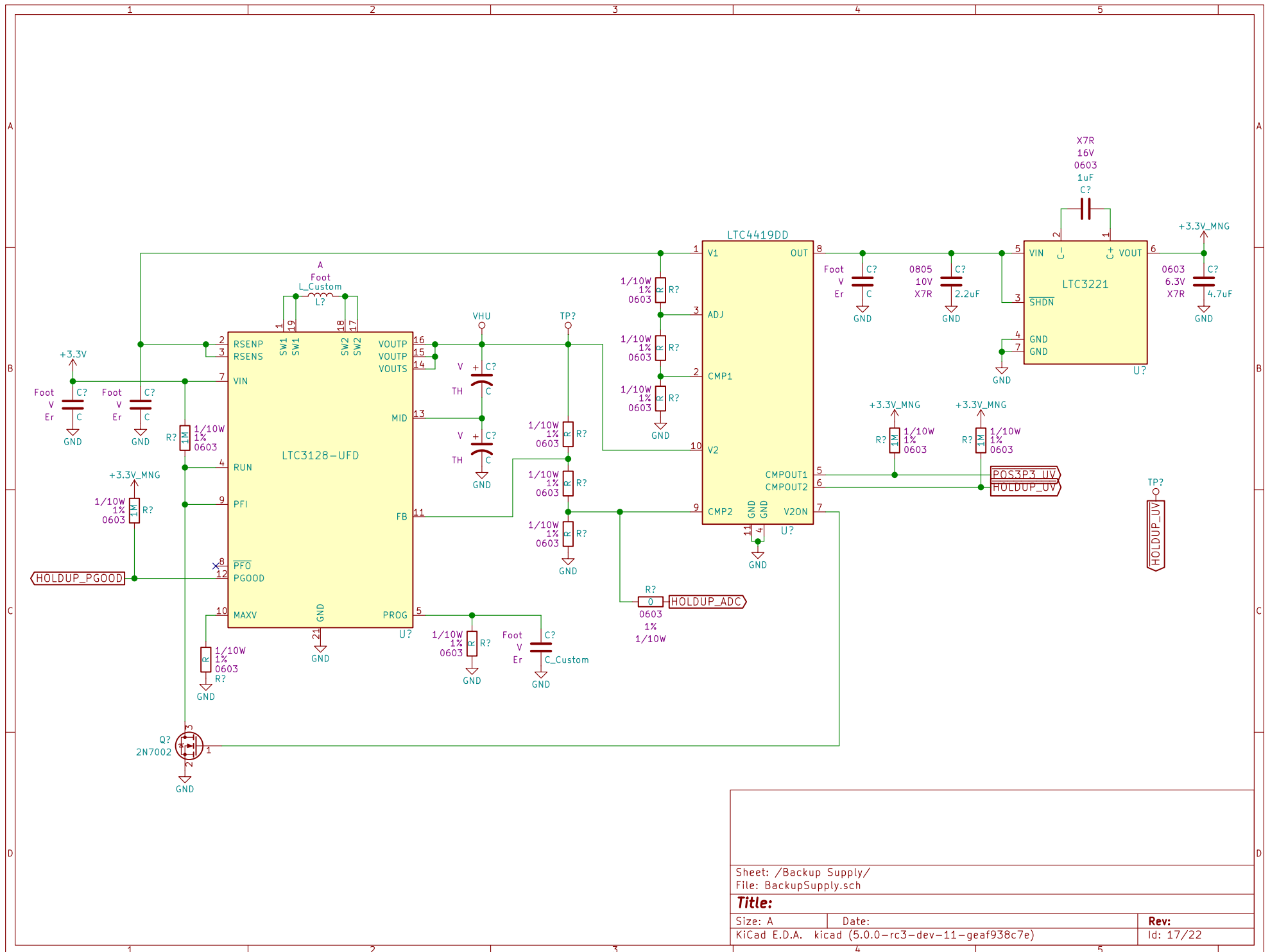
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	1	2	3	4	5	
A	<div><ul style="list-style-type: none">* All of PORTC should be configured as open drain outputs for sinking current from LEDs* All of PORTA is used for ADC measurments/external oscillator* Most of PORTD is used for driving grid switches, can be set up as a 6 bit structure located at the PORTC memory address* All of PORTF is used for driving anode switches, so smae thing as above with 8 bits* All of PORTE is used for measuring pushbuttons so all should be set as inputs with IOC's enabled* Same with PORTB except with only 6 bits, and used for generic input logic signals* RD0 and RD1 should be routed to SMT1 and SMT2 so that the signal measurement timers can be used to measure diplsay update rate and refresh rate* RG3 should be configured as an output for a CCPx module to produce a PWM signal for dimming LEDs on the board. Display itself will be dimmed in software by adjusting the on time* EUSART5 must be used for USB serial communication since only this module can be routed to PORTG* RG0 and RG1 will be the inputs to CCPx modules to measure the switching frequency (maybe duty cycle as well) of VFF and VAN supplies* RG4 should be set/cleared every other display refresh cycle, and the input snese ciruit queued when cleared, so that energy/charge per display cycle can be measured* While operating, the external 16MHz clock with internal 4x PLL will yield an FOSC of 64MHz. While the display is off, FOSC should be switched to something slower internally to minimize power consumption</div>					A
B						B
C						C
D	<div><div></div><div>Sheet: /Firmware Notes/ File: FirmwareNotes.sch</div><div>Title:</div><div><div>Size: A</div><div>Date:</div><div>Rev:</div></div><div>KiCad E.D.A. kicad (5.0.0-rc3-dev-11-geaf938c7e) Id: 15/22</div></div>					D
	1	2	3	4	5	



Sheet: /Programming/ File: Programming.sch		
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Sheet: /Backup Supply/
File: BackupSupply.sch

Title:

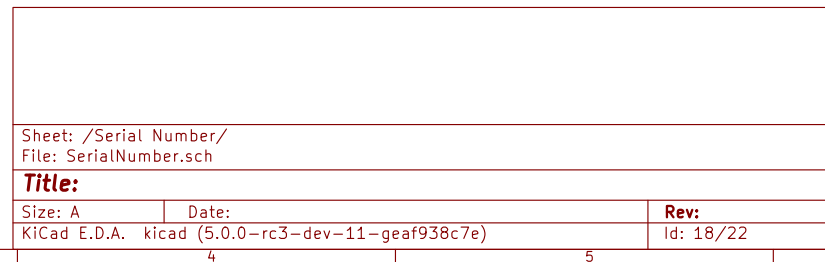
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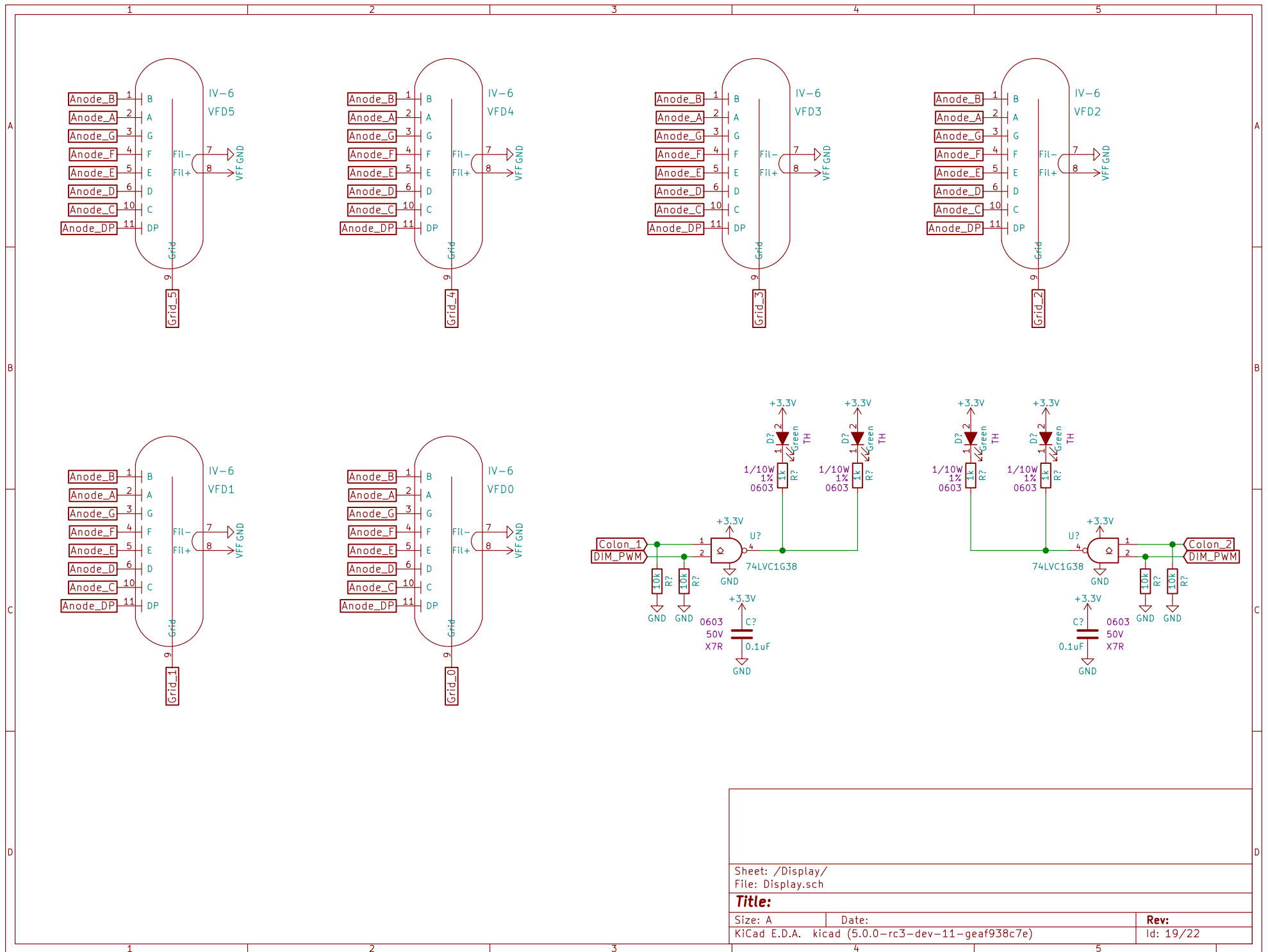
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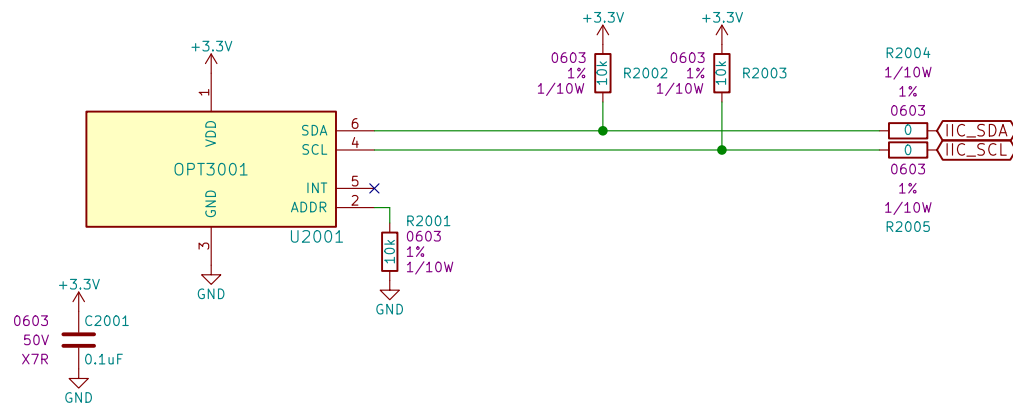
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Sheet: /Ambient Light Sensor/
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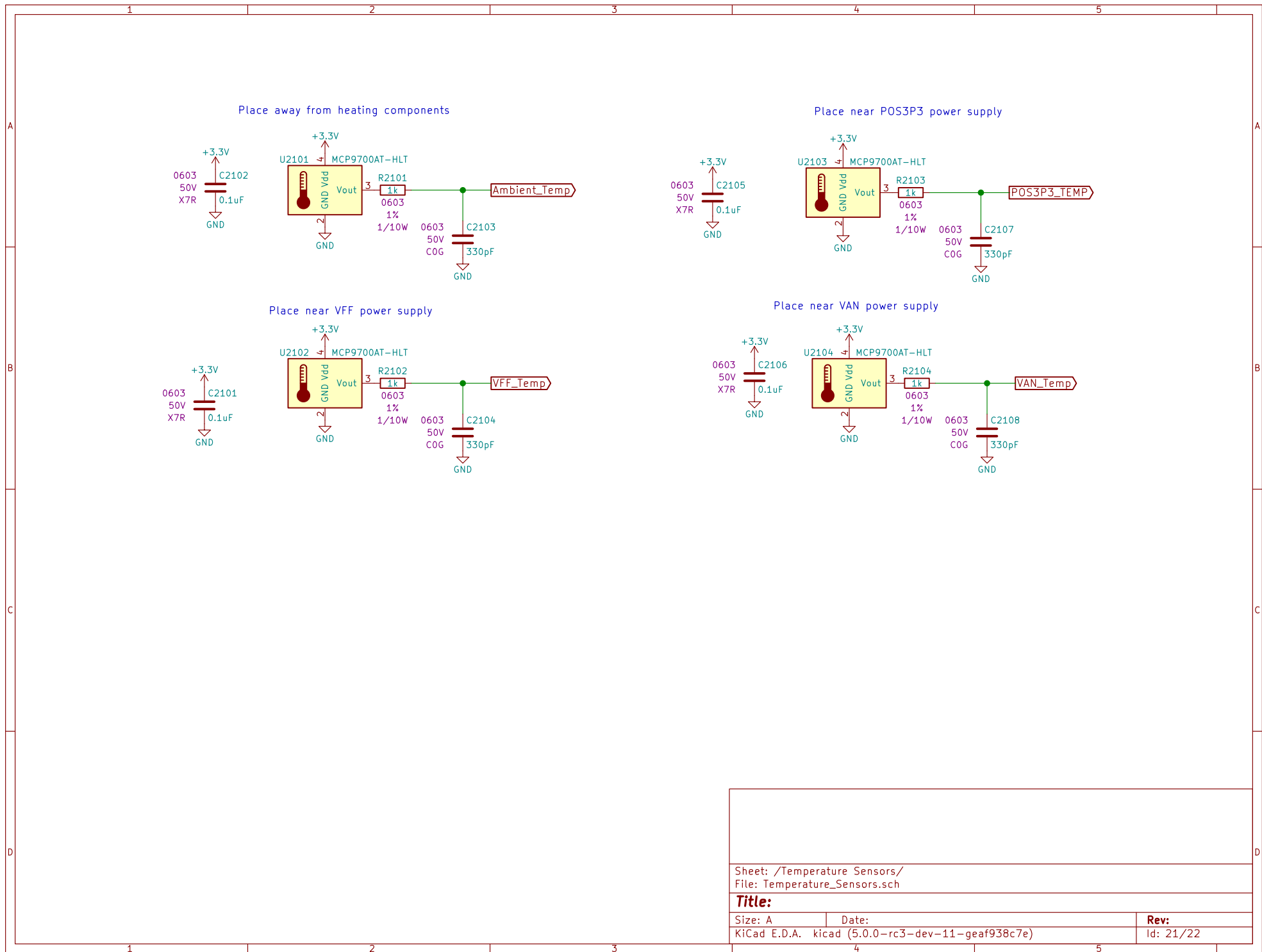
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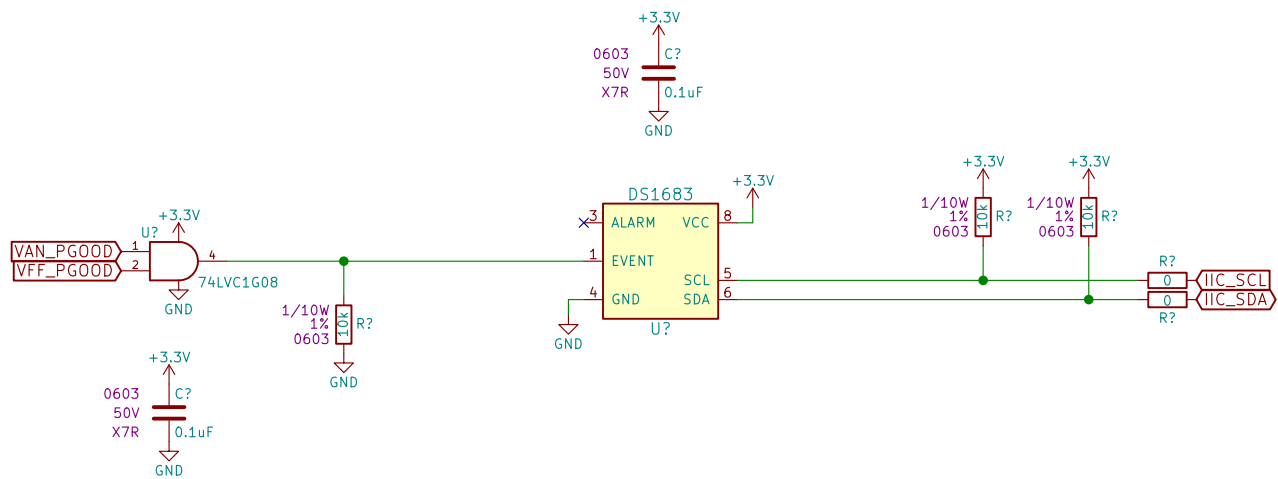
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Sheet: /Tube Life Timer/
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