

VFD Clock Logic Board

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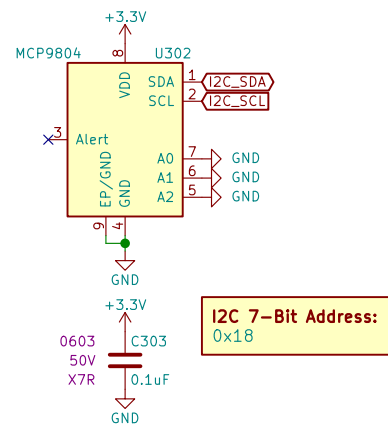
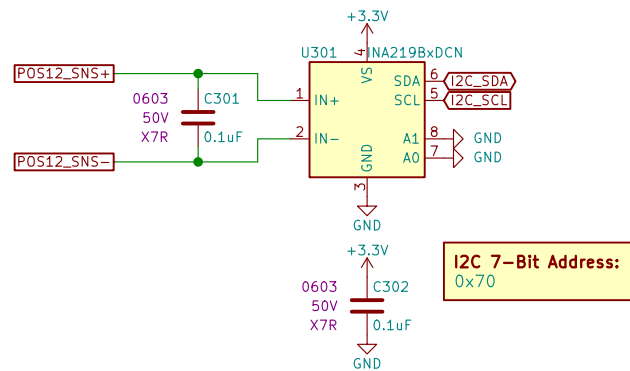
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Size: A Date: 2019-04-11

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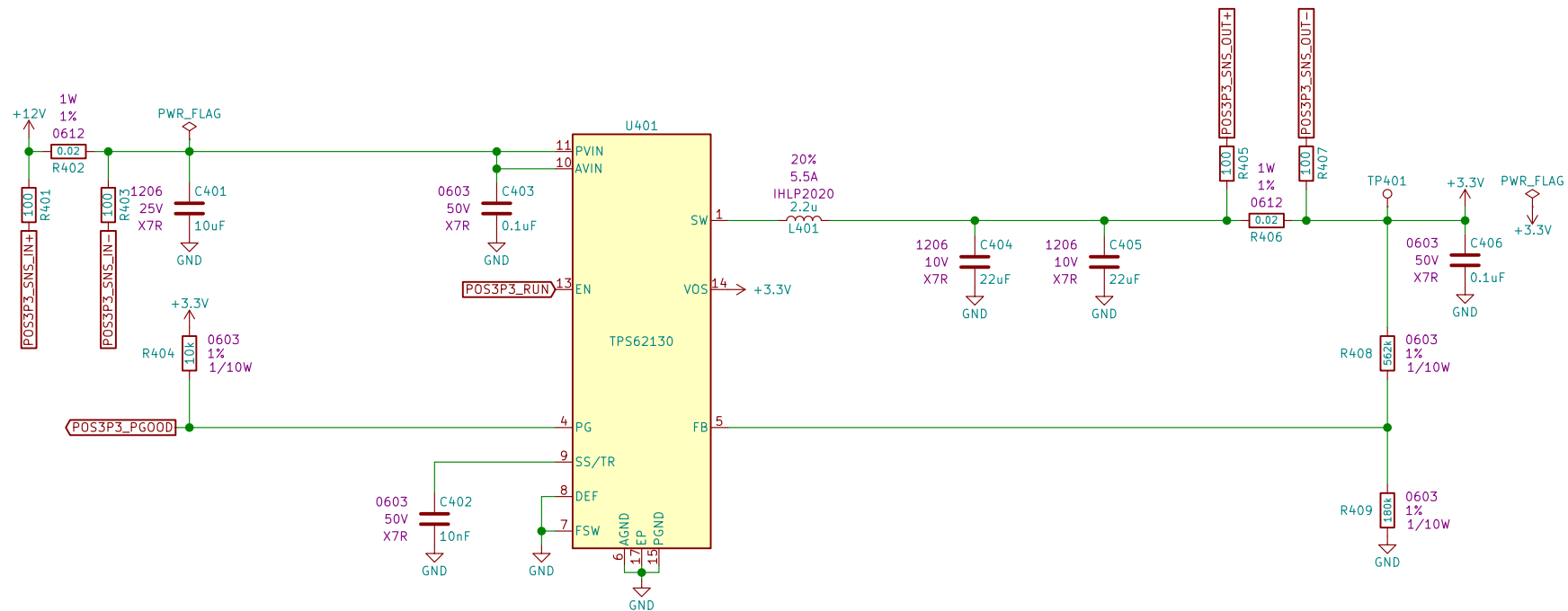


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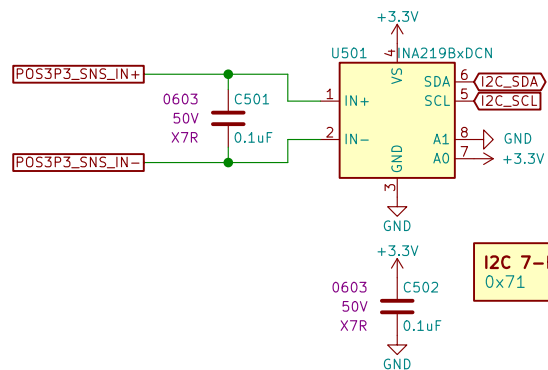


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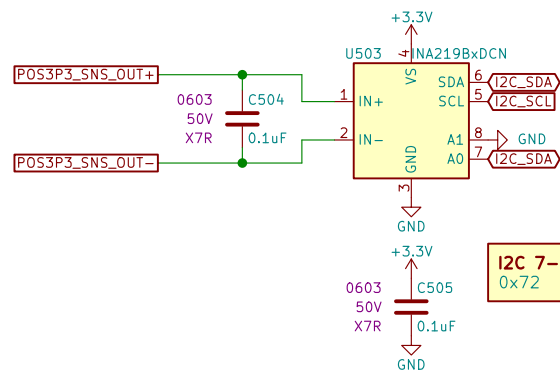
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Size: A Date: 2019-01-03
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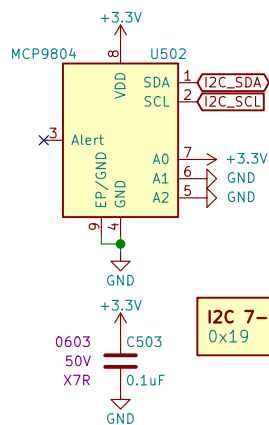
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I2C 7-Bit Address:
0x71



I2C 7-Bit Address:
0x72



I2C 7-Bit Address:
0x19

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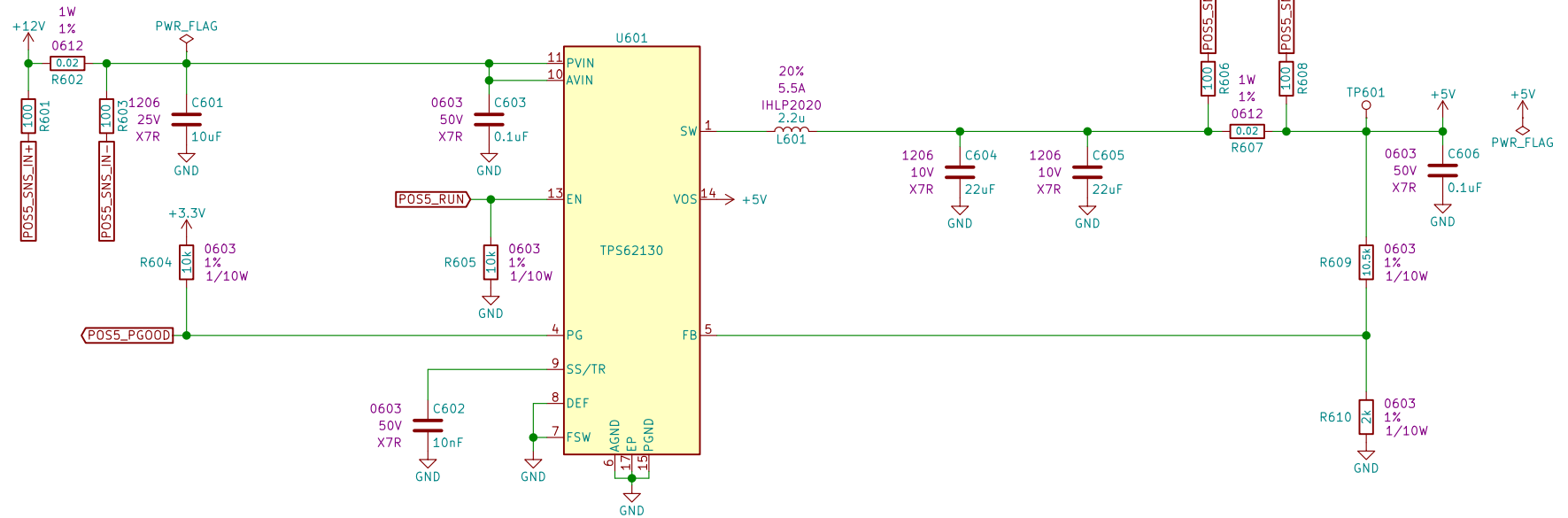
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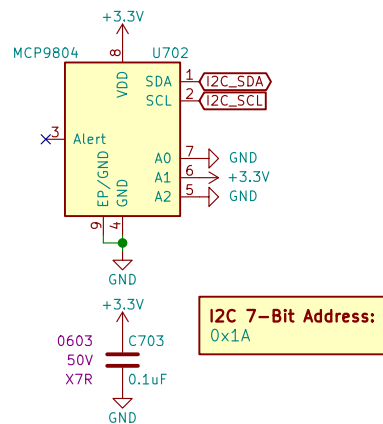
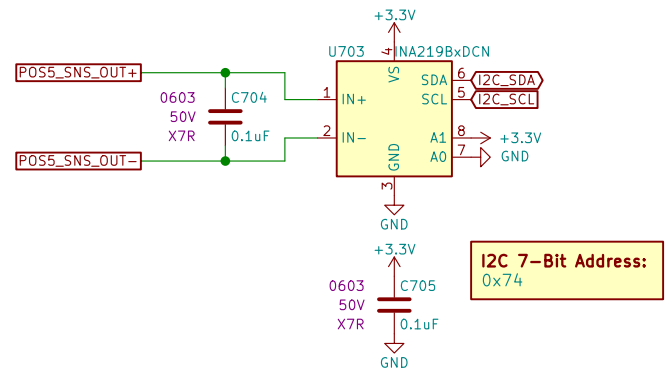
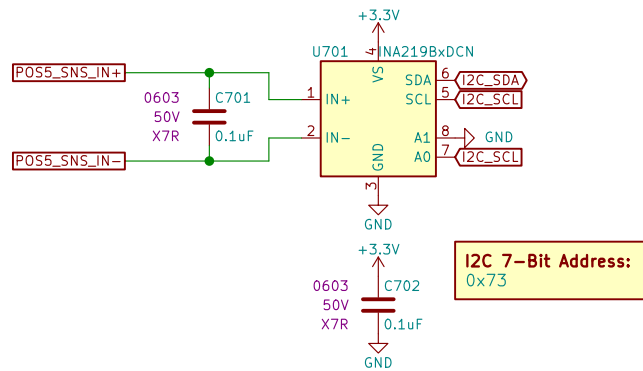


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Sheet: /+5V Telemetry/
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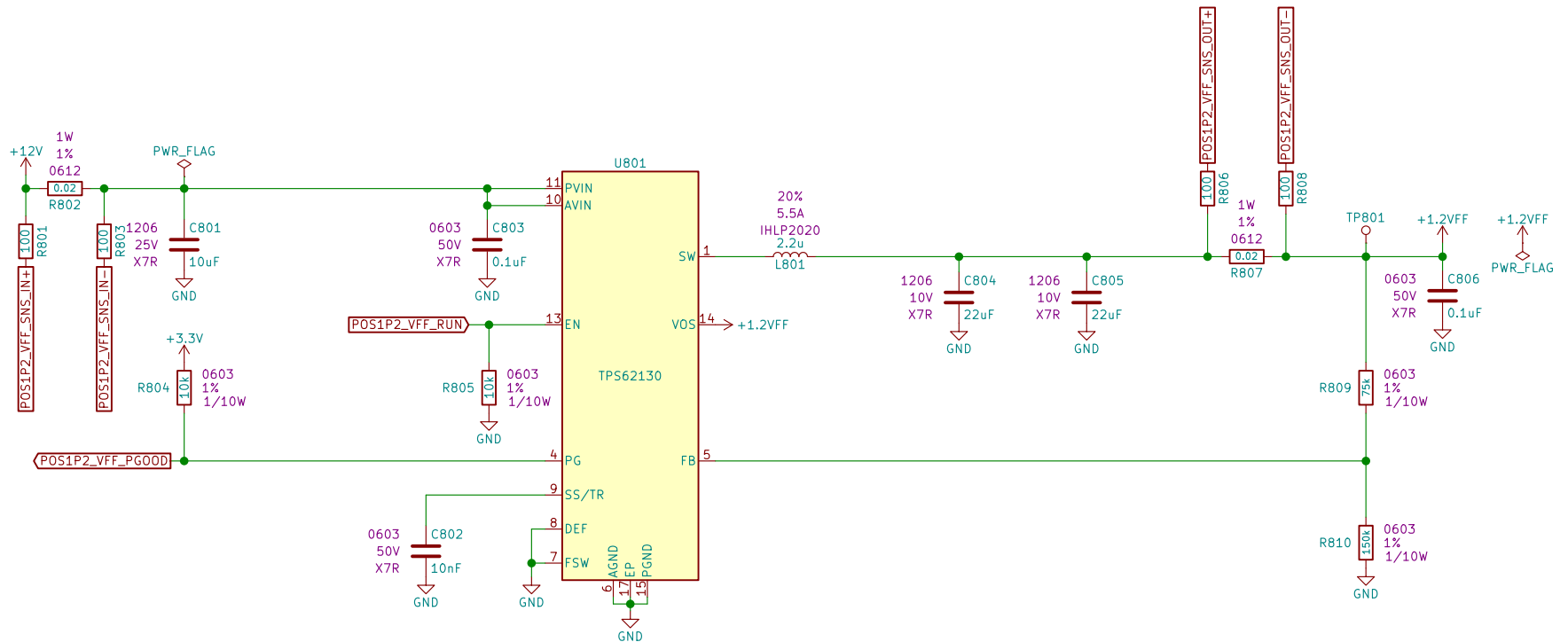
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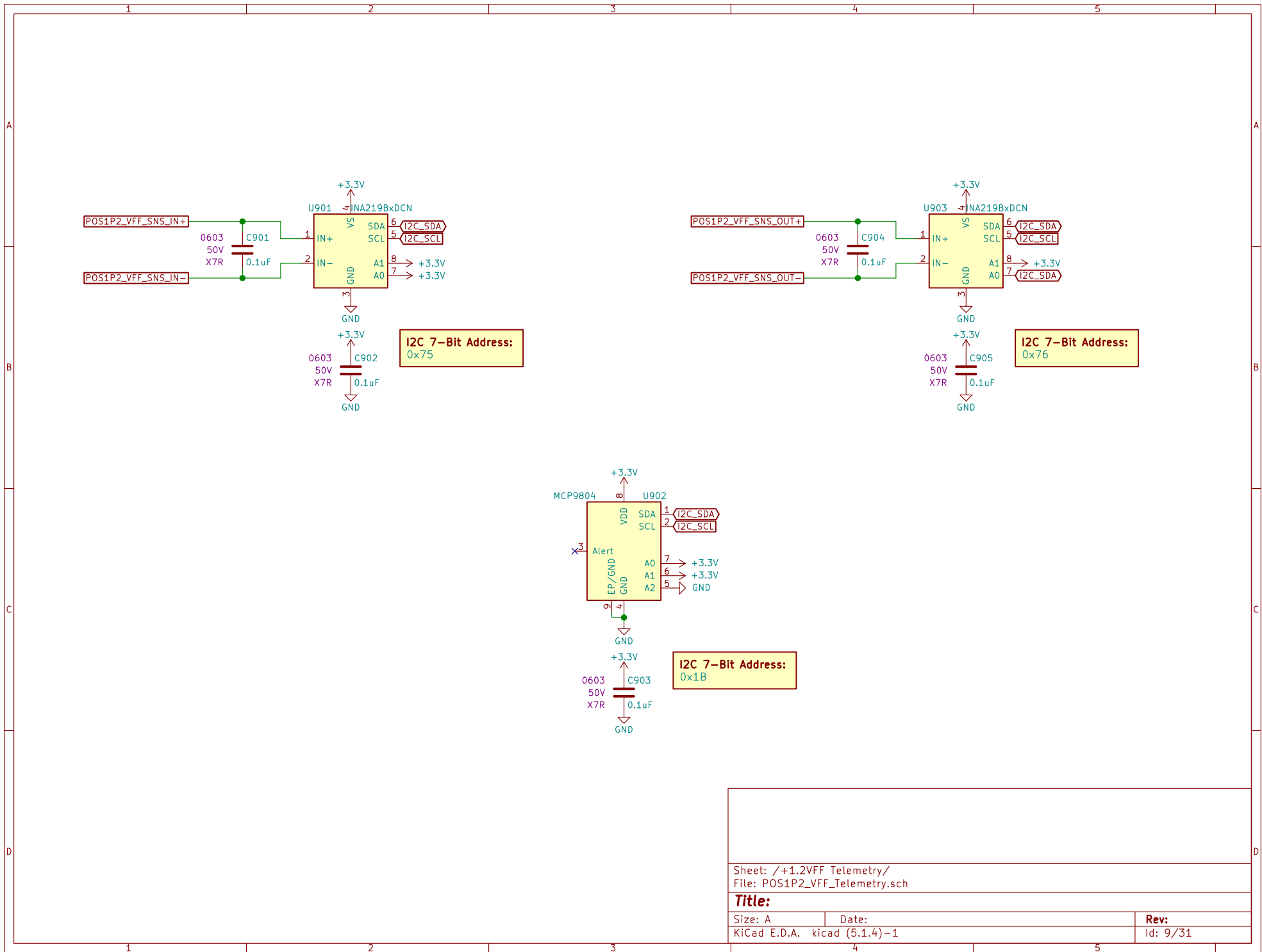


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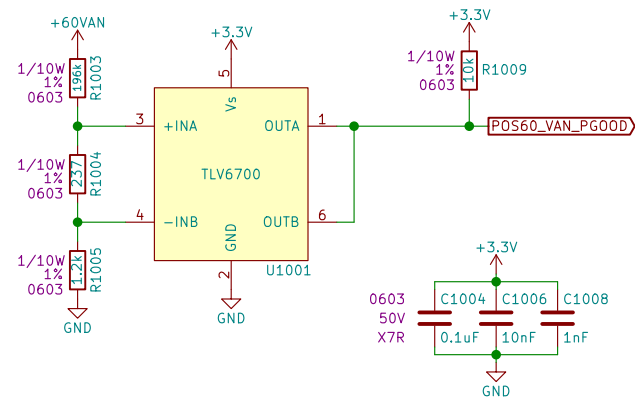
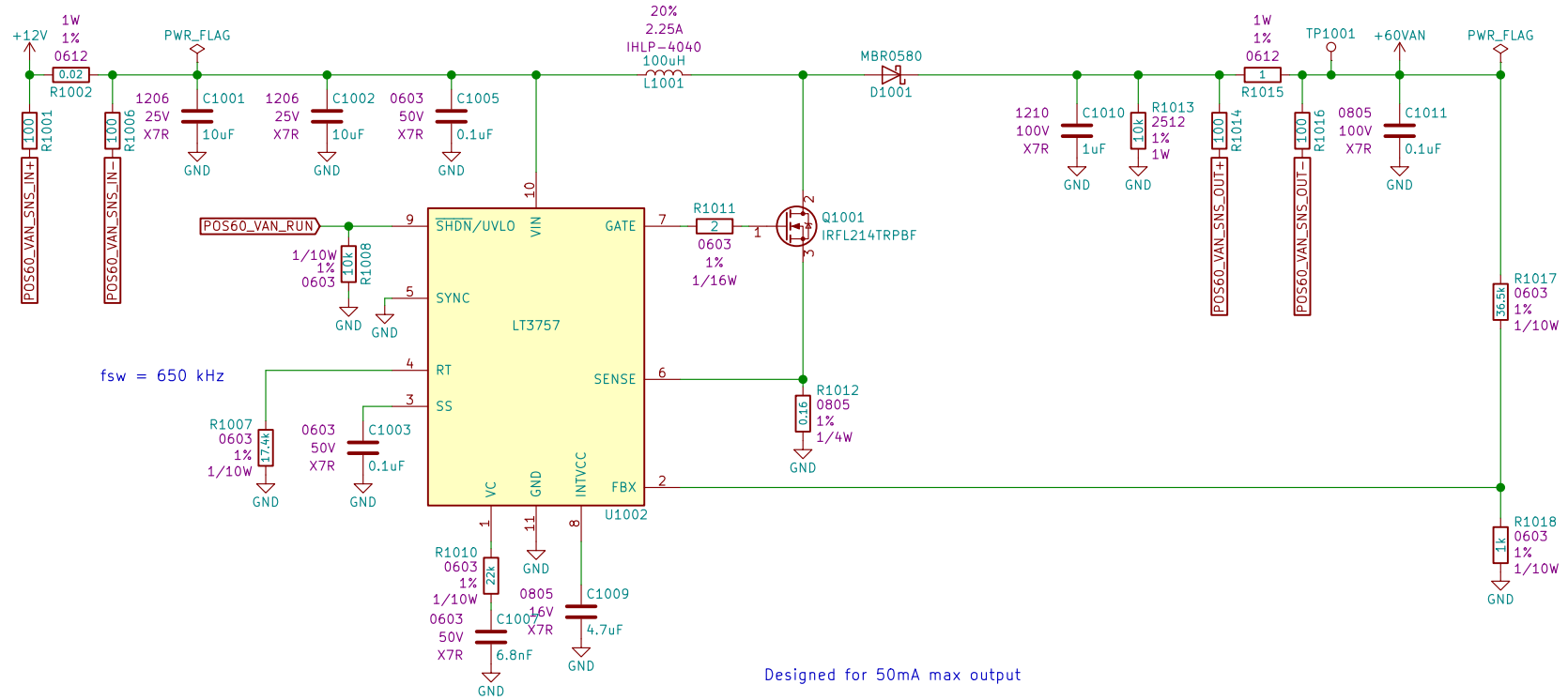
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Anode/Grid +60V, 50mA Power Supply



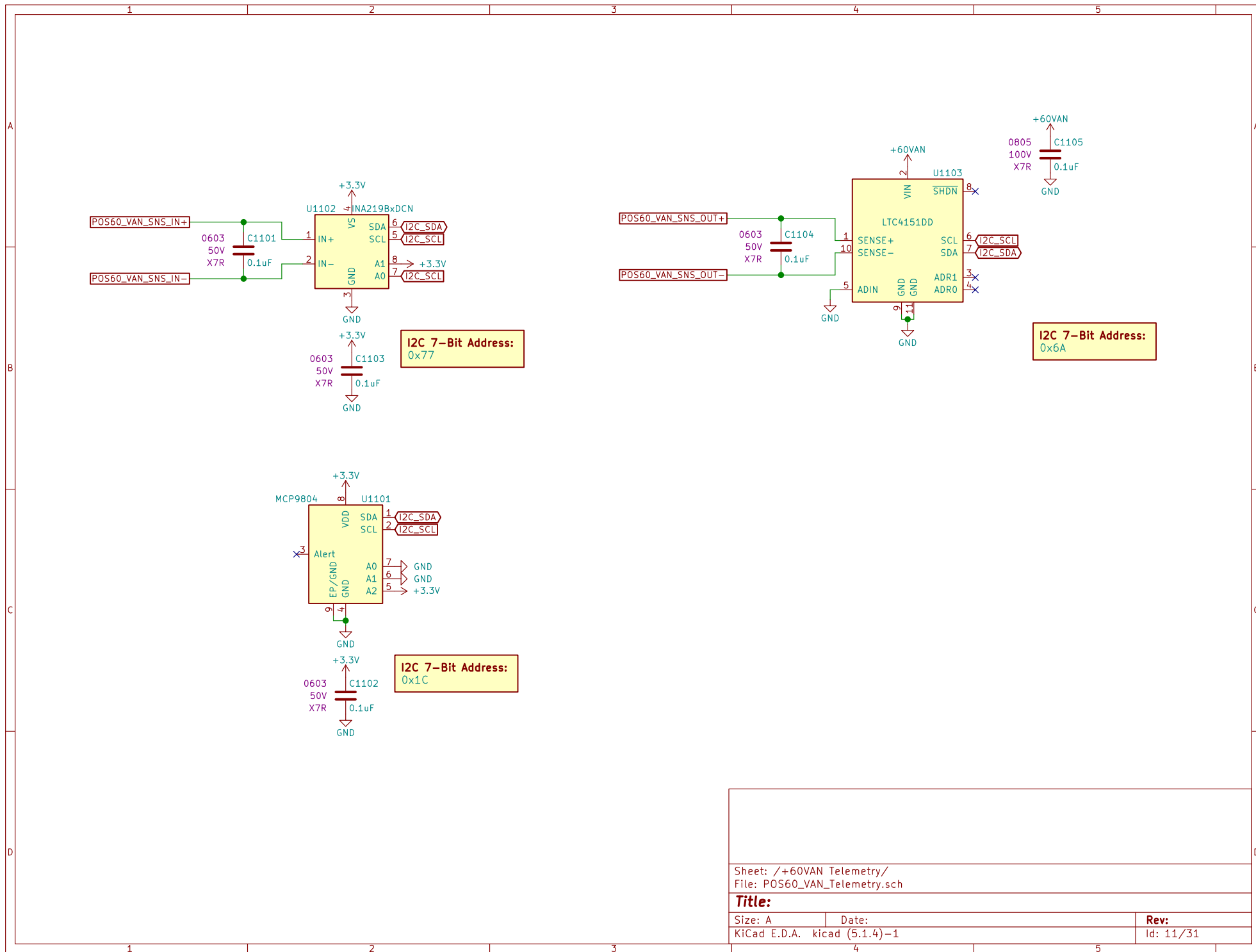
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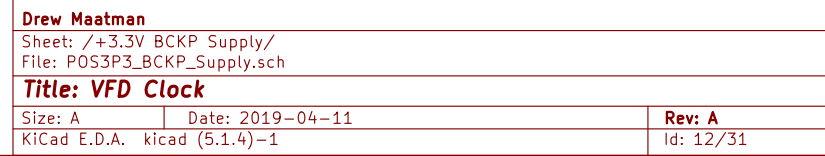
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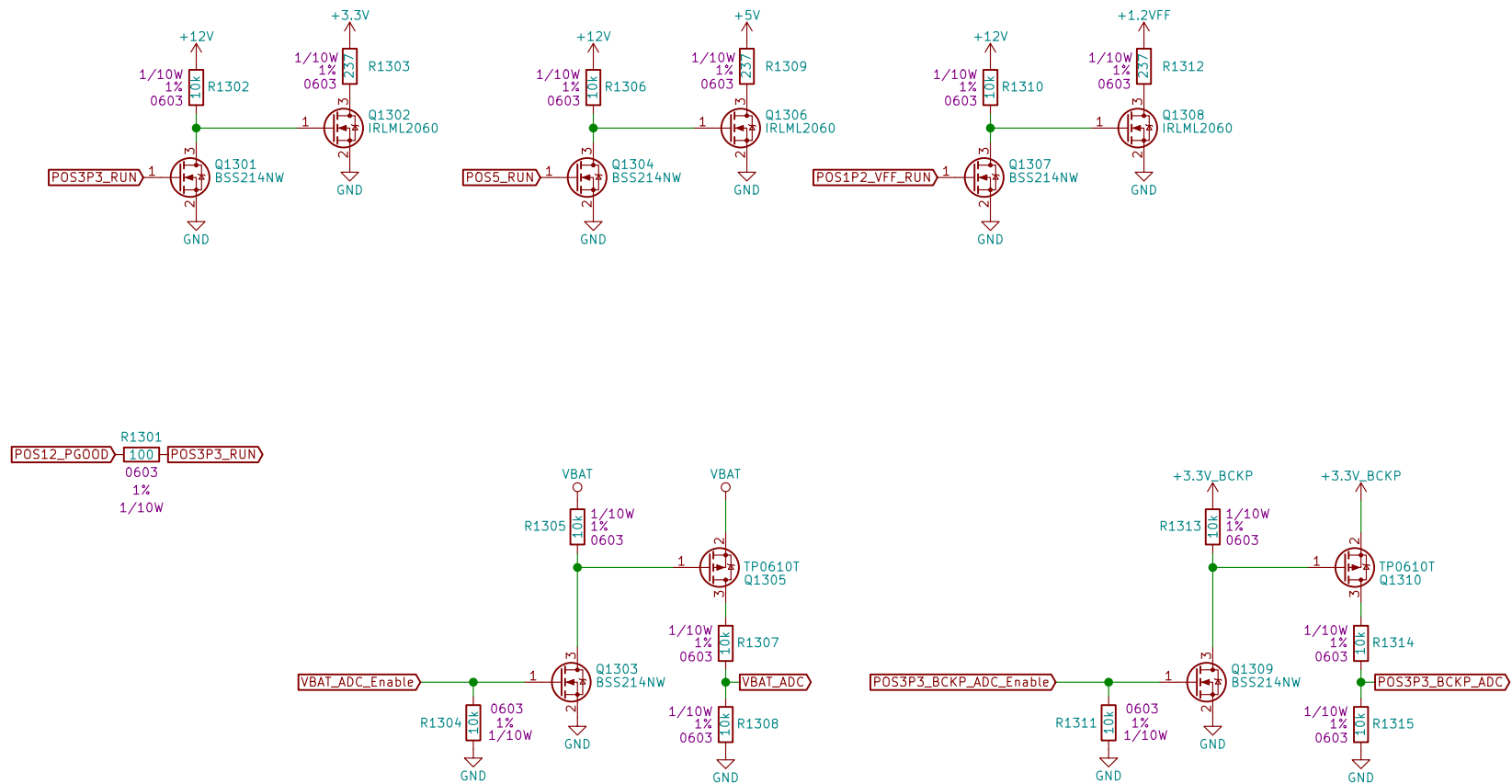
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Sheet: /Misc Power/
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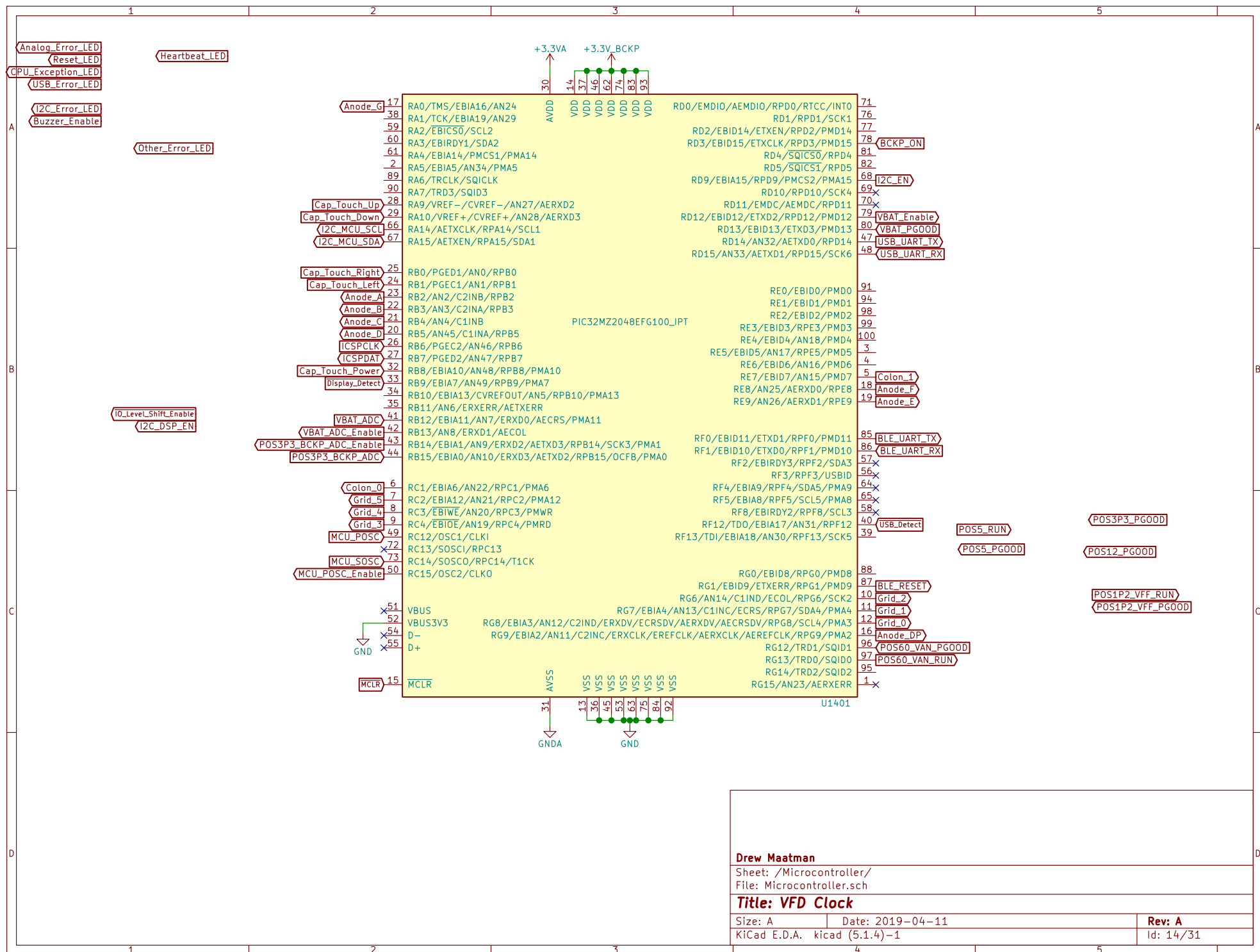
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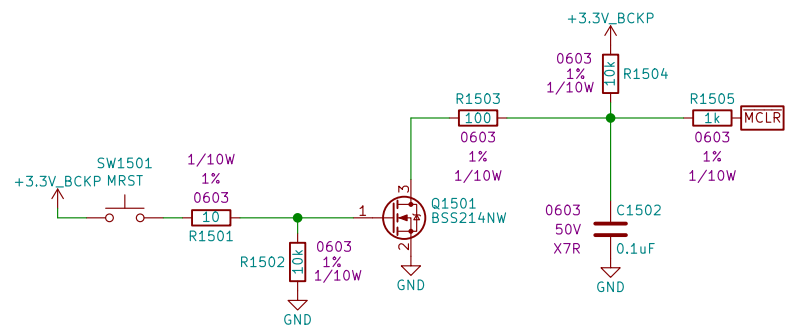
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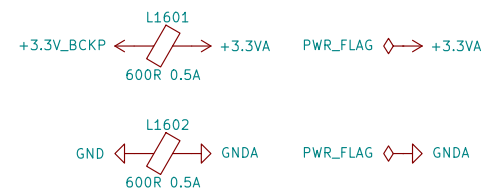
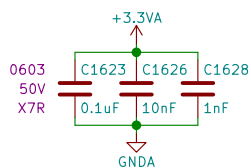
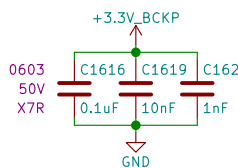
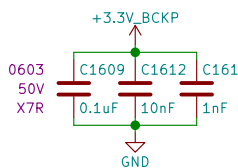
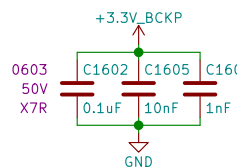
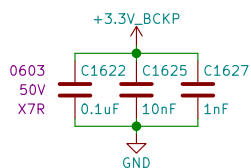
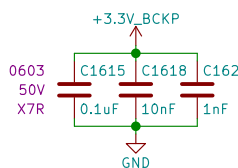
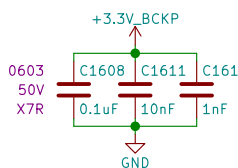
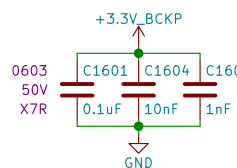
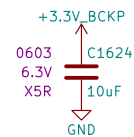
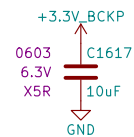
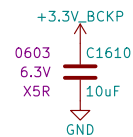
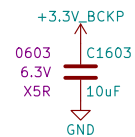
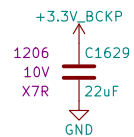
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Sheet: /Microcontroller Bypass/
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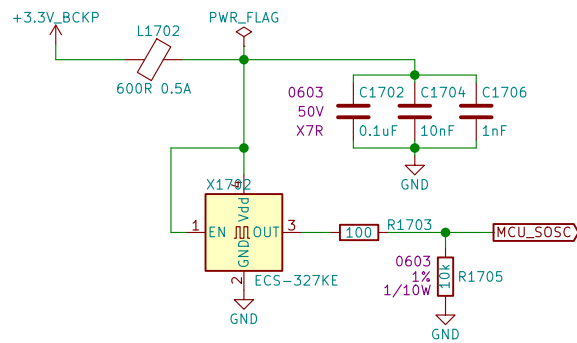
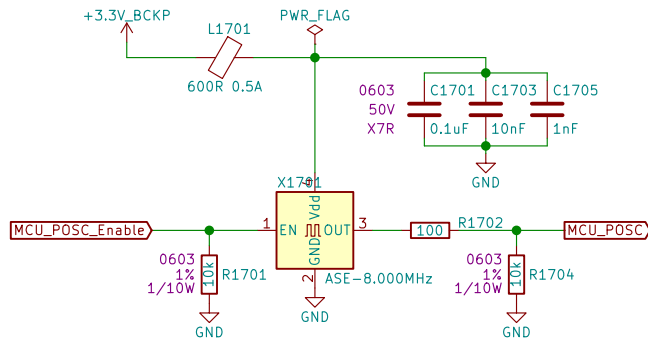
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Sheet: /Microcontroller Clocking/
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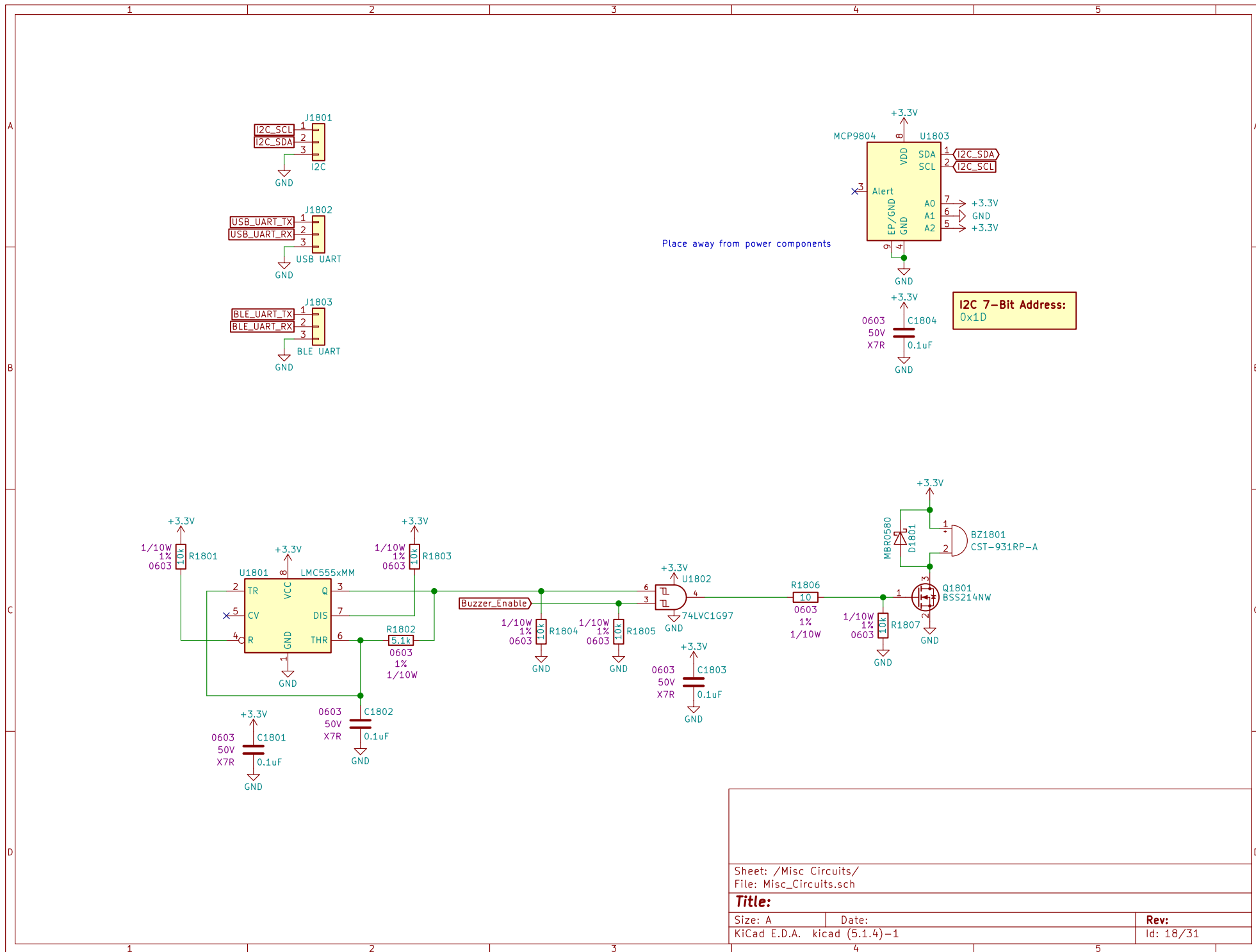
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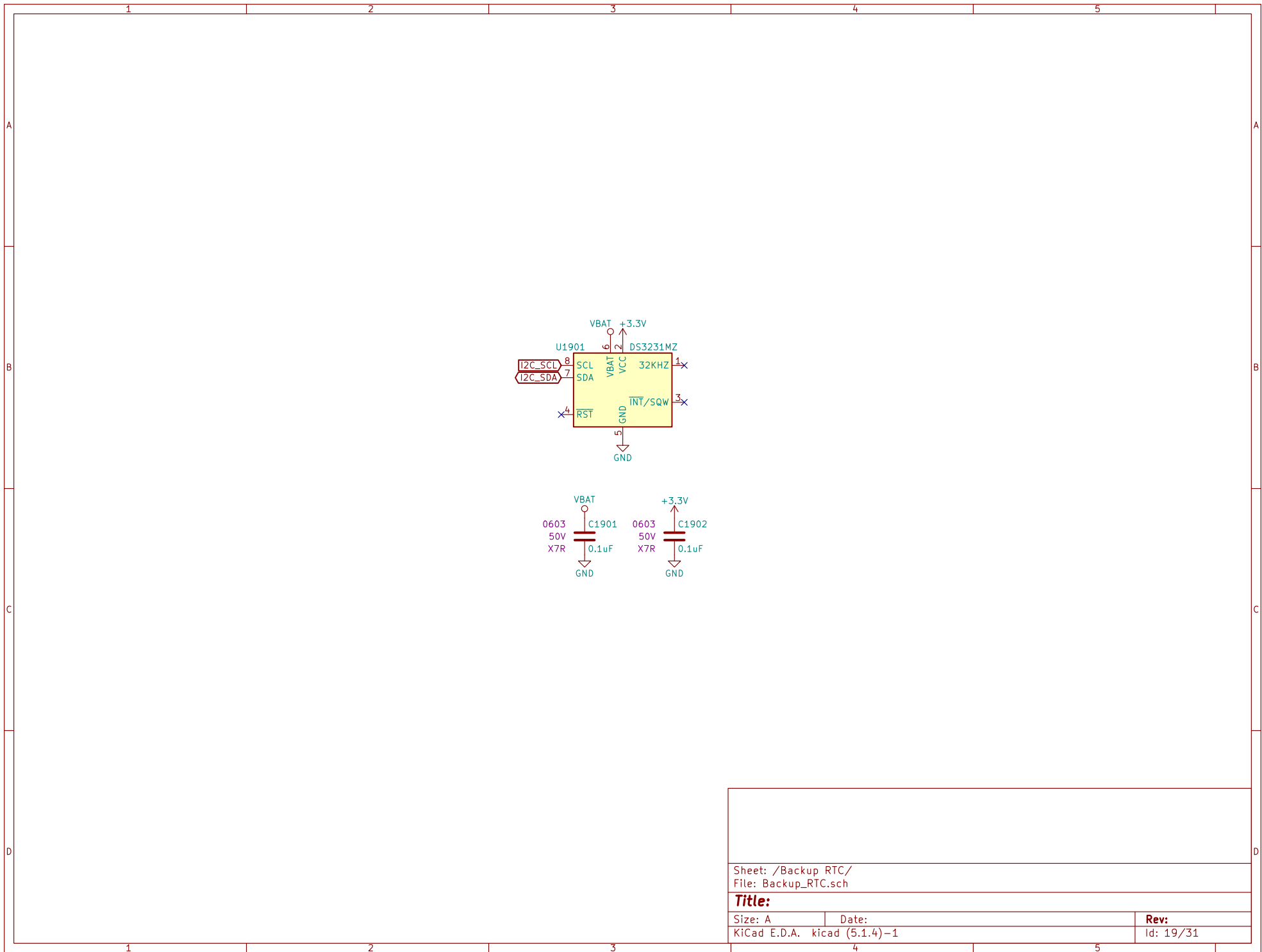
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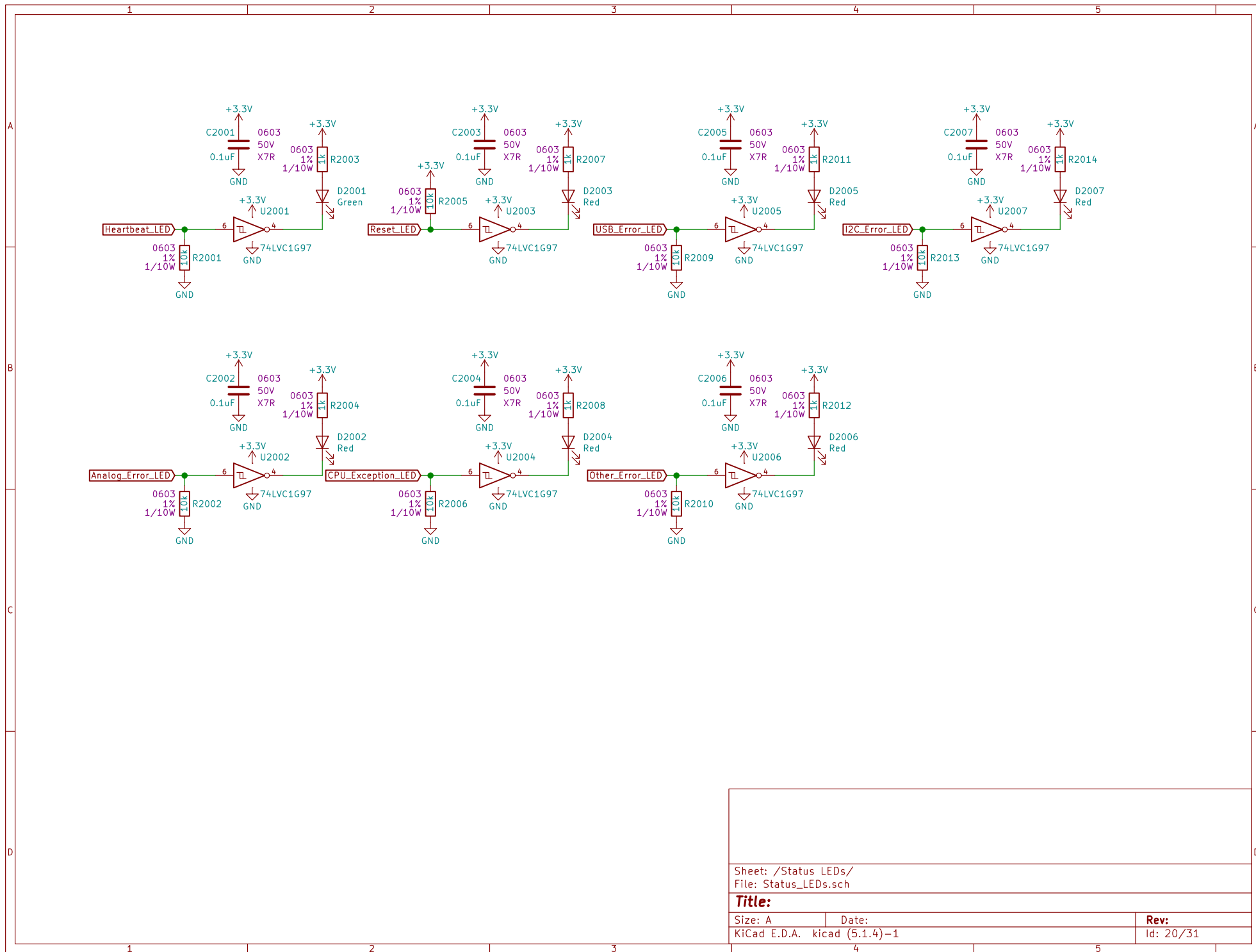
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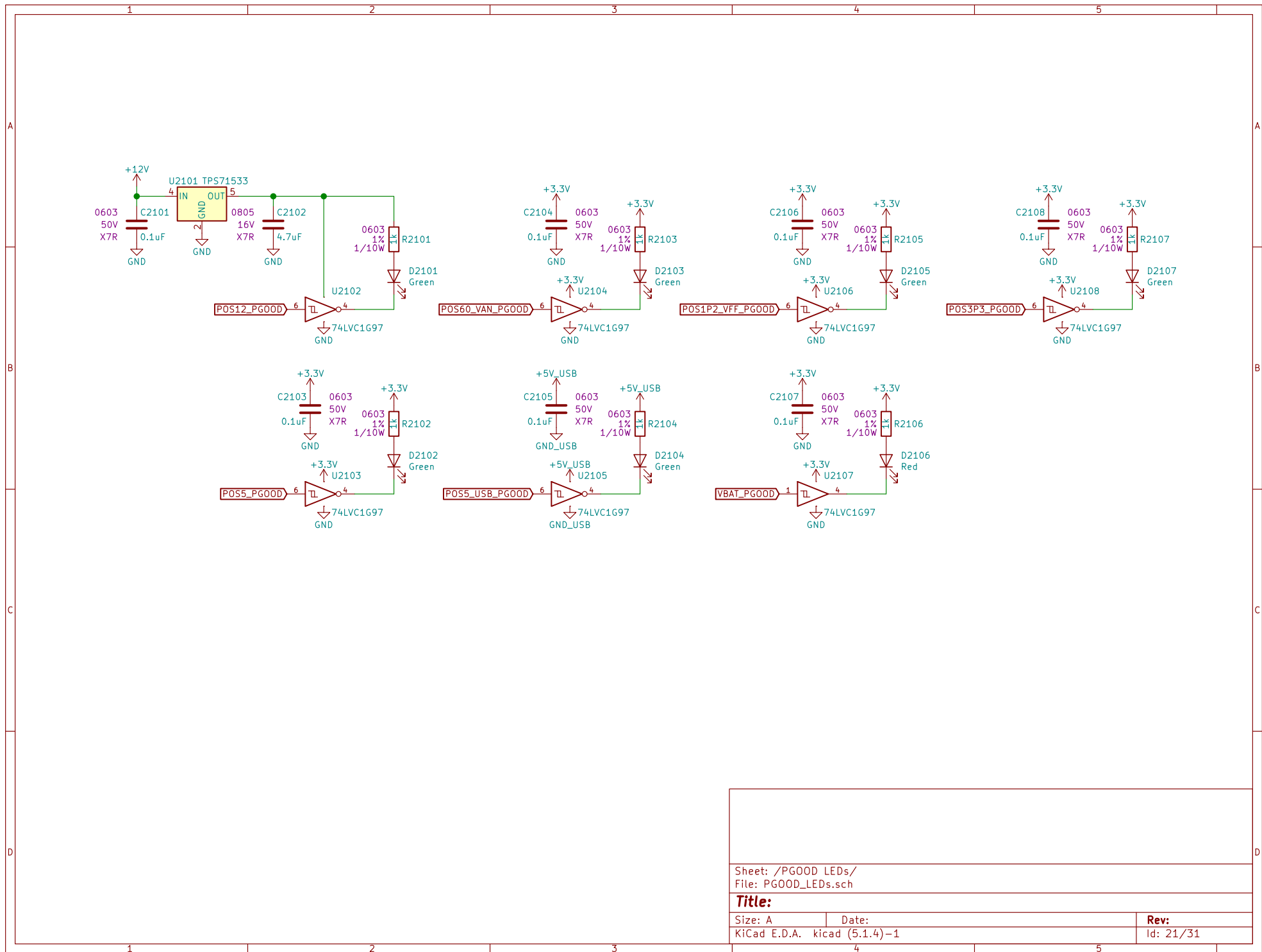
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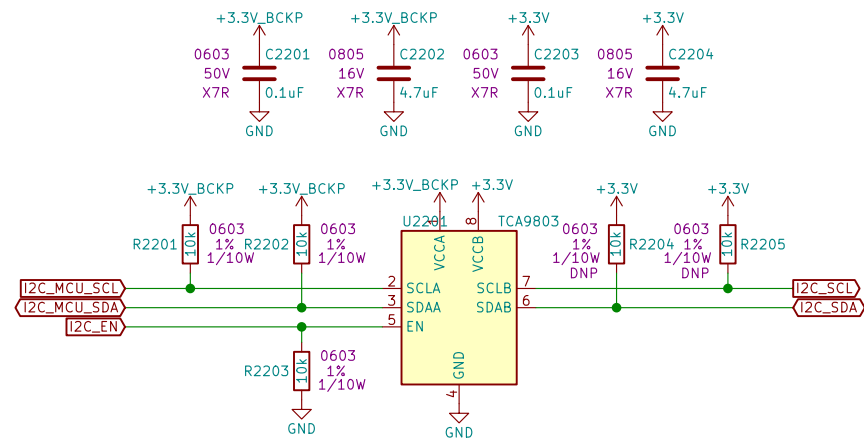
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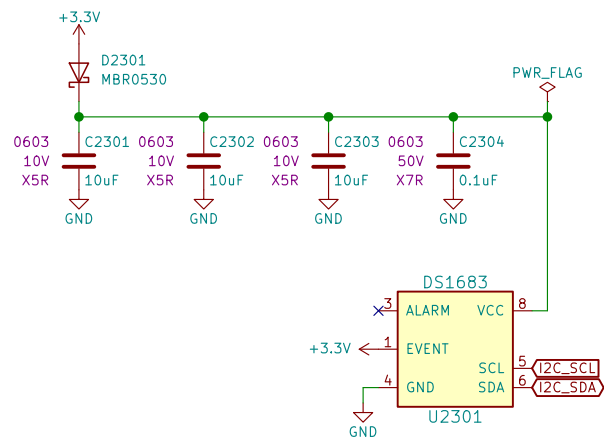
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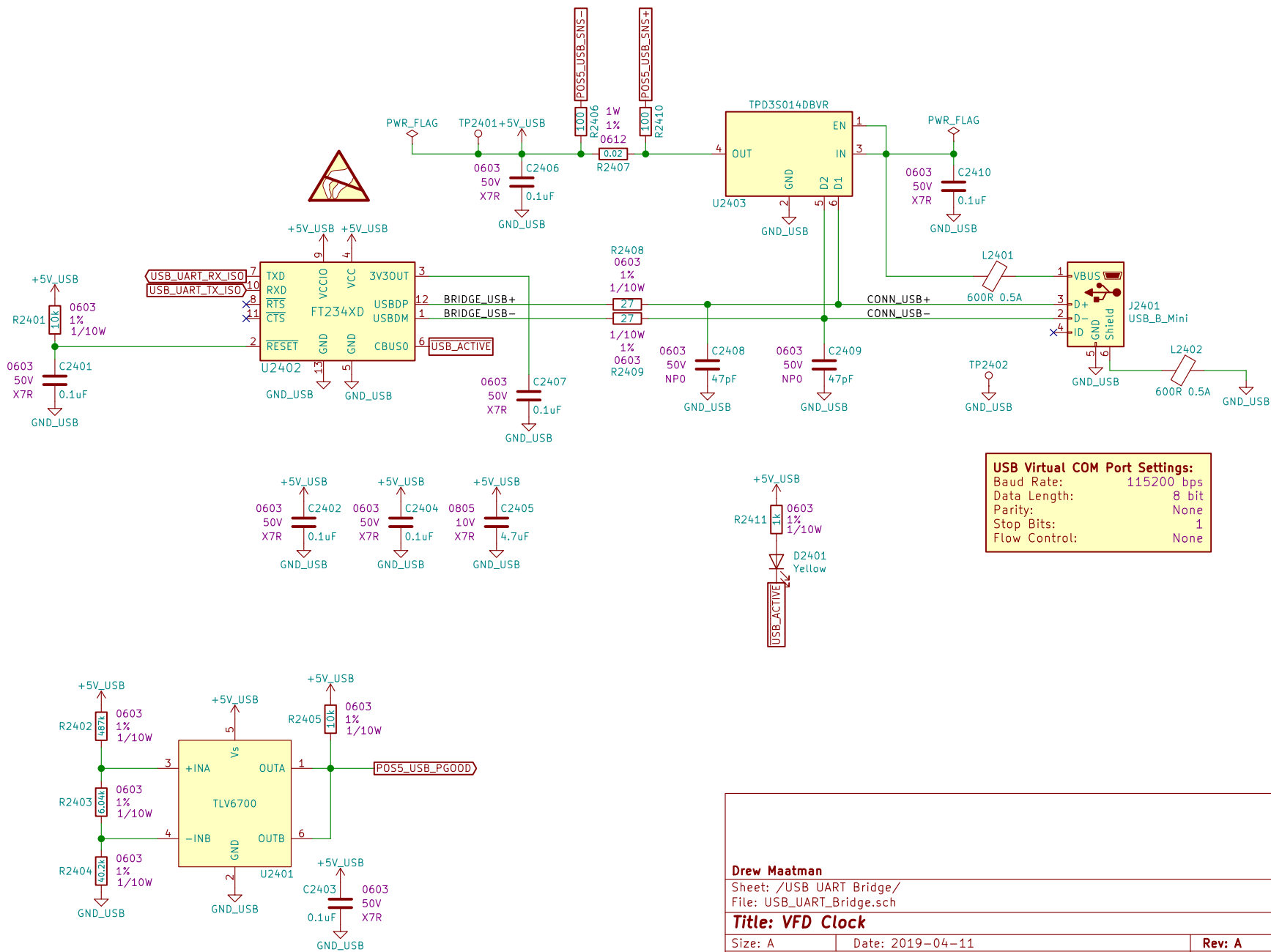
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I2C 7-Bit Address:
0x3B

Sheet: /Time of Flight/ File: Time_of_Flight.sch		
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Size: A	Date:	Rev:
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11. USB UART Bridge



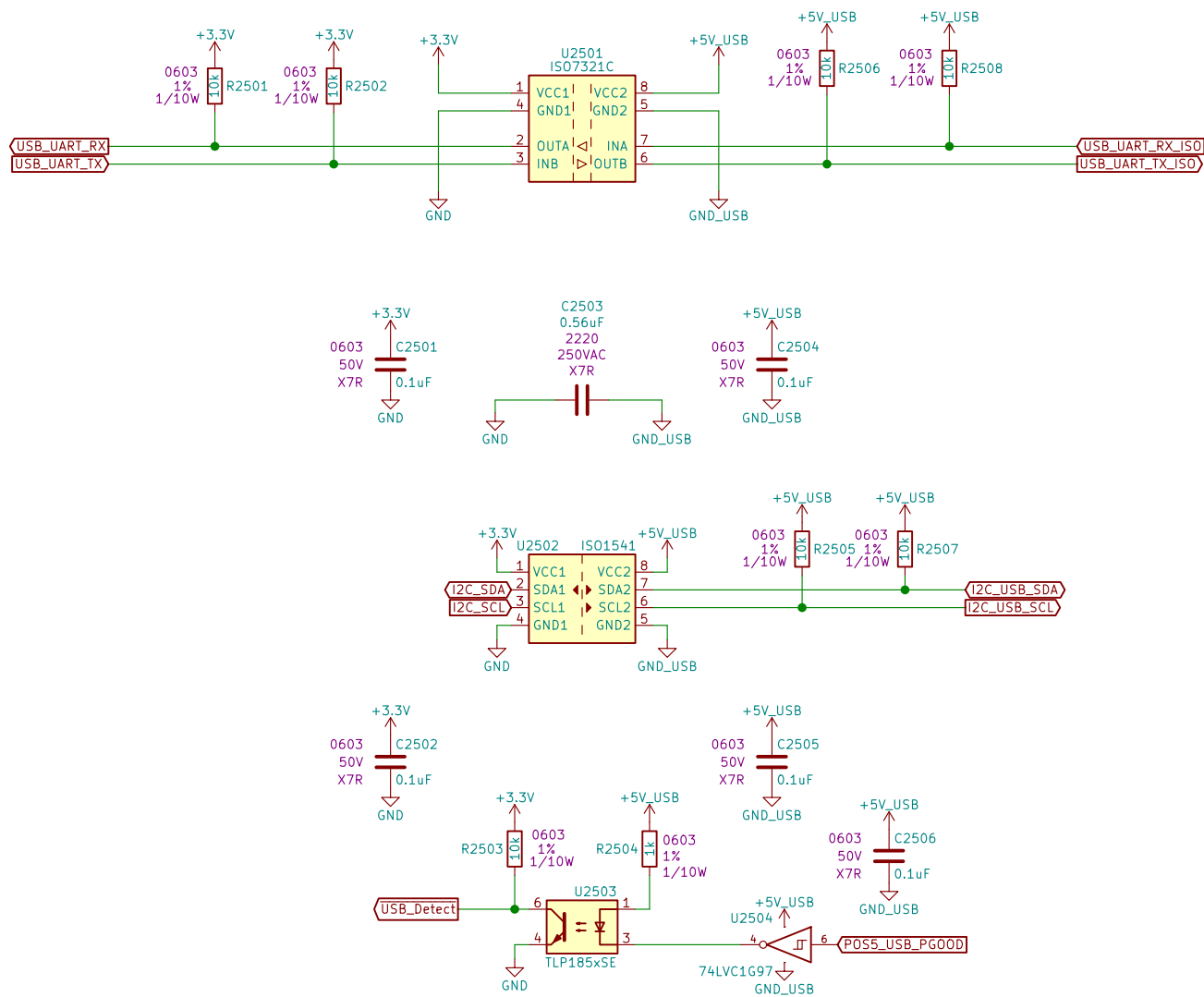
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Sheet: /USB UART Bridge/
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Title: VFD Clock

Size: A Date: 2019-04-11
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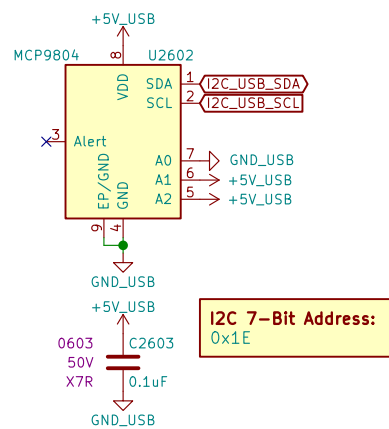
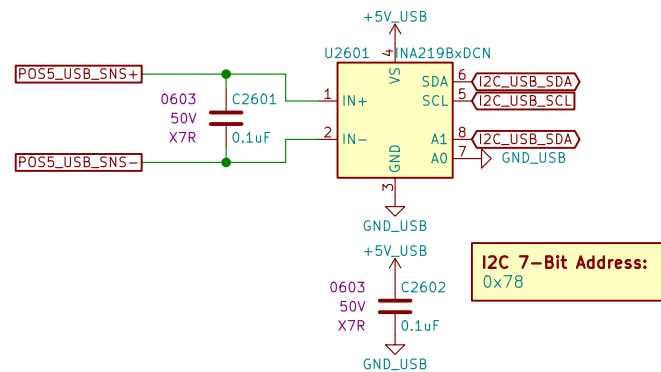
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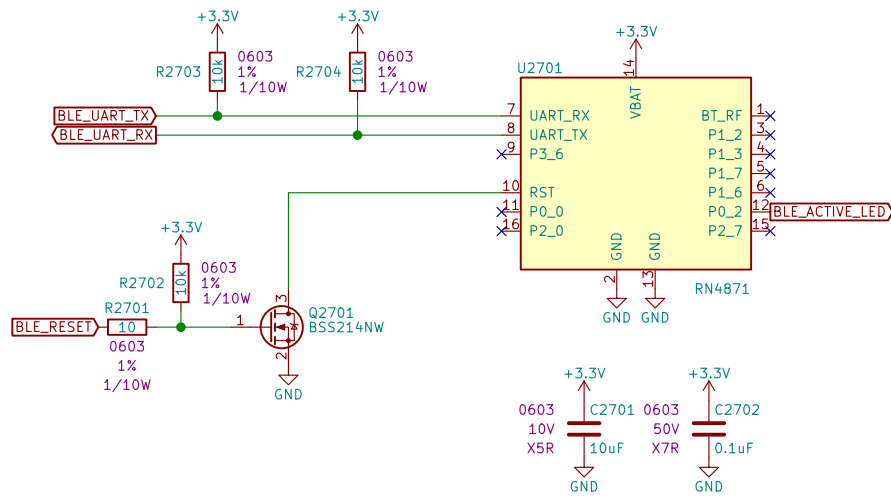


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Sheet: /BLE/
File: BLE.sch

Title:

Size: A

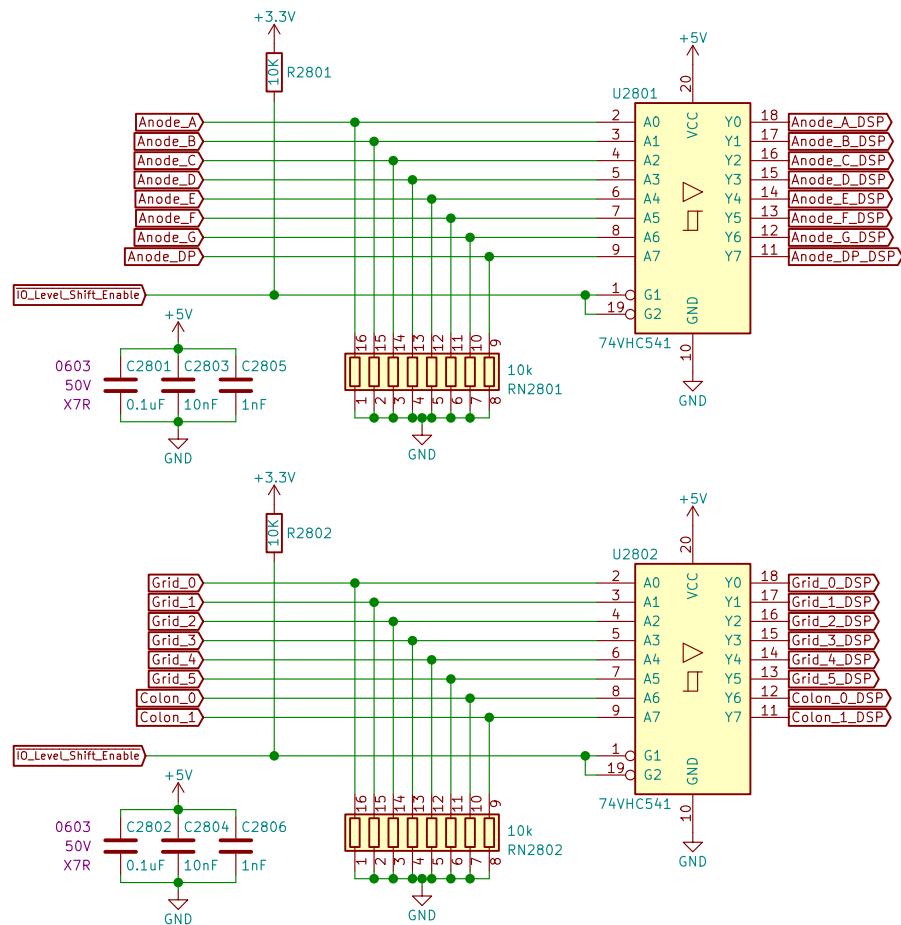
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I/O Buffers



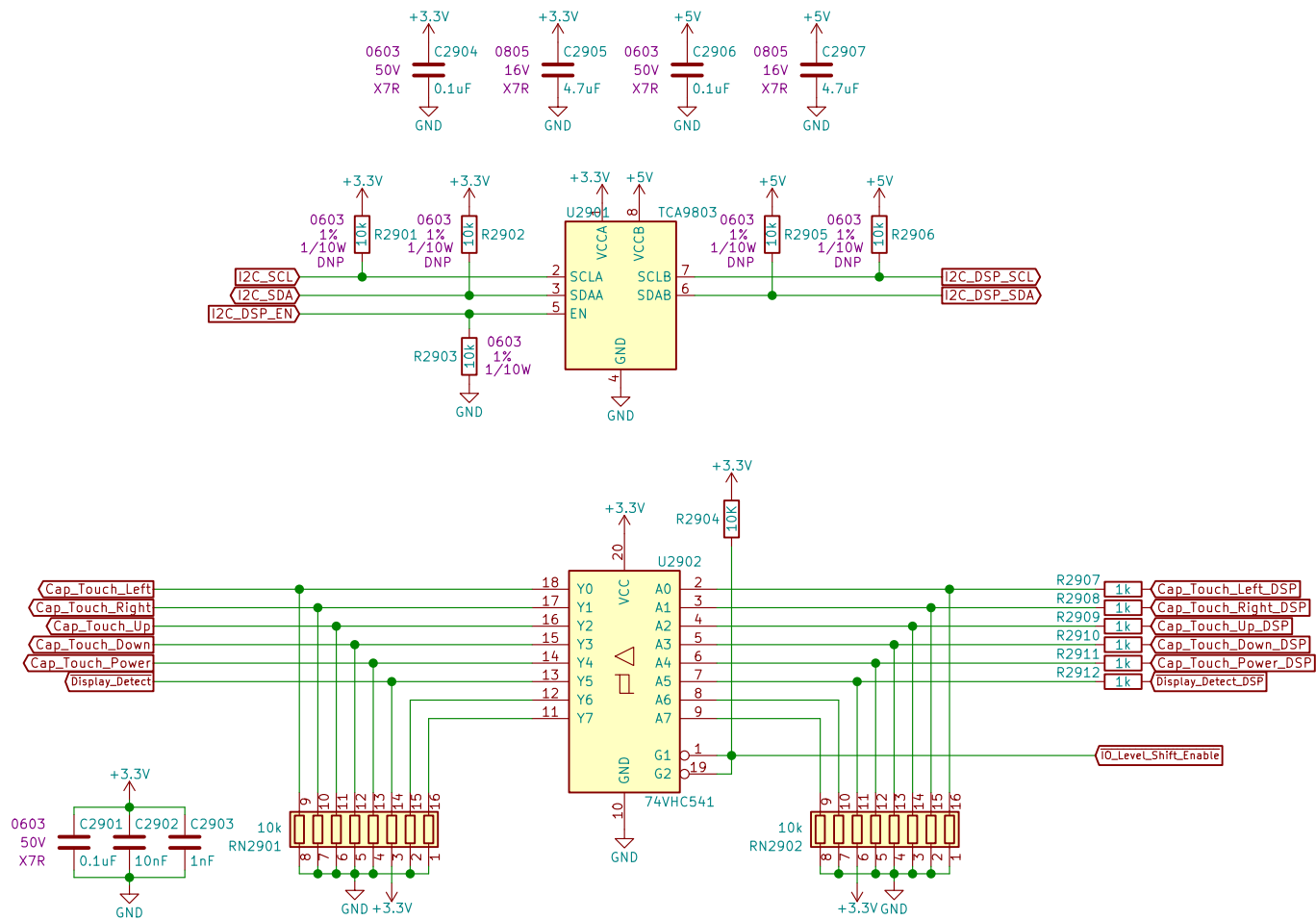
Marquette University Senior Design 2018/2019 Group E44

Sheet: /IO Buffers 1/
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Title: Electronic Display Logic Board

Size: A Date: 2018-12-15
KiCad E.D.A. kicad (5.1.4)-1

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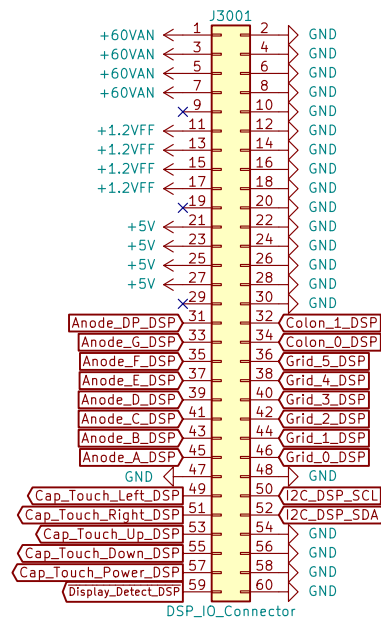


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Id: 29/31

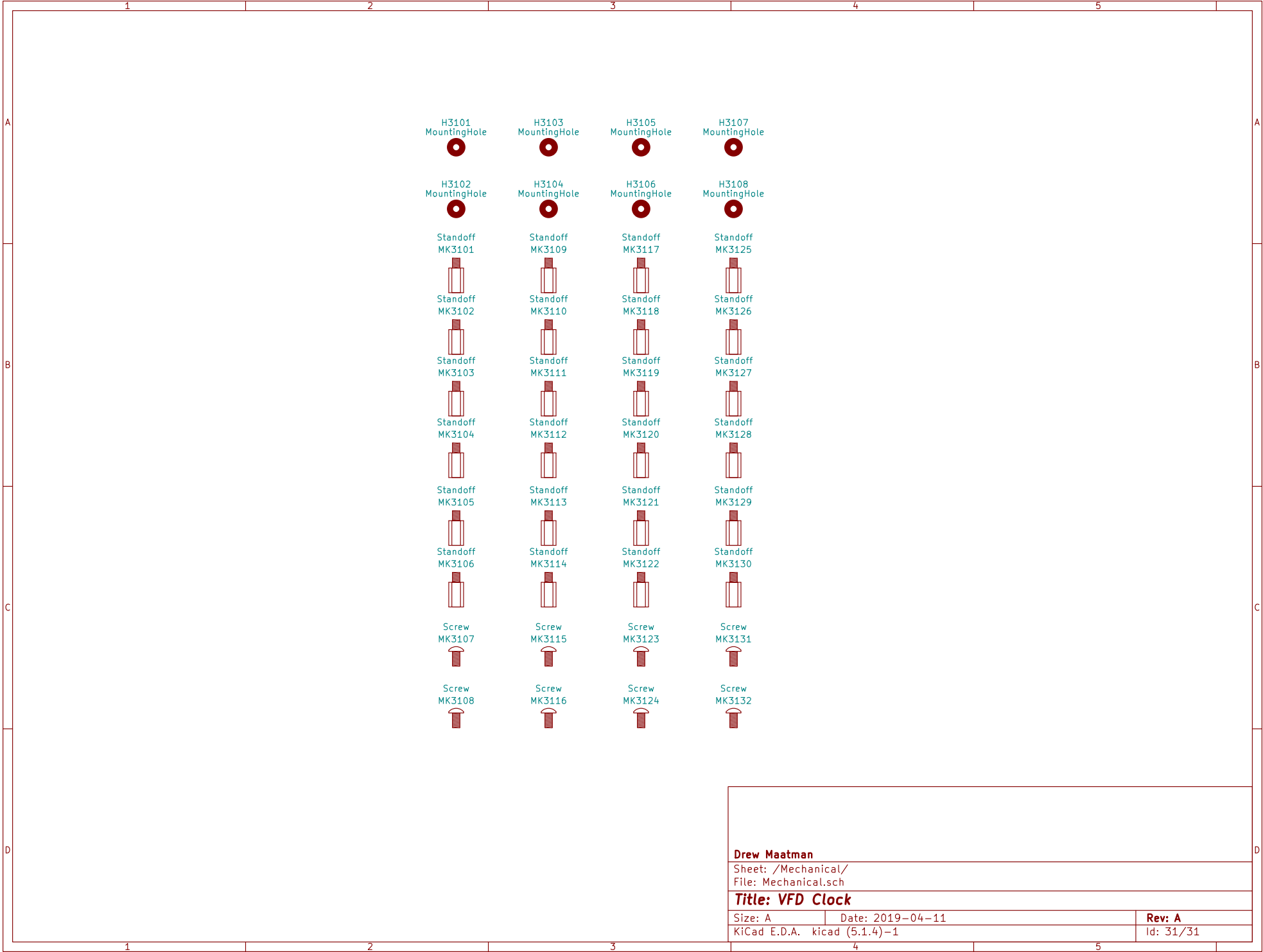


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Size: A Date:
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Sheet: /Mechanical/
File: Mechanical.sch

Title: VFD Clock

Size: A Date: 2019-04-11
KiCad E.D.A. kicad (5.1.4)-1

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