

VFD Clock Logic Board

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Consider adding net ties on all current sense shunt resistors for net isolation

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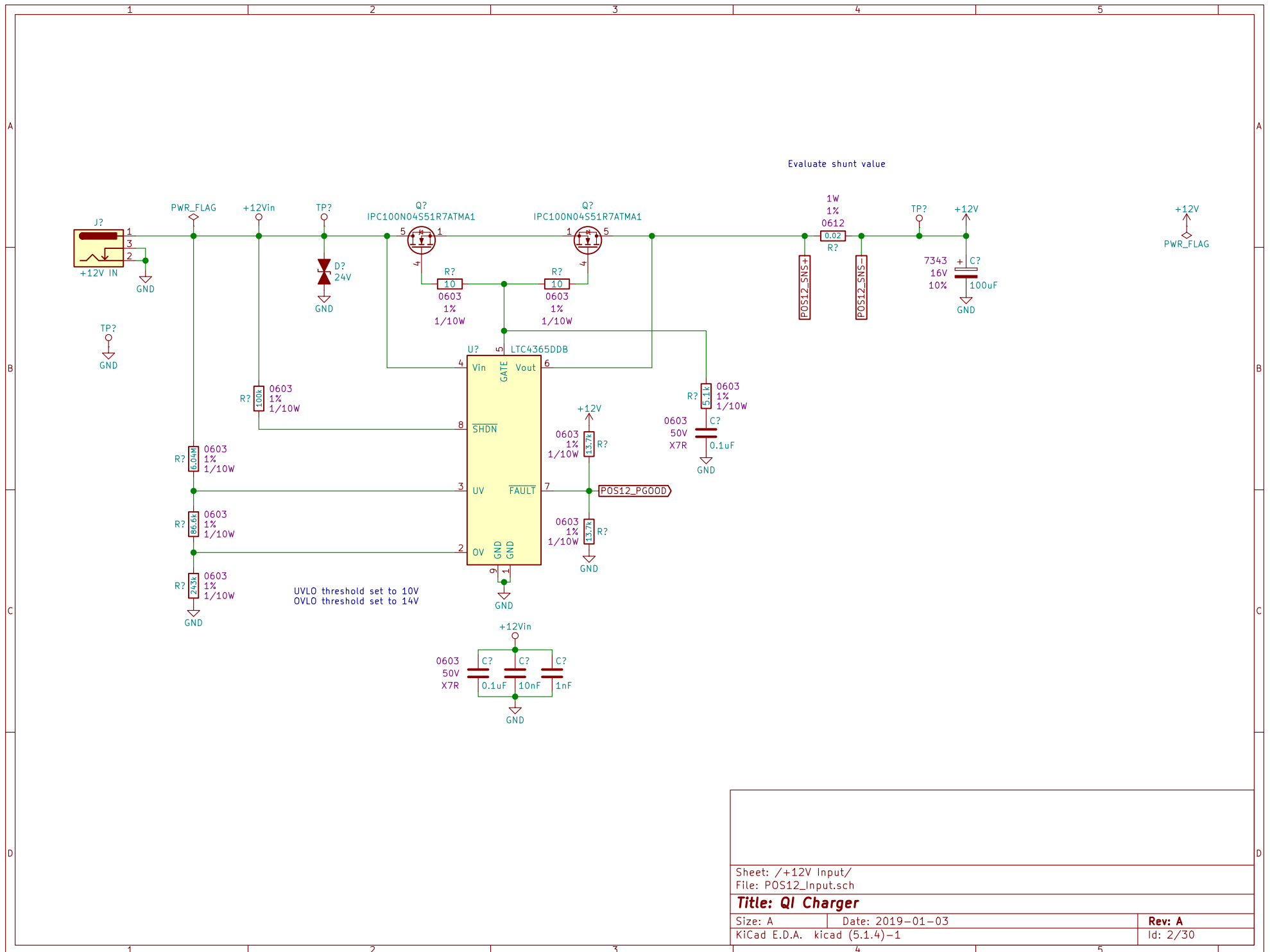
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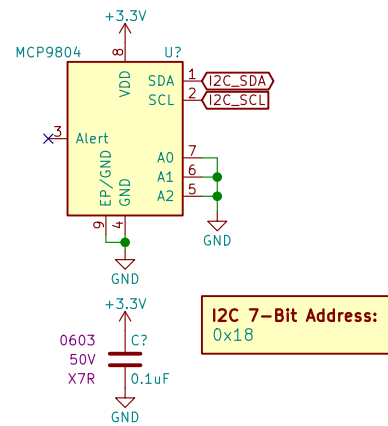
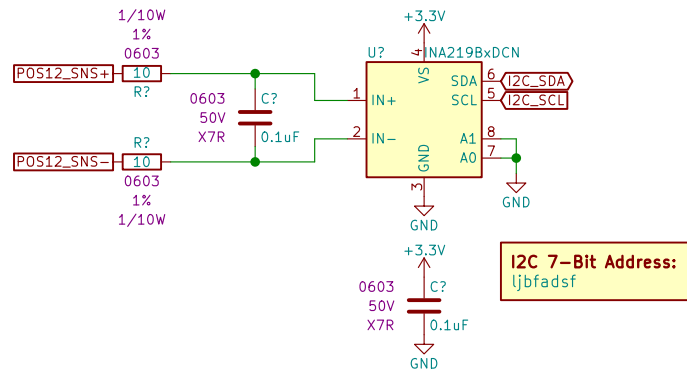
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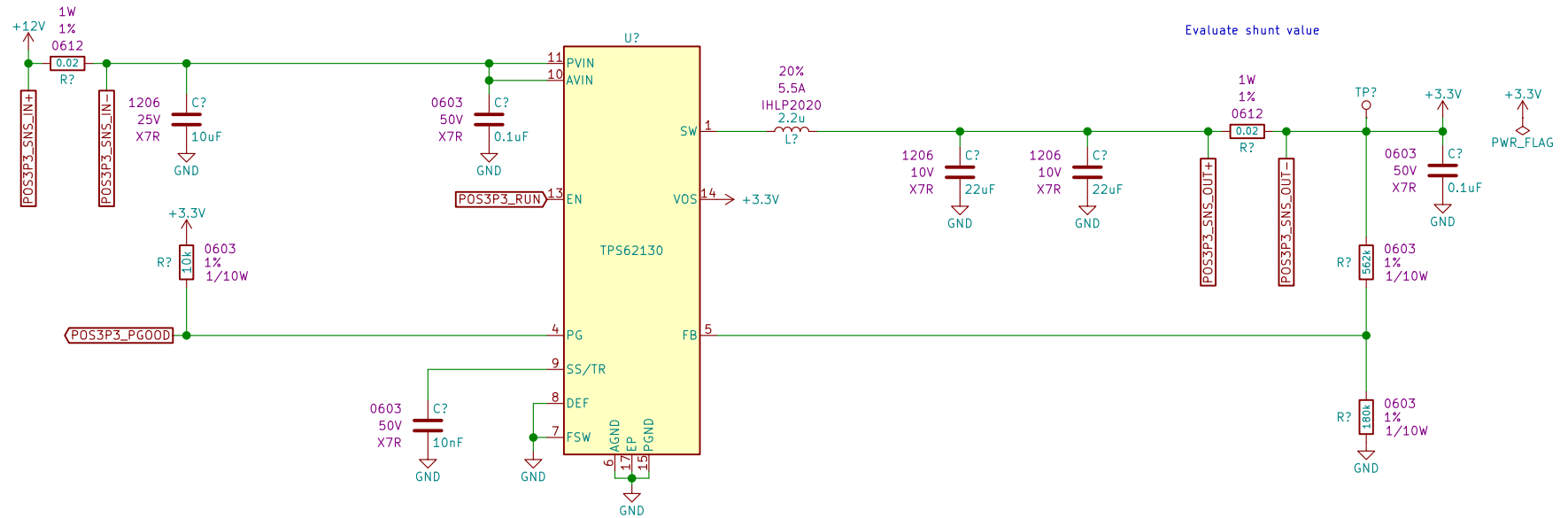


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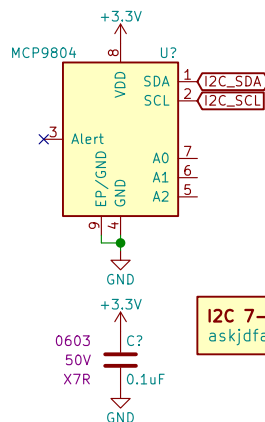
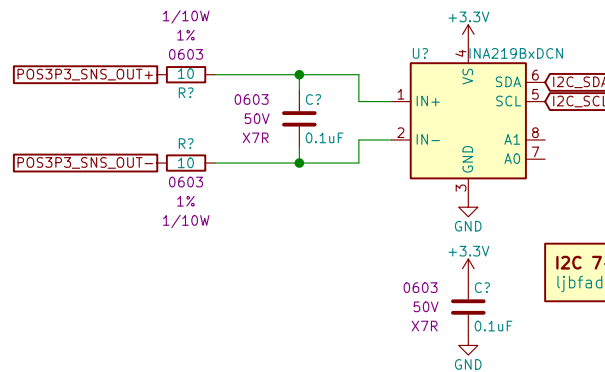
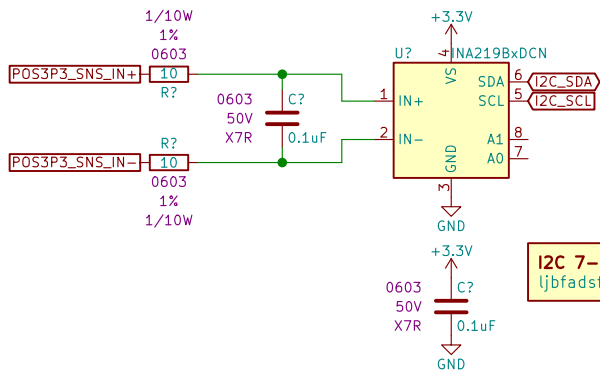


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Size: A Date: 2019-01-03
KiCad E.D.A. kicad (5.1.4)-1

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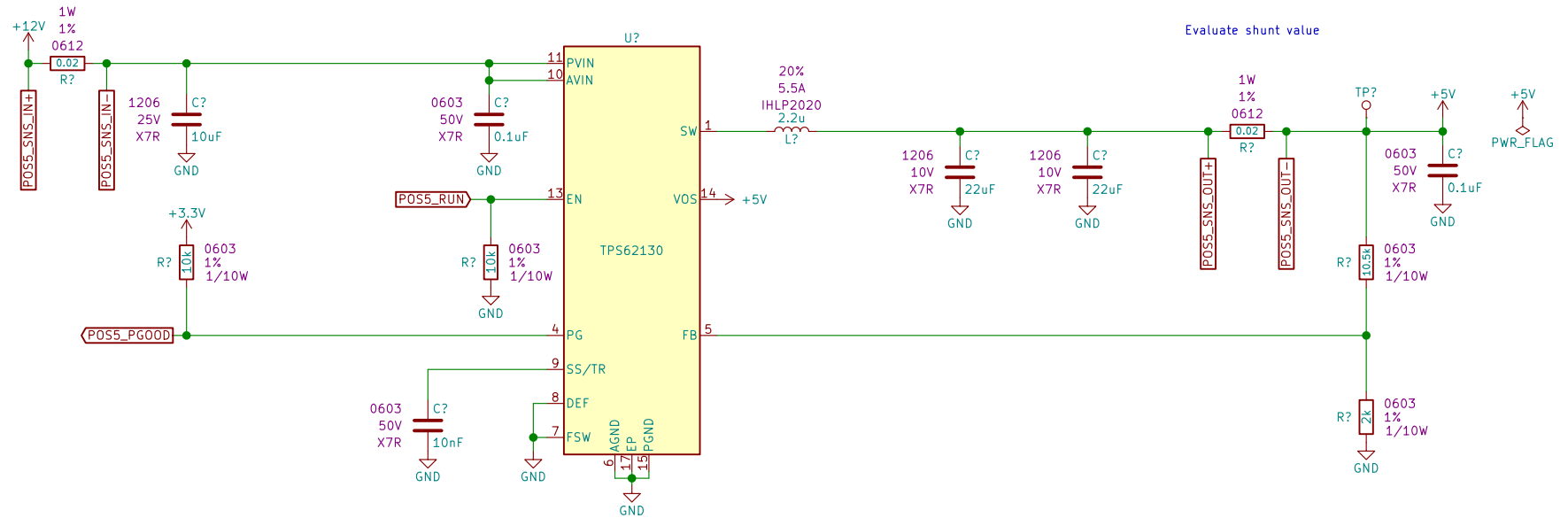
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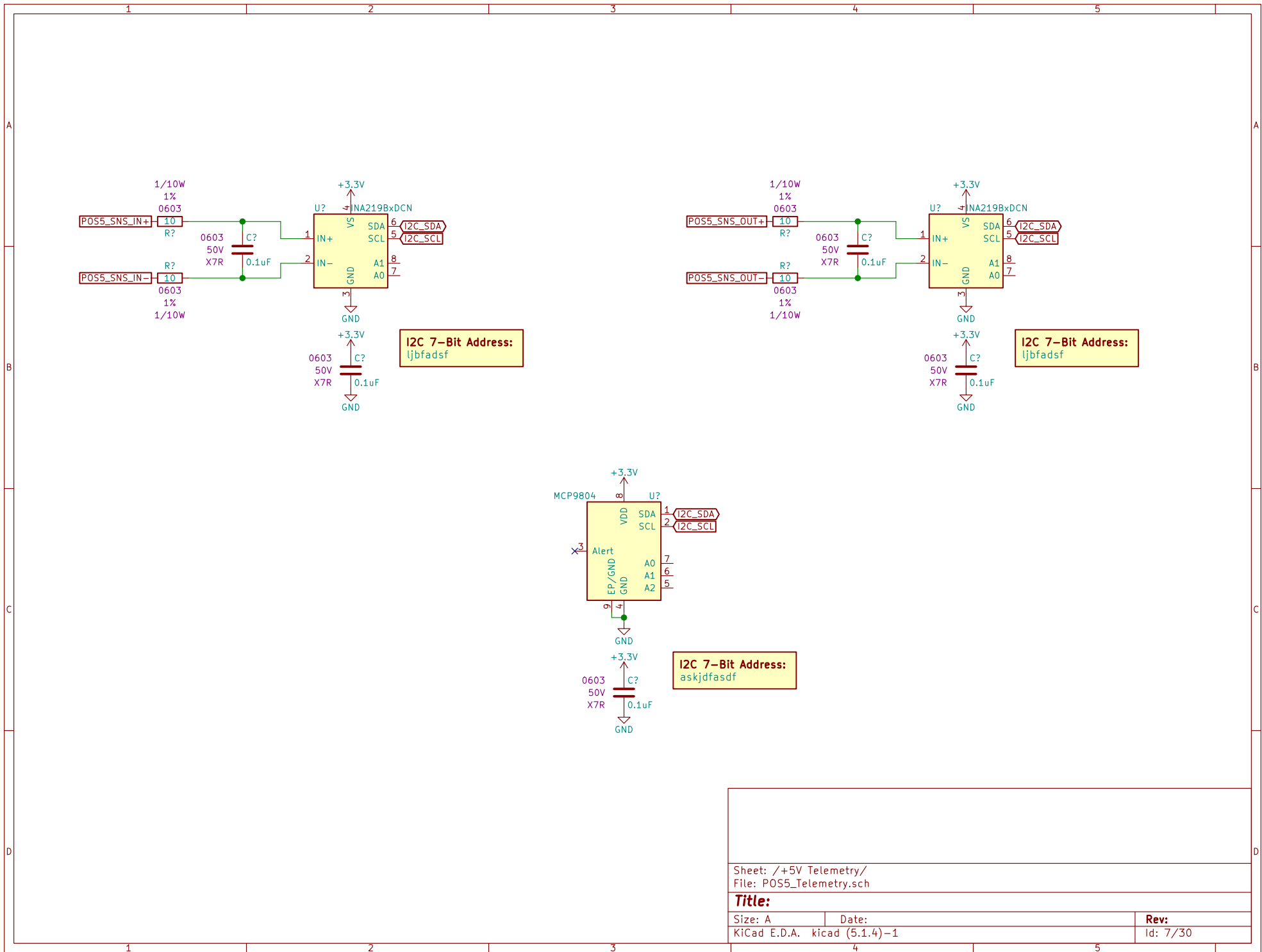


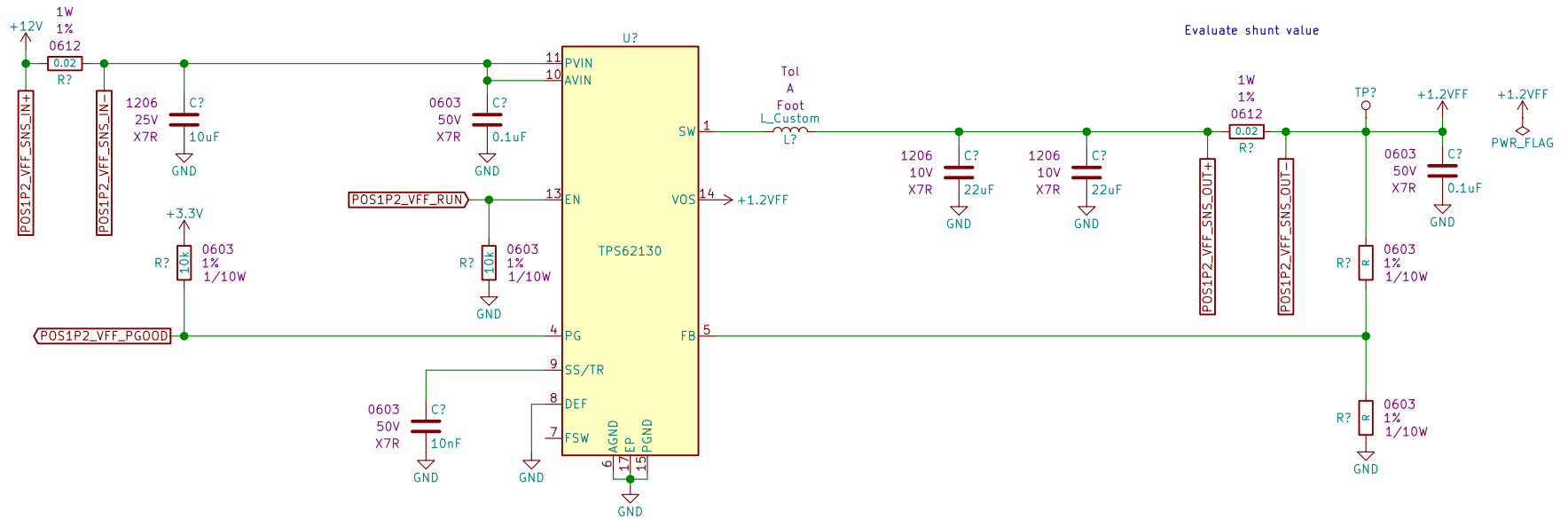
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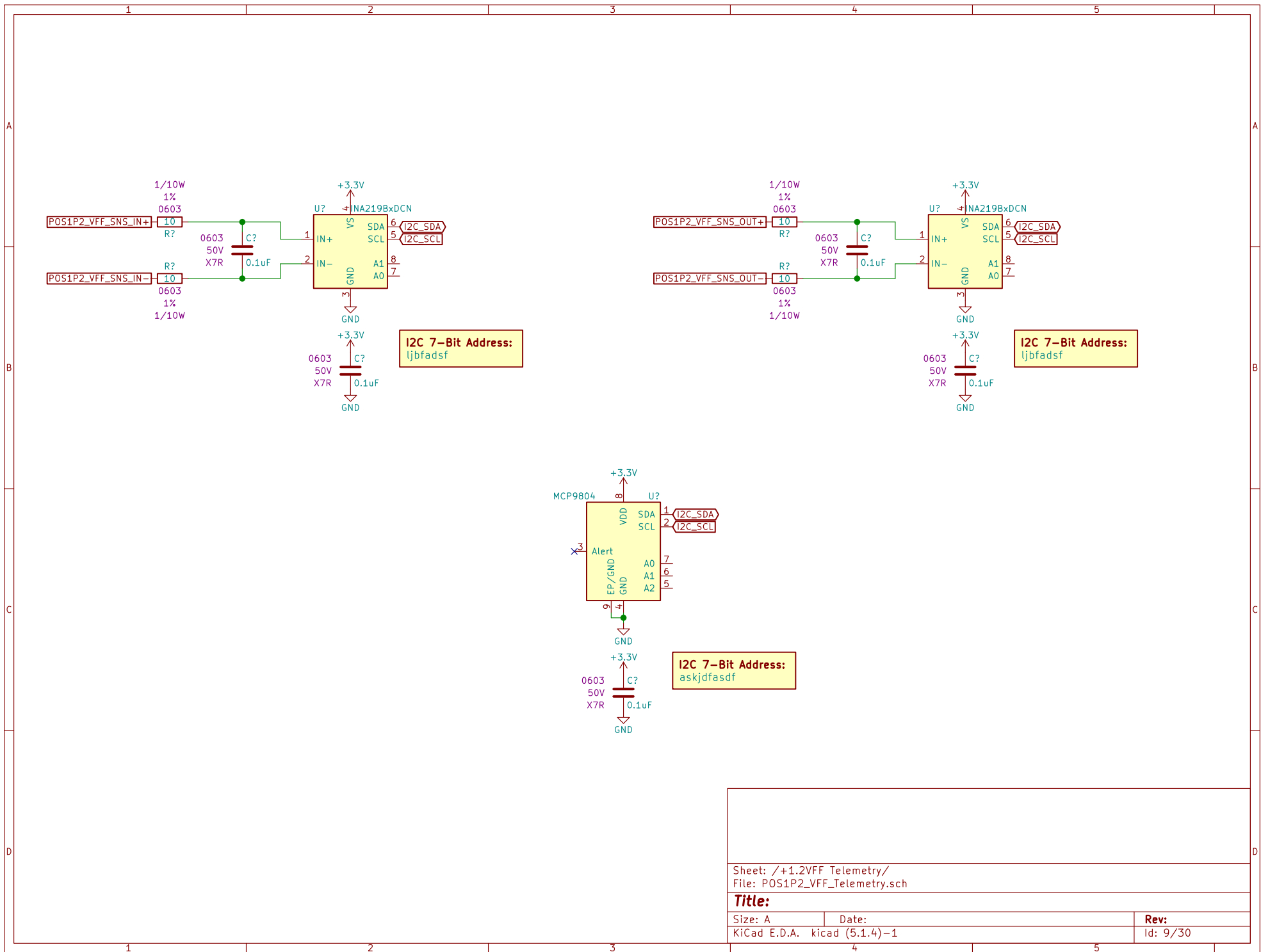


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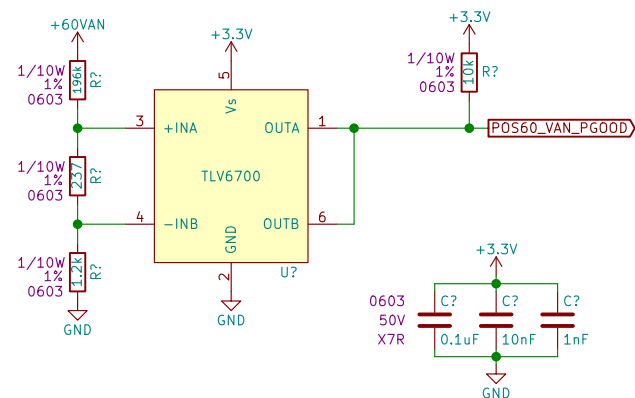
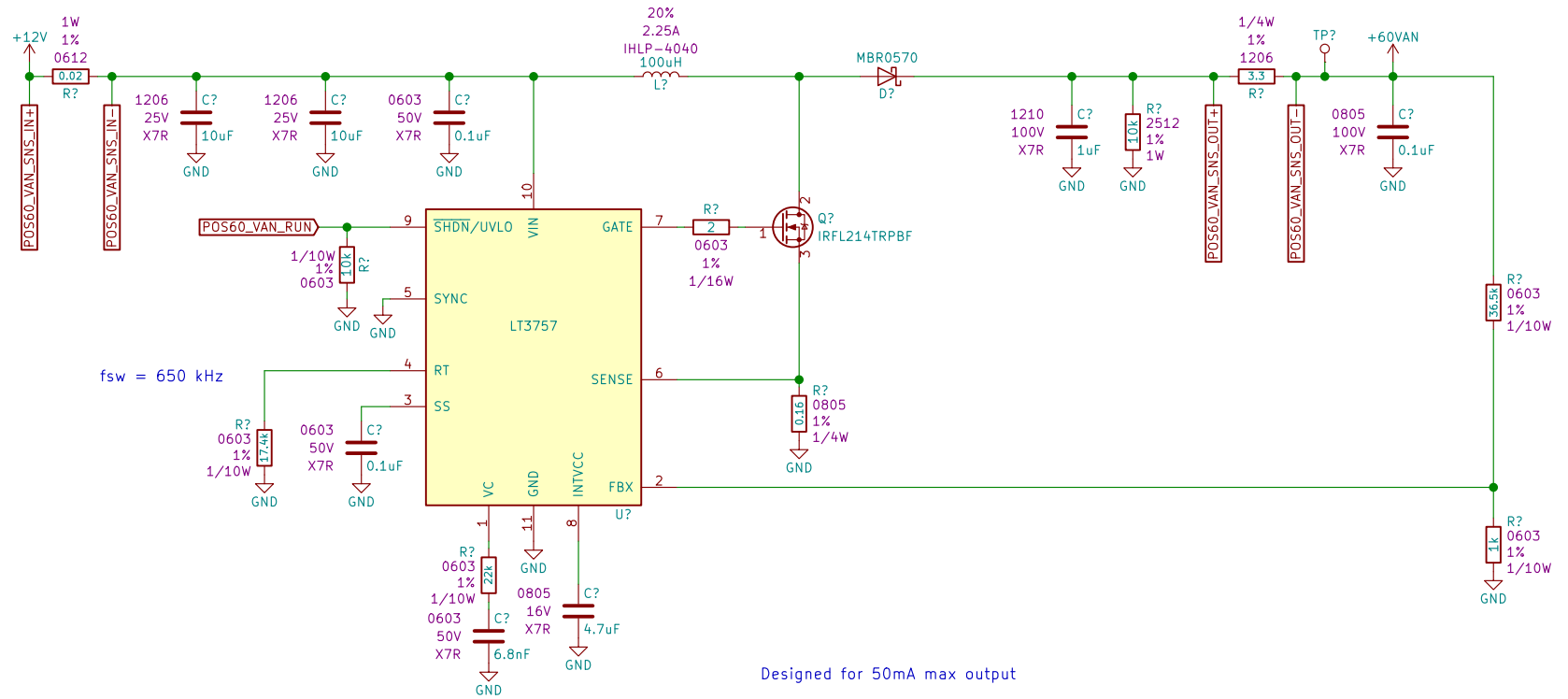
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Anode/Grid +60V, 50mA Power Supply



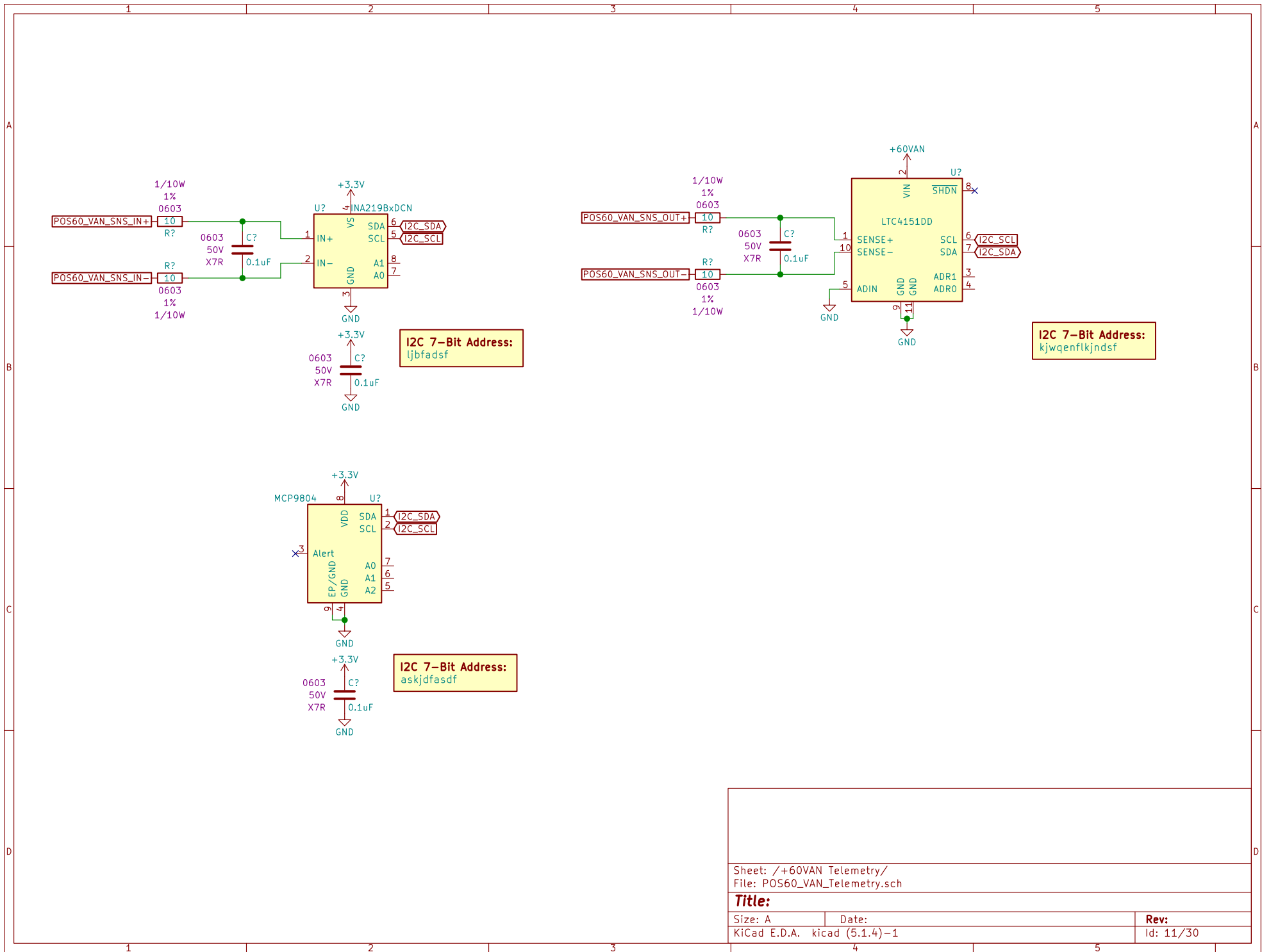
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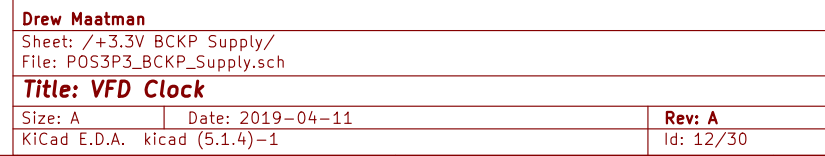
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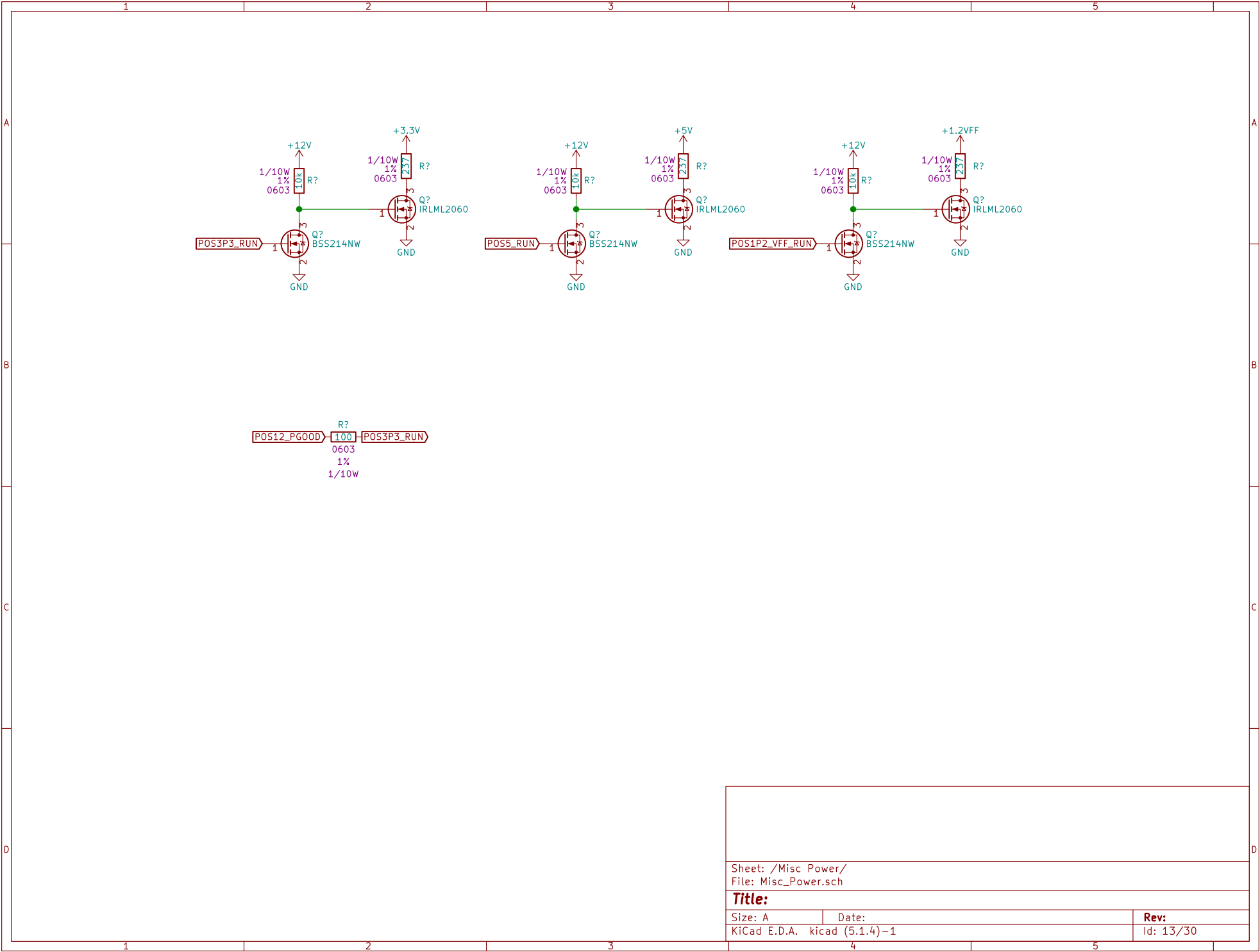
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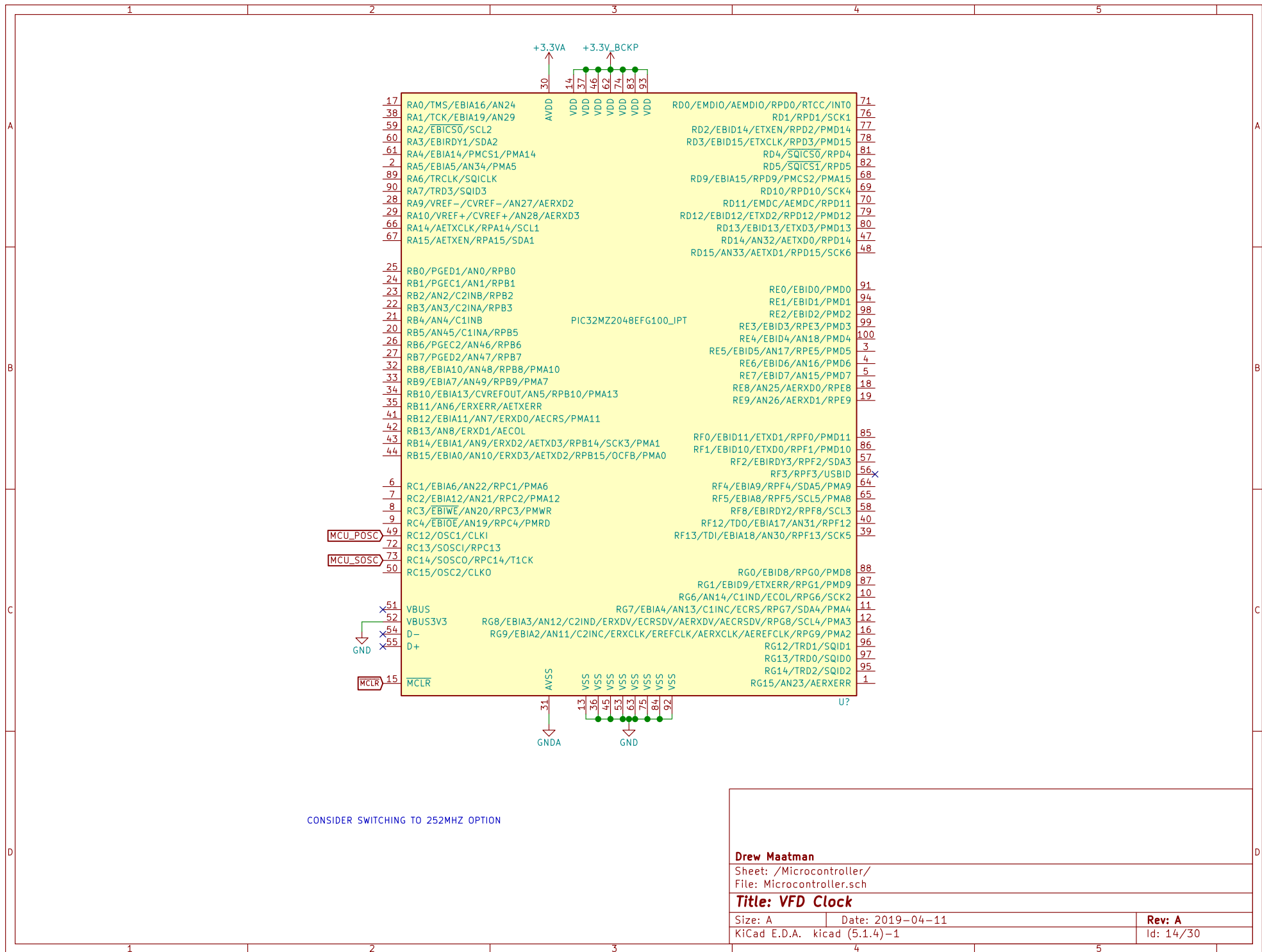
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CONSIDER SWITCHING TO 252MHZ OPTION

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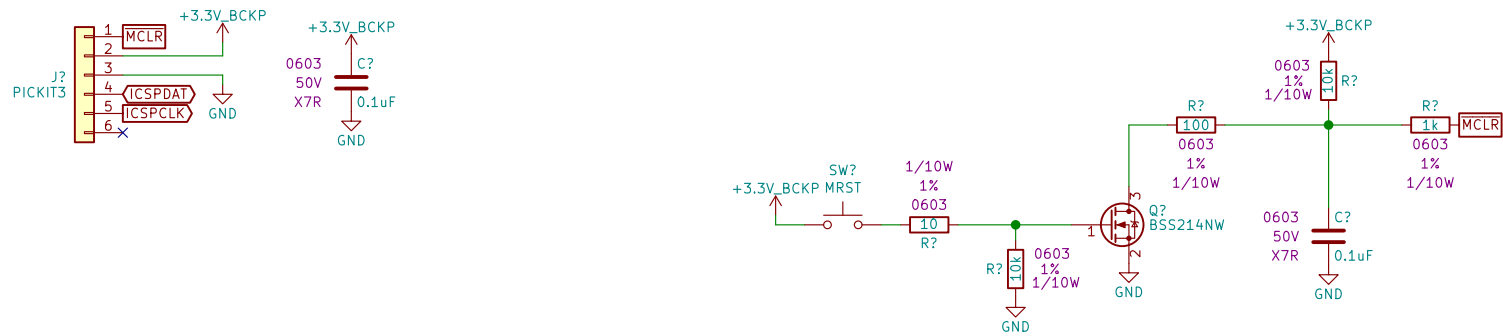
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Title: VFD Clock

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Id: 14/30

Microcontroller Programming



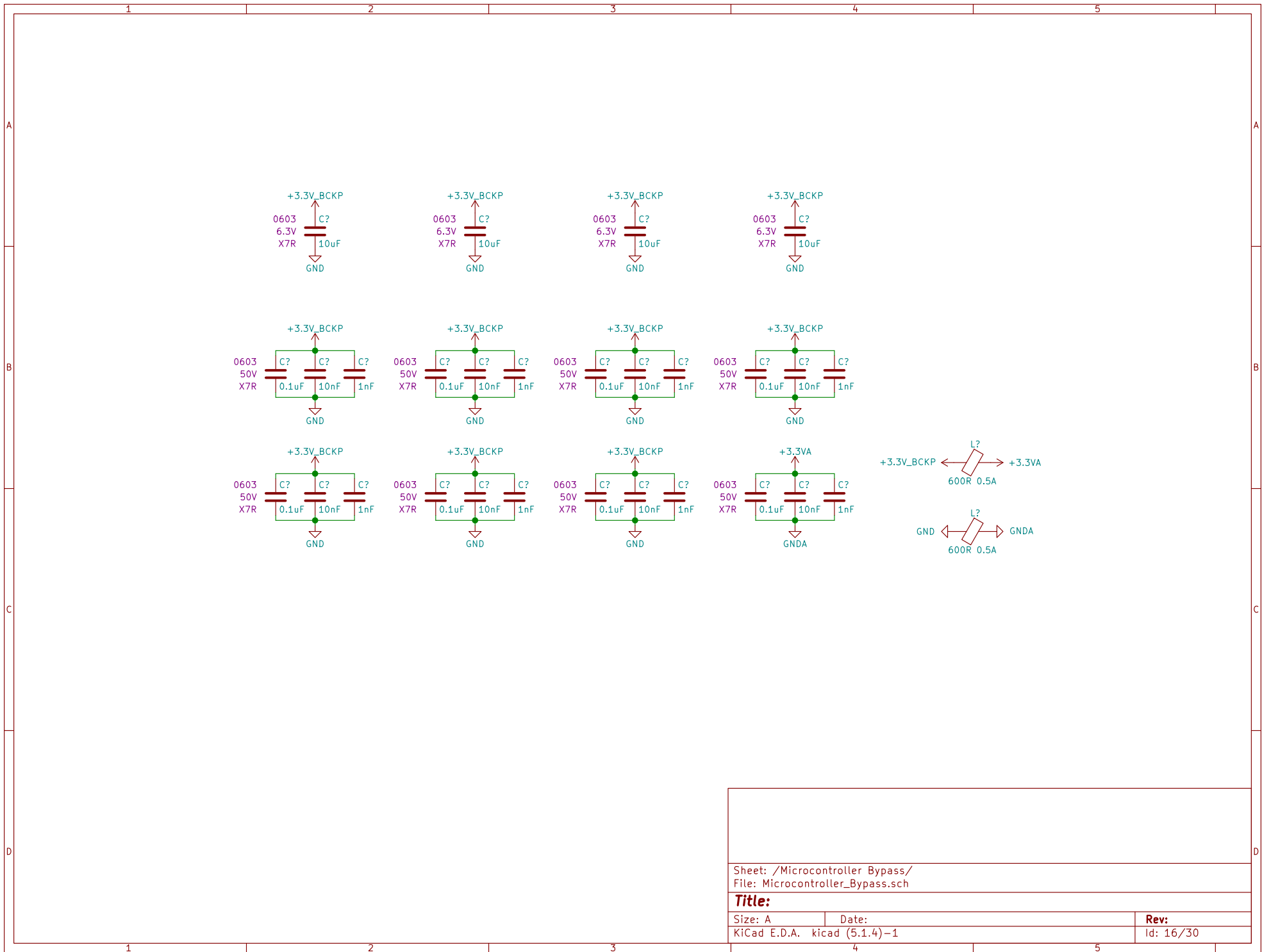
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Sheet: /Microcontroller Bypass/
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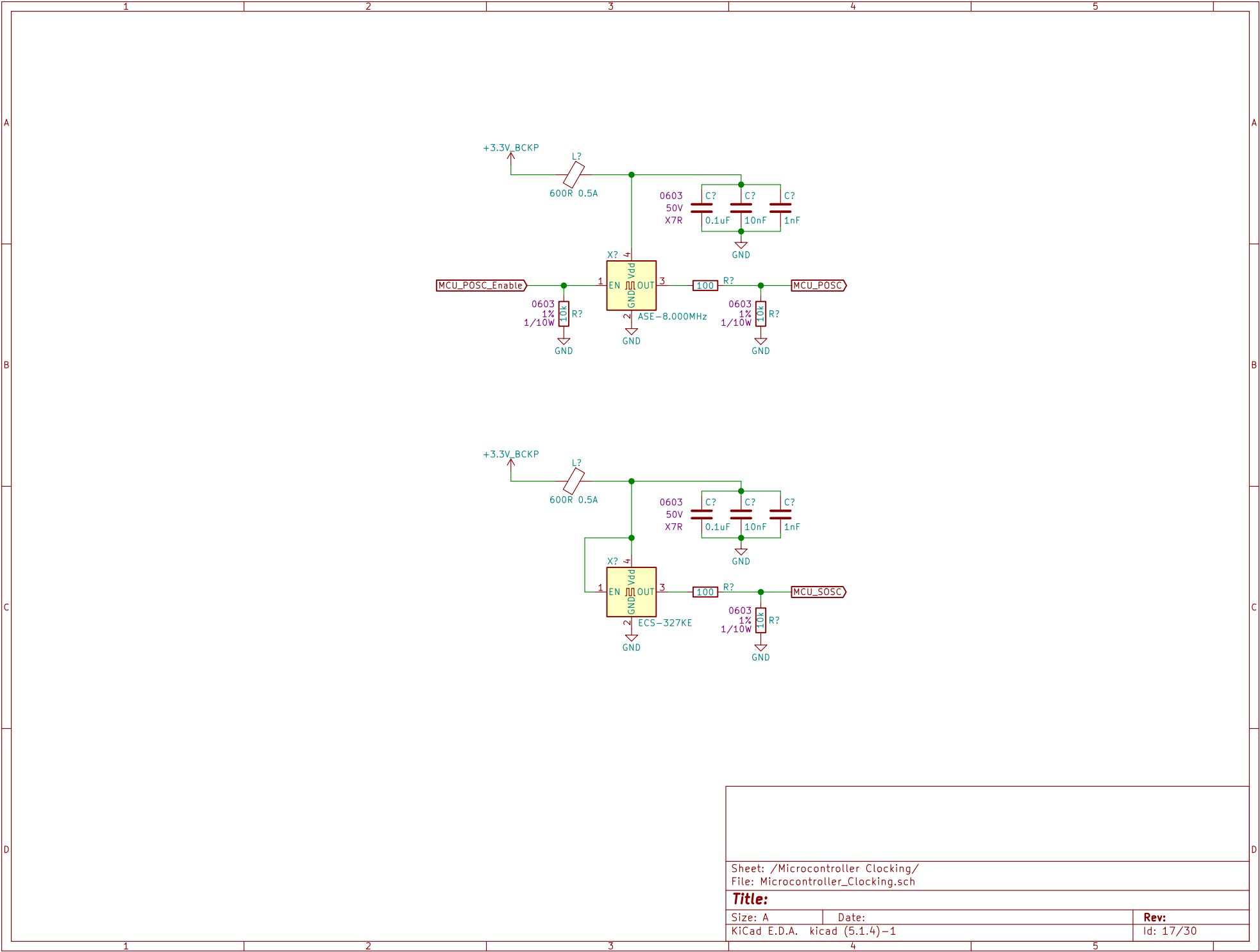
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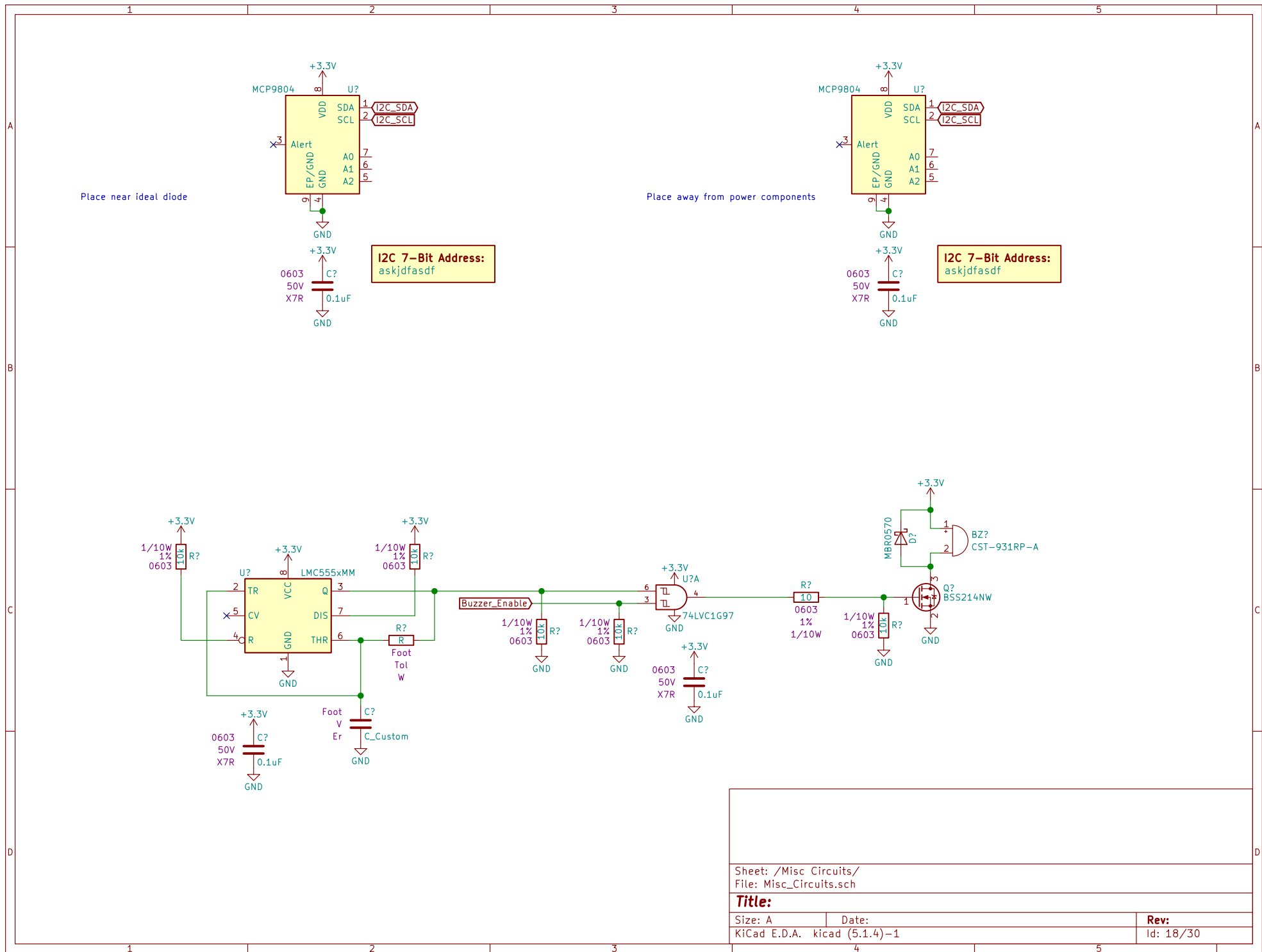
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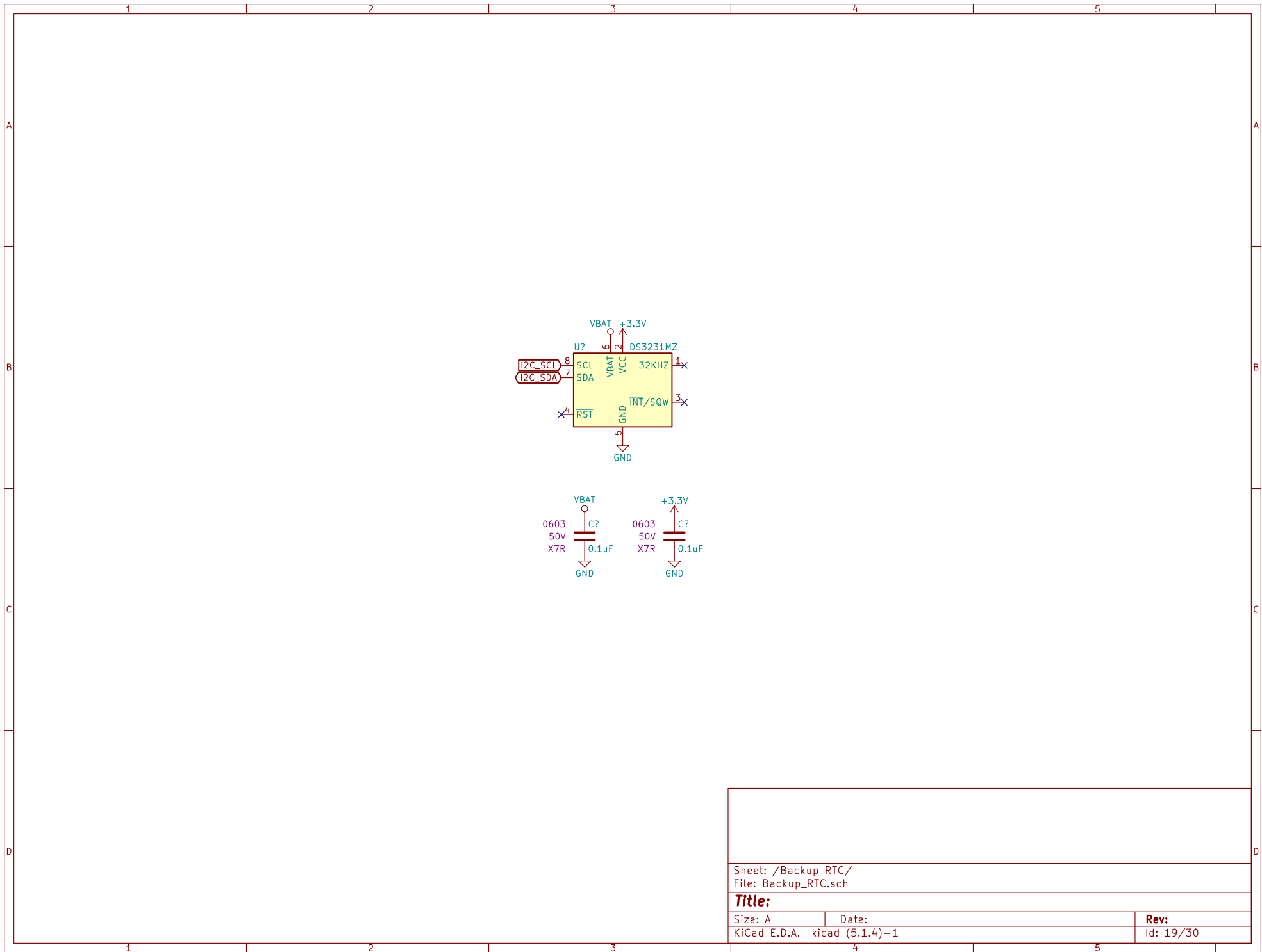
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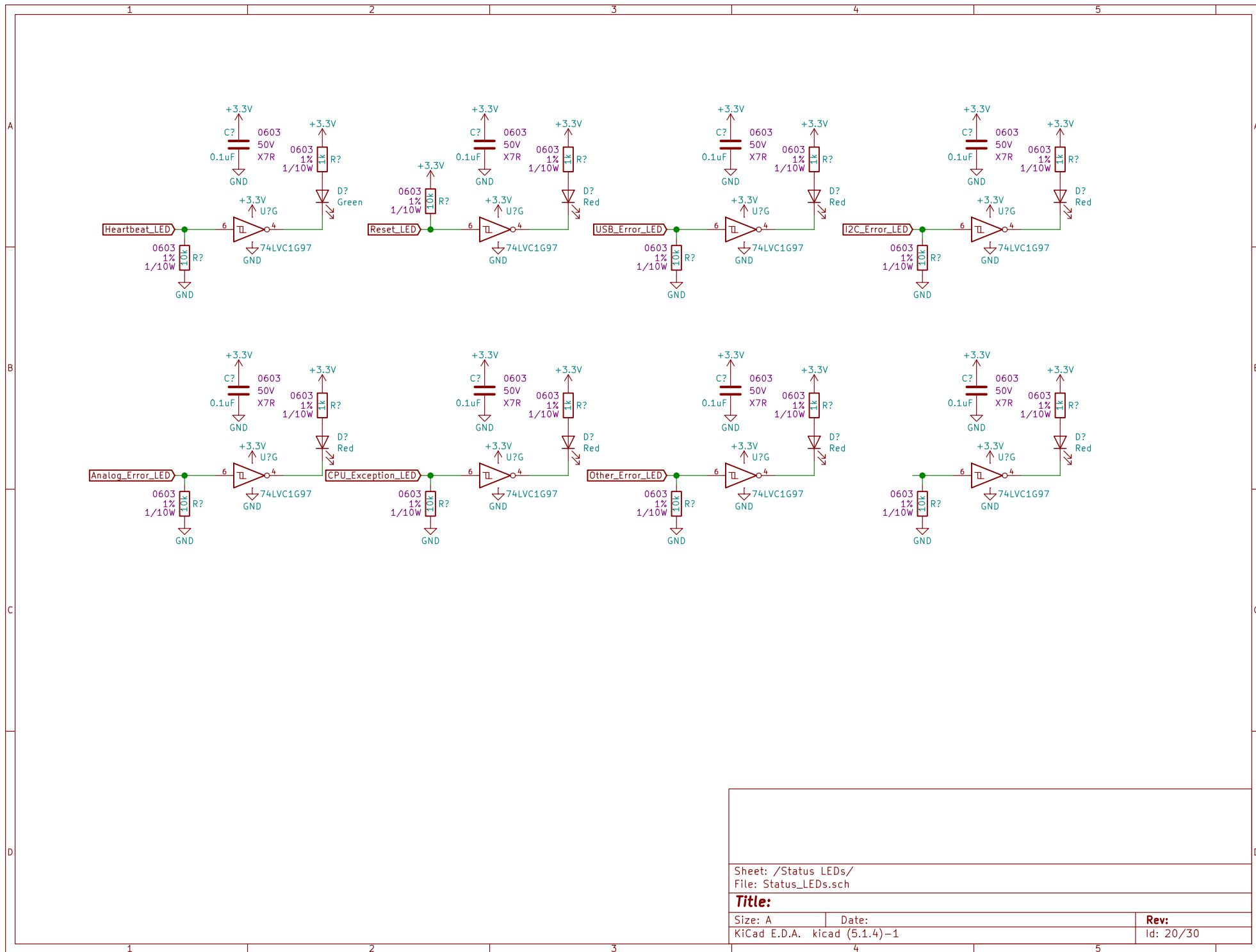


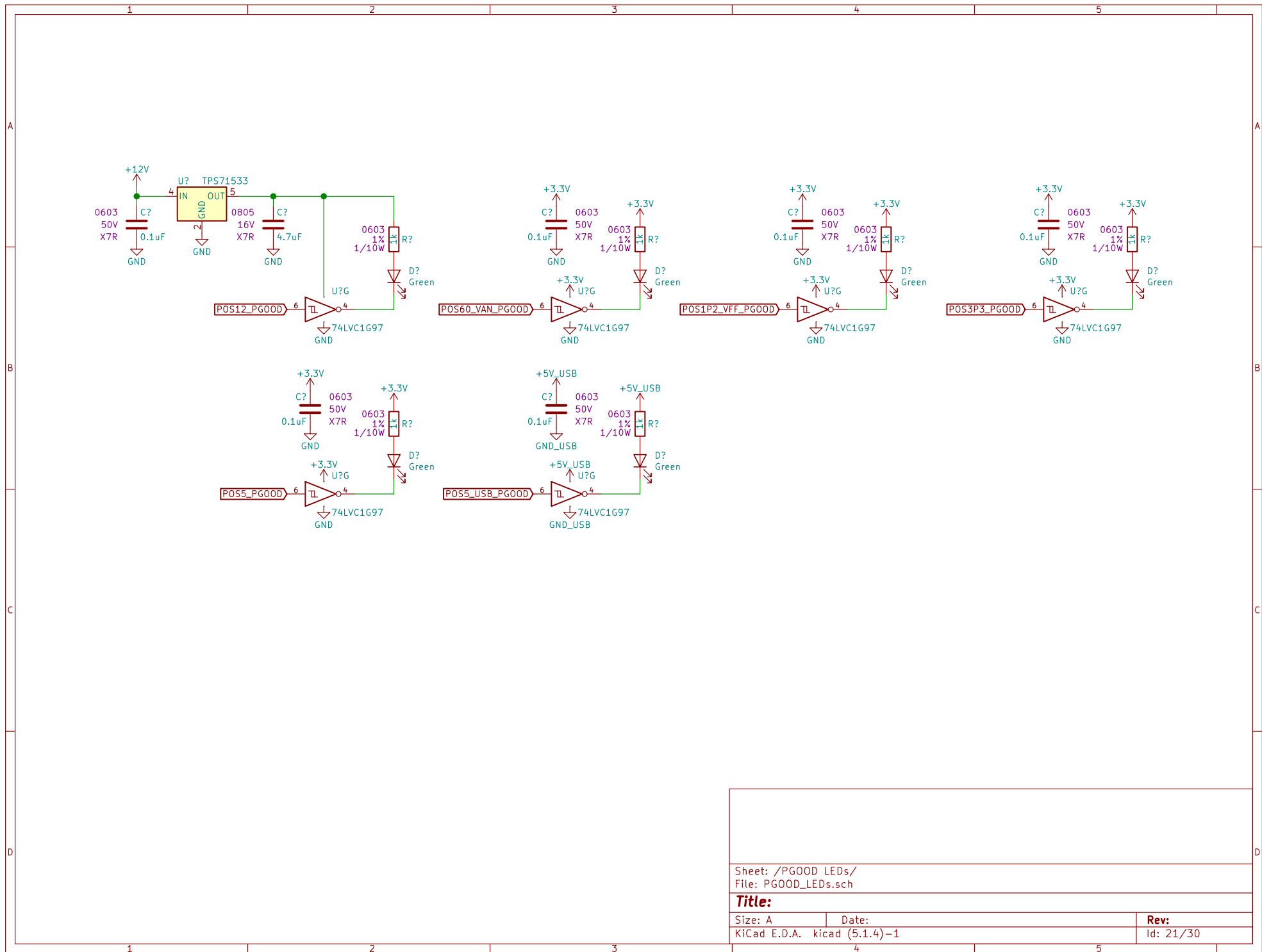
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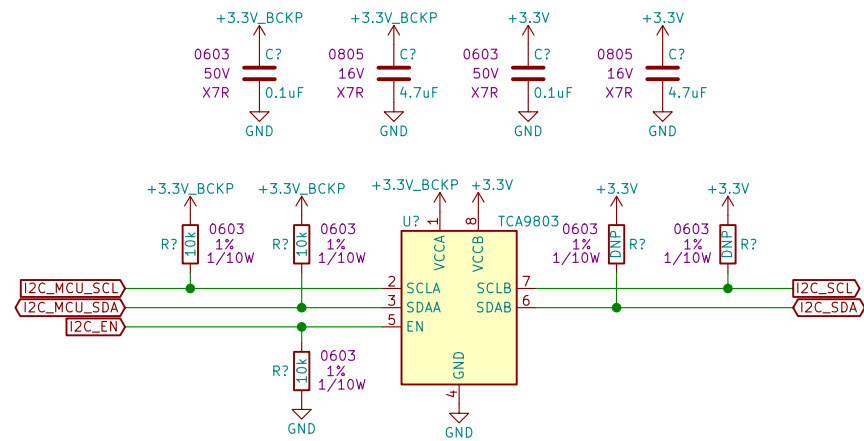
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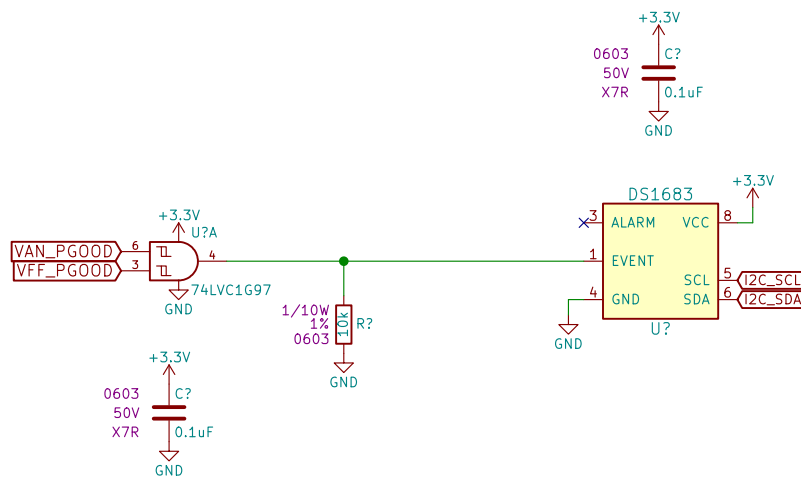
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Rev:

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Sheet: /Time of Flight/
File: Time_of_Flight.sch

Title:

Size: A

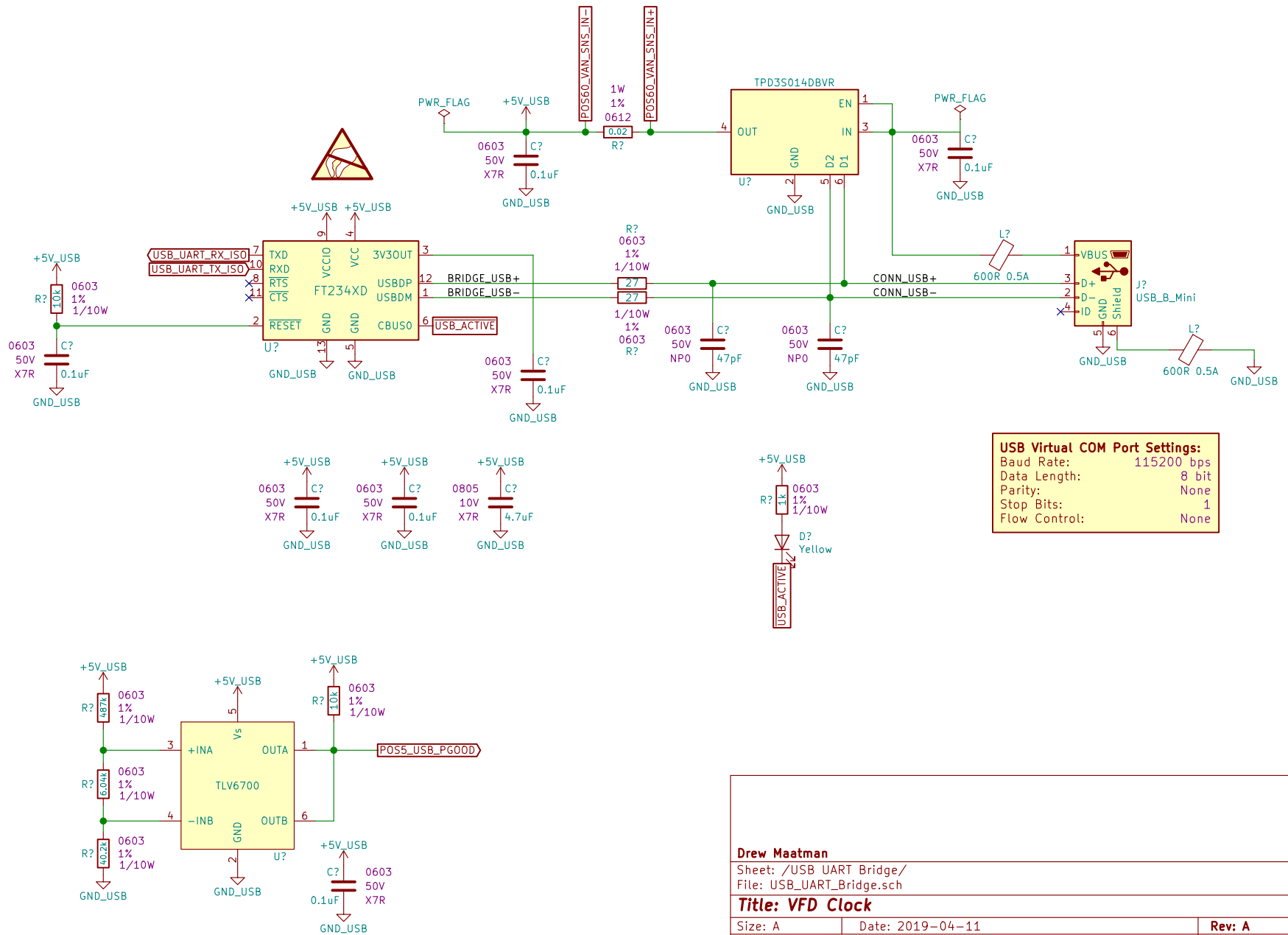
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Rev:

Id: 23/30

11. USB UART Bridge



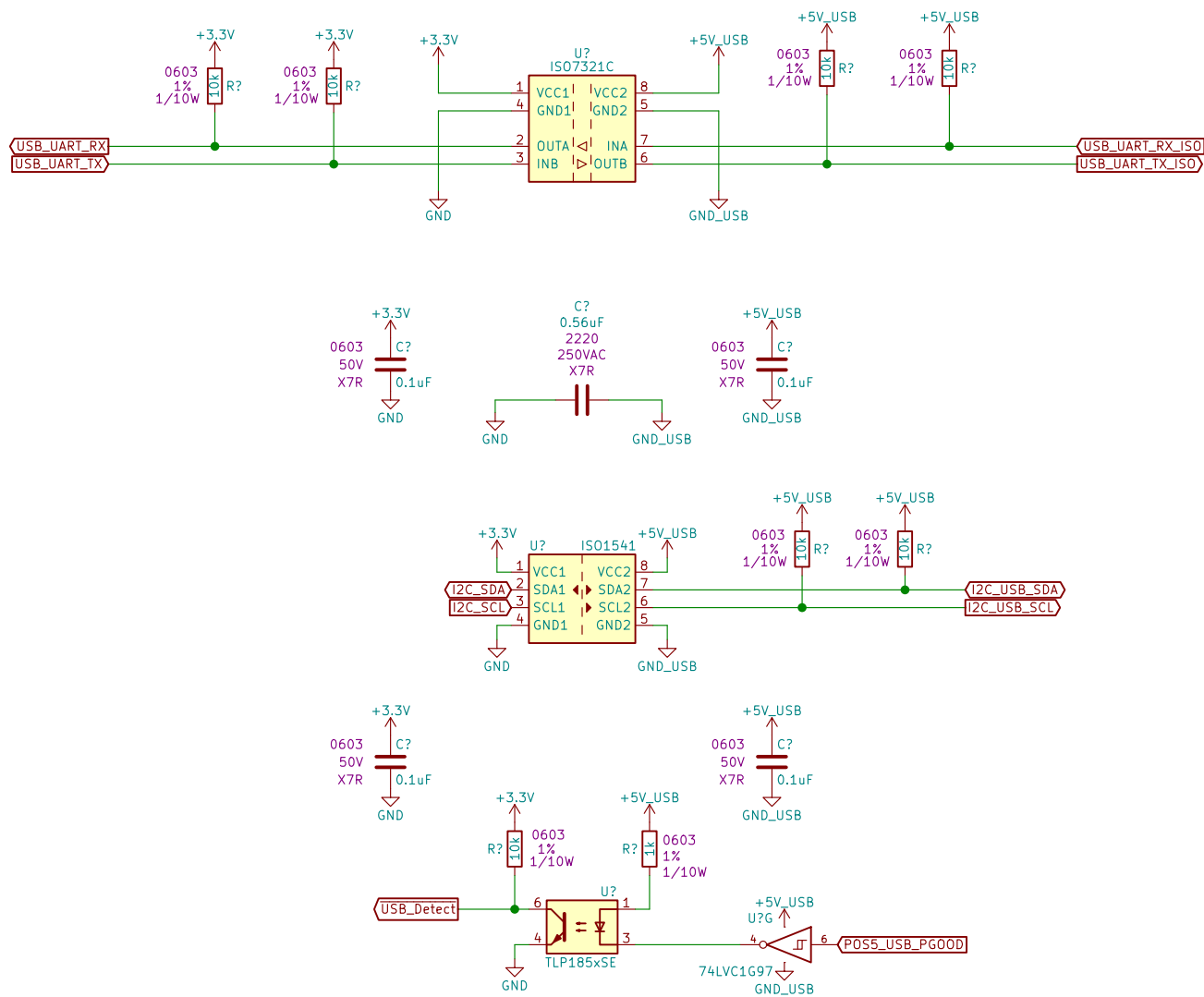
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Title: VFD Clock

Size: A Date: 2019-04-11
KiCad E.D.A. kicad (5.1.4)-1

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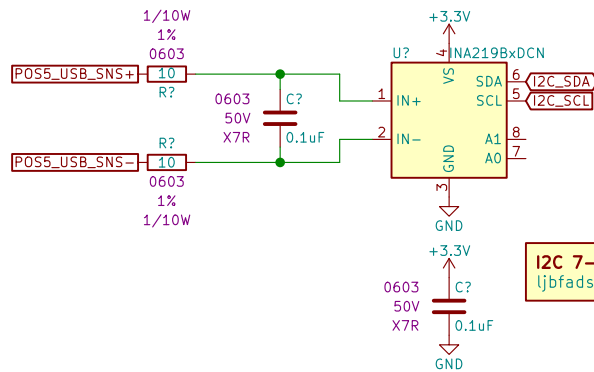
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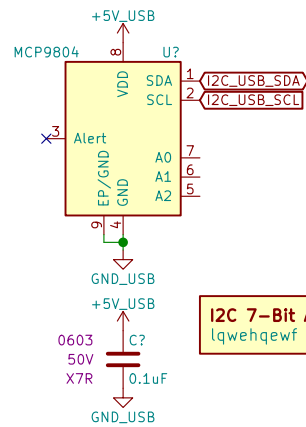
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Rev: A
Id: 25/30



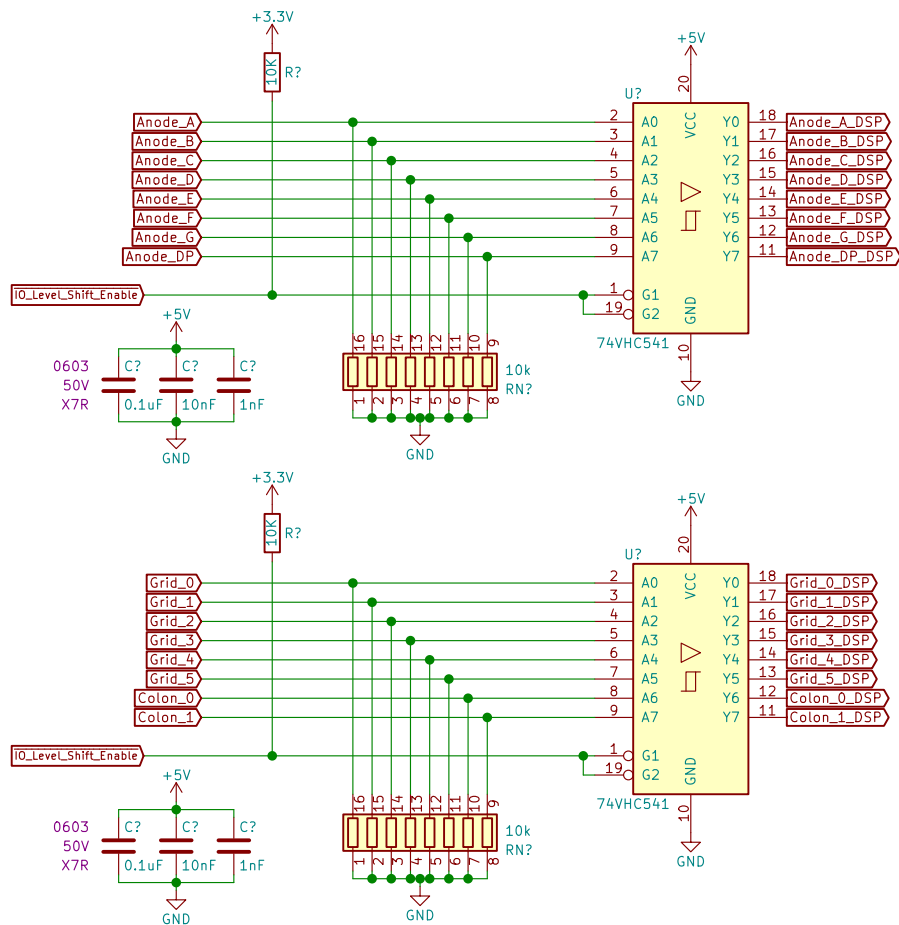
I2C 7-Bit Address:
ljbfadsf



I2C 7-Bit Address:
lqwheqewf

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KiCad E.D.A. kicad (5.1.4)-1		Id: 26/30

I/O Buffers



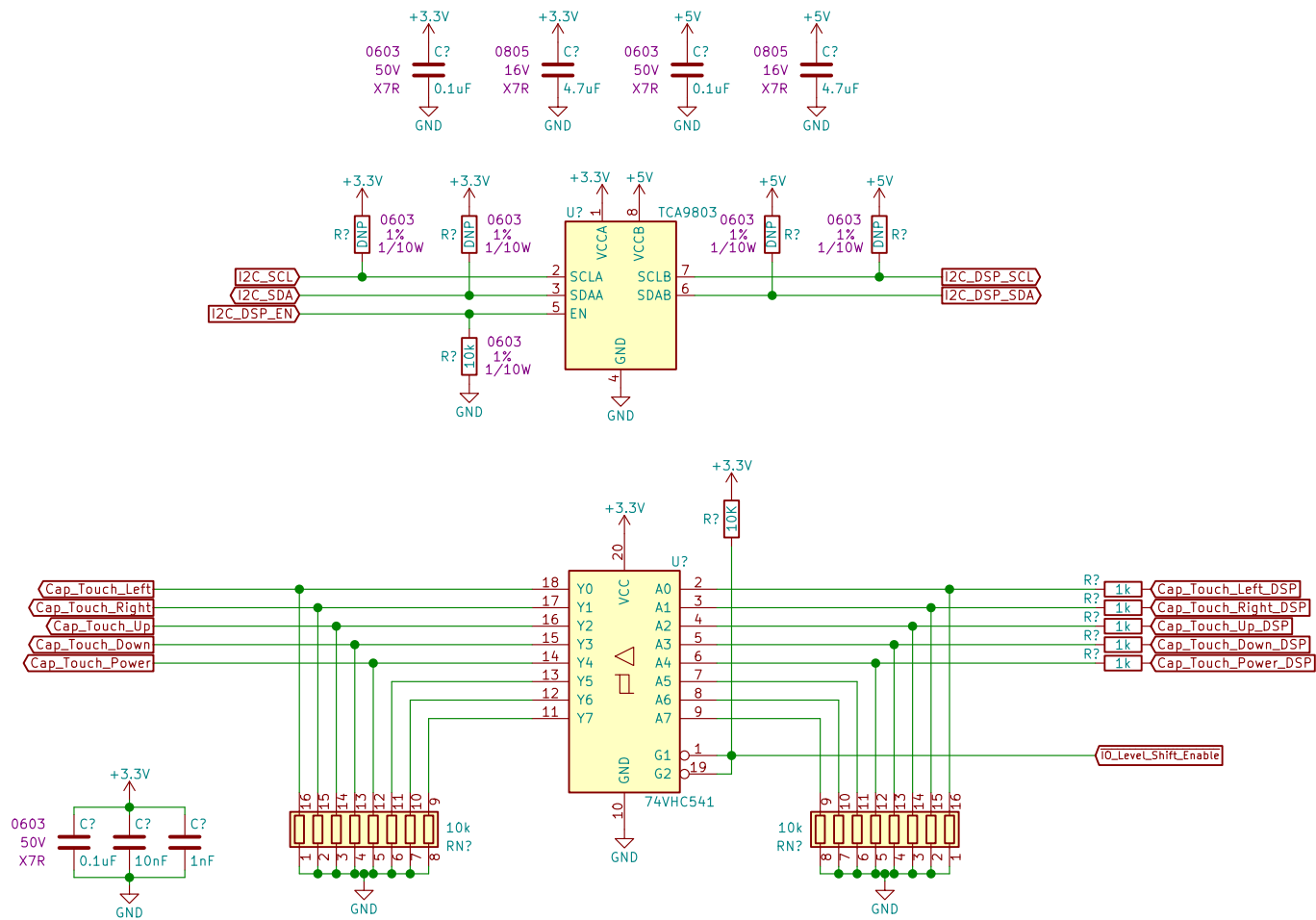
Marquette University Senior Design 2018/2019 Group E44

Sheet: /IO Buffers 1/
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Title: Electronic Display Logic Board

Size: A Date: 2018-12-15
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Rev: A
Id: 27/30

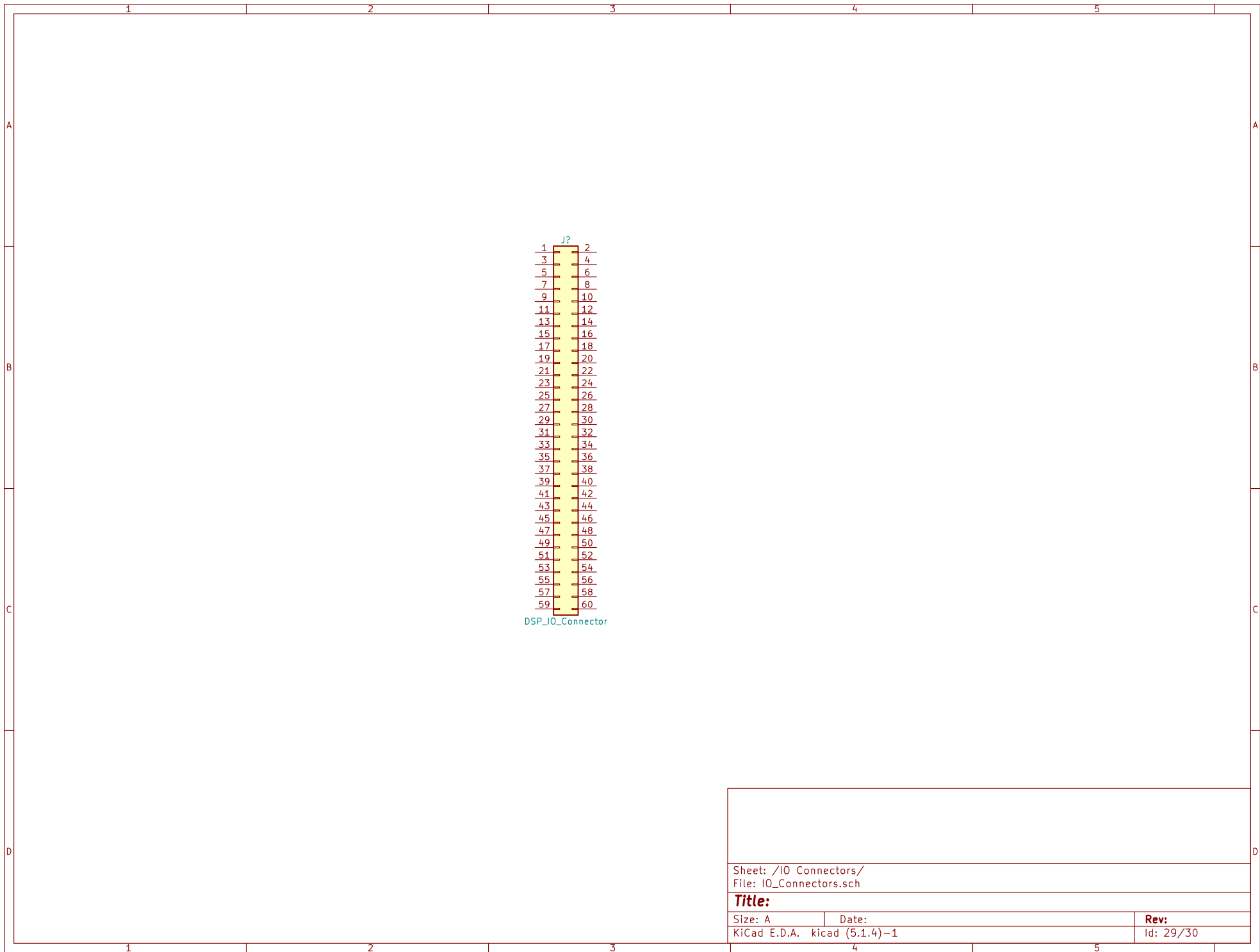


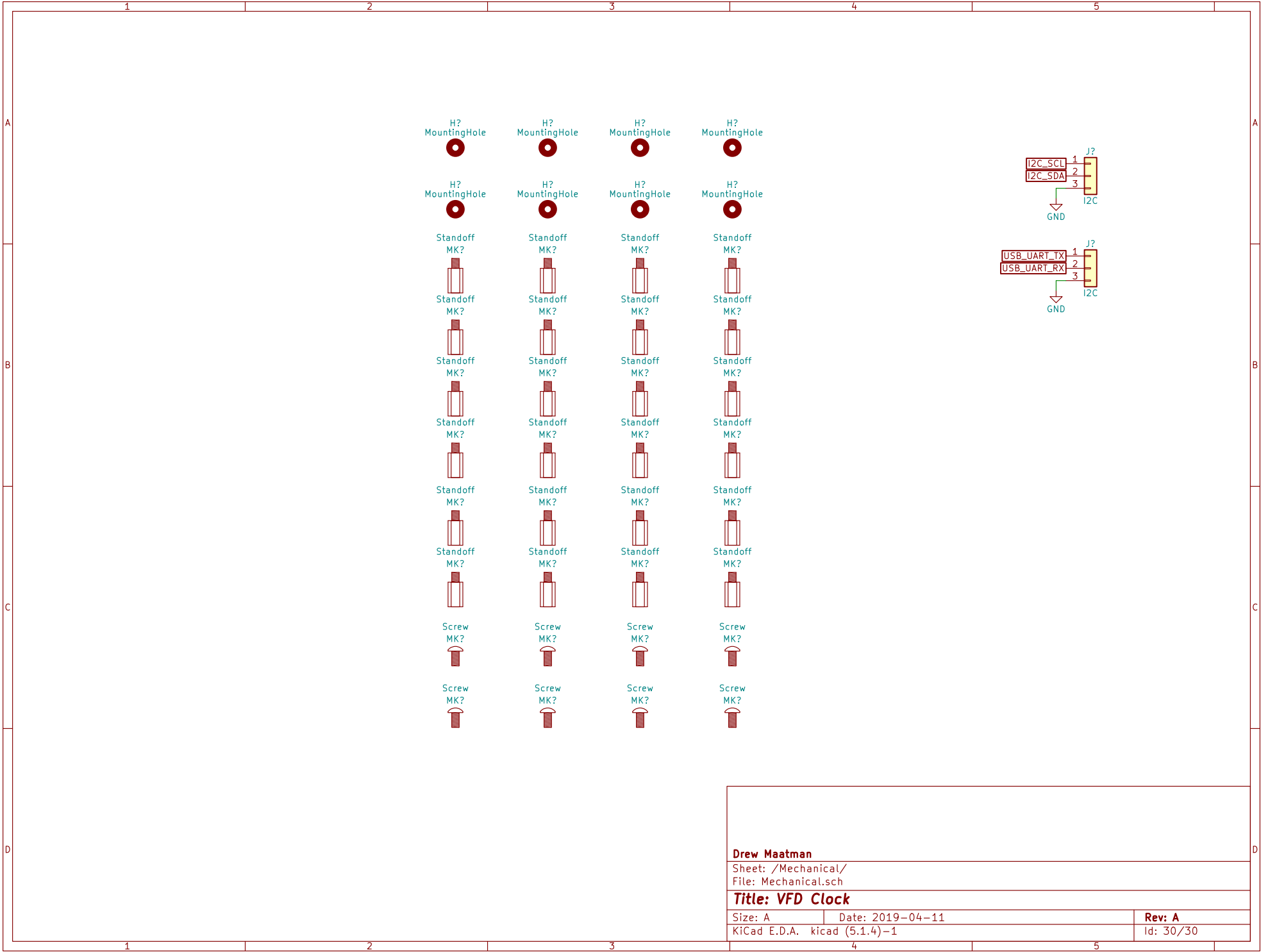
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Rev:
Id: 28/30





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Sheet: /Mechanical/
File: Mechanical.sch

Title: VFD Clock

Size: A Date: 2019-04-11
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Rev: A
Id: 30/30