

Sheet: /		
File: VFD_Clock.sch		
Title:		
Size: A	Date:	Rev:
KiCad E.D.A. kicad 4.0.7		Id: 1/21

The schematic illustrates the power input section of a device, featuring several key components and connections:

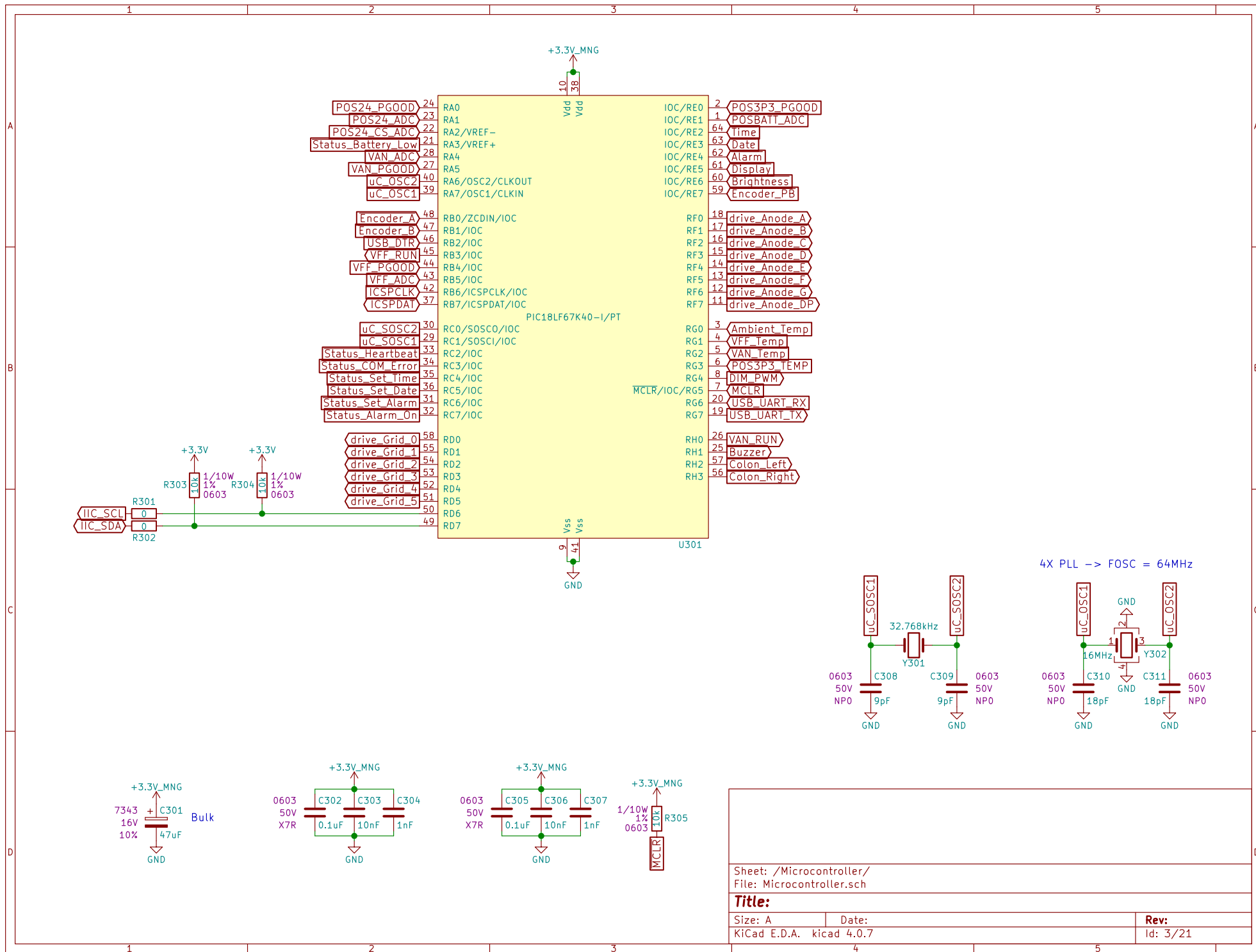
- Input Protection:** The input signal (VIN) is connected to J201. It passes through a fuse (F201, C1Q 2, 1206, 2A) and a diode (Q201, Si2319CDS) to protect against reverse polarity and overvoltage/undervoltage. A PWR\_FLAG signal is generated at this stage.
- Voltage Monitoring:** The input voltage is monitored by a TPS2411PWR (U203) which provides UV (Undervoltage), OV (Overvoltage), and PWR\_FLAG signals. The UV signal is connected to R205 (1/10W, 1%, 0603) and the OV signal to R207 (1/10W, 1%, 0603). The PWR\_FLAG signal is connected to R206 (1/10W, 1%, 0603).
- Signal Conditioning:** The UV signal is also connected to R208 (1/10W, 1%, 0603) and the OV signal to R209 (1/10W, 1%, 0603). The PWR\_FLAG signal is connected to R204 (1k, 0603, 1/16W) and the UV signal to R203 (0, 0603, 1/16W).
- Power Regulation:** The input voltage is regulated by a TPS2411PWR (U203) which provides a +3.3V output. This output is connected to R201 (1/10W, 1%, 0603) and the UV signal to R202 (1/10W, 1%, 0603).
- Signal Processing:** The UV signal is also connected to R205 (1/10W, 1%, 0603) and the OV signal to R207 (1/10W, 1%, 0603). The PWR\_FLAG signal is connected to R206 (1/10W, 1%, 0603).
- Signal Conditioning:** The UV signal is also connected to R208 (1/10W, 1%, 0603) and the OV signal to R209 (1/10W, 1%, 0603). The PWR\_FLAG signal is connected to R204 (1k, 0603, 1/16W) and the UV signal to R203 (0, 0603, 1/16W).

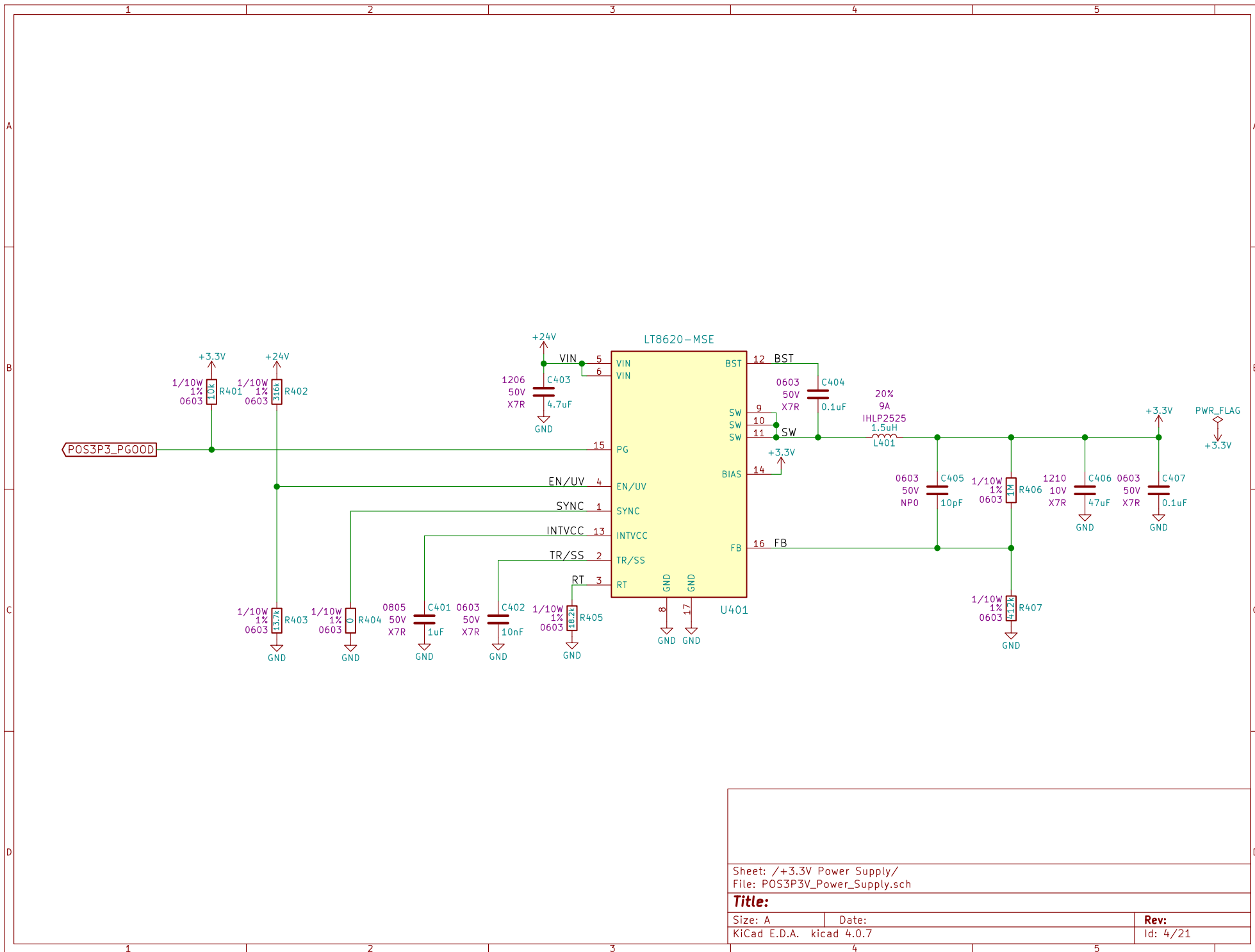
The schematic also shows a +3.3V\_MNG signal connected to C201 (0.1uF, 0603, 50V, X7R) and C202 (10nF, 0603, 50V, X7R). The +3.3V signal is connected to C203 (0.1uF, 0603, 50V, X7R) and C204 (1nF, 0603, 50V, X7R). The +24V signal is connected to C205 (47uF, 7343, 16V, 10%) and C206 (0.1uF, 0603, 50V, X7R).

Sheet: /Power Input/  
File: PowerInput.sch

Size: A	Date:
KiCad E.D.A.	kiCad 4.0.7

Rev: 2  
Id: 2/21





Sheet: /+3.3V Power Supply/  
File: POS3P3V\_Power\_Supply.sch

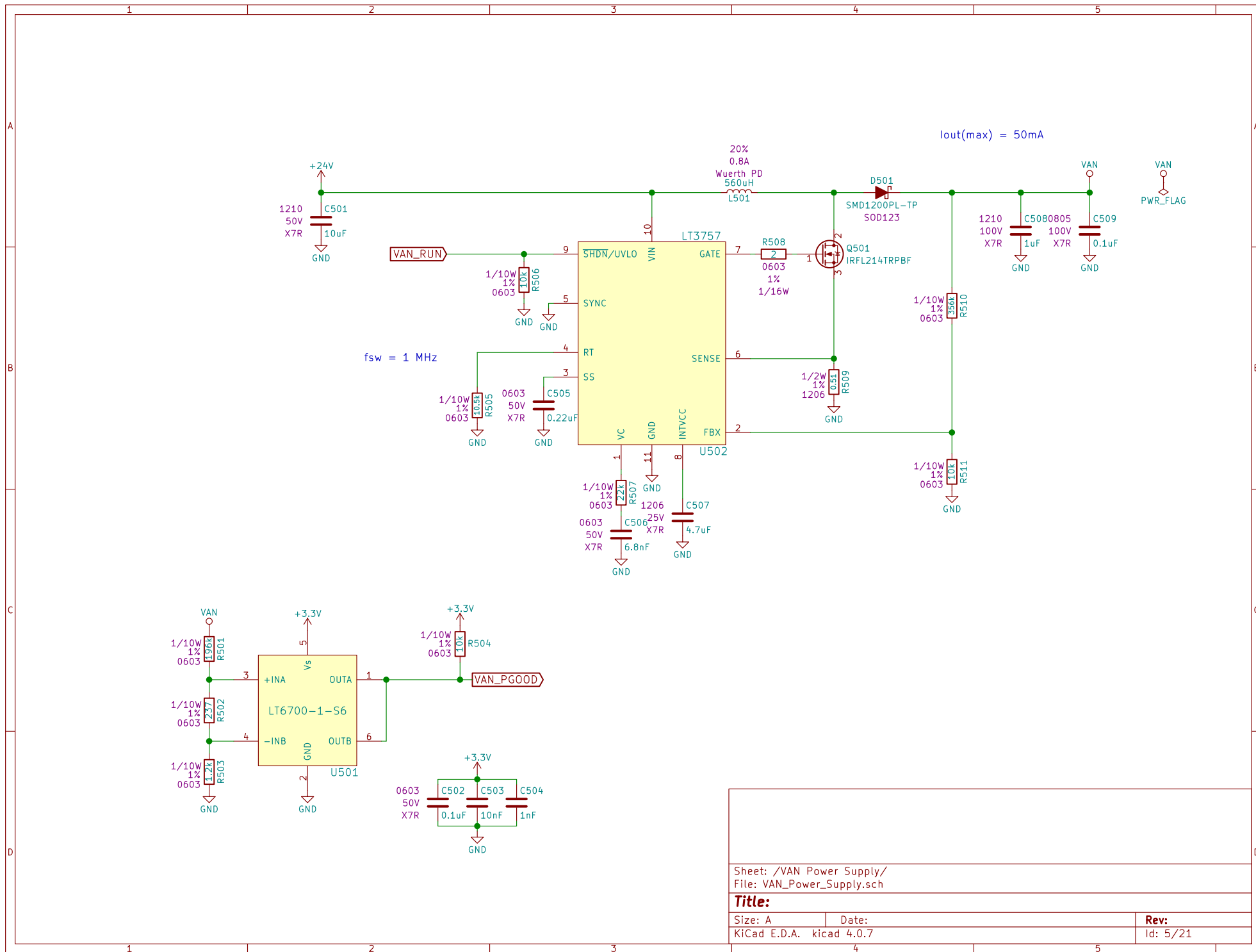
**Title:**

Size: A  
KiCad E.D.A. kicad 4.0.7

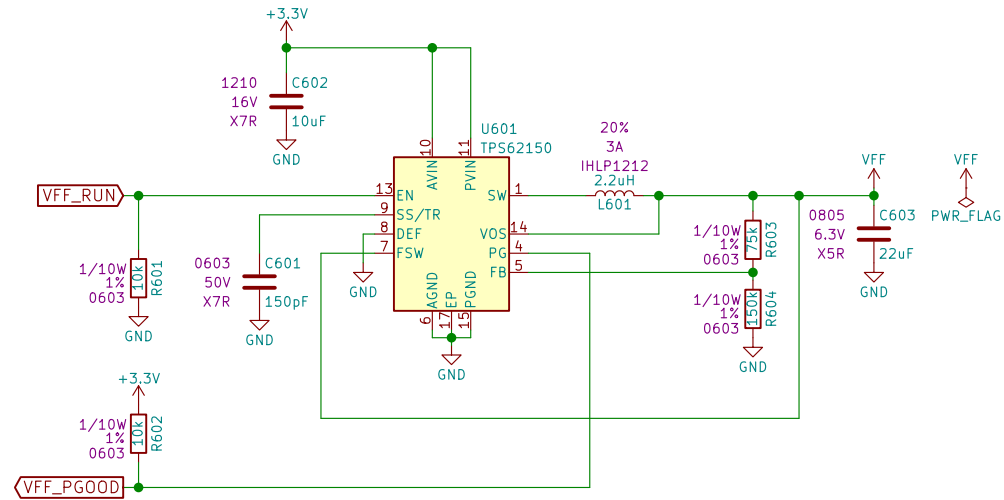
Date:

Rev:

Id: 4/21



# +1.2V Filament Voltage Power Supply



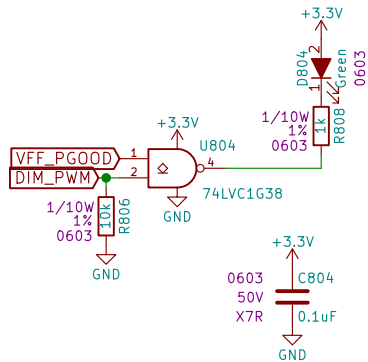
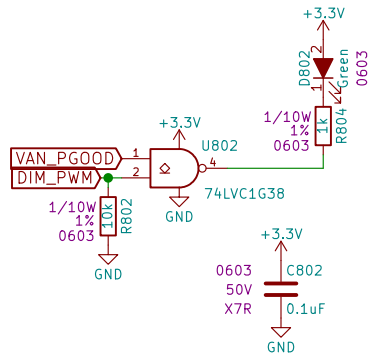
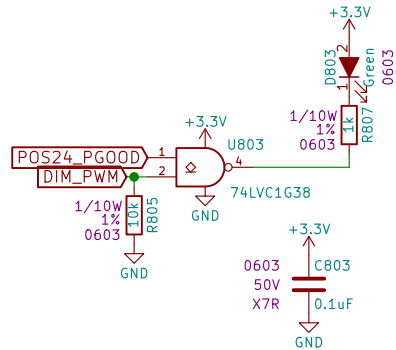
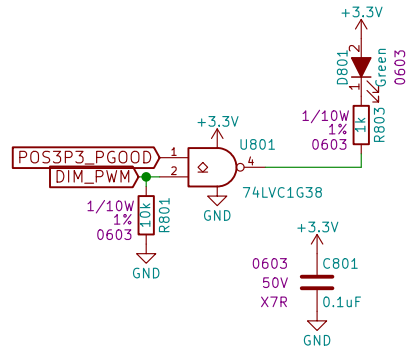
Sheet: /VFF Power Supply/  
File: VFF\_Power\_Supply.sch

## Title:

Size: A Date:  
KiCad E.D.A. kicad 4.0.7

Rev:  
Id: 6/21

Id: 7/21



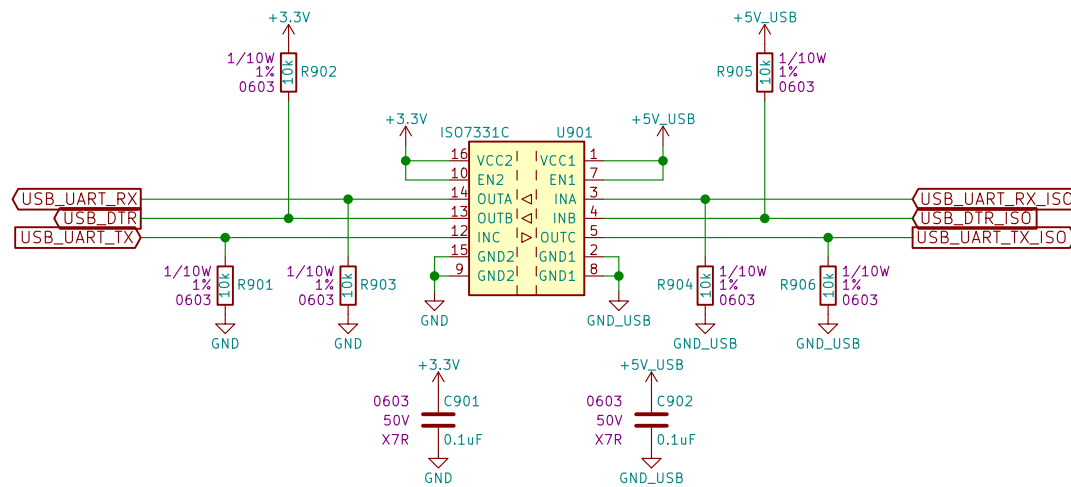
Sheet: /PGOOD Indicators/  
File: PGOOD\_Indicators.sch

**Title:**

Size: A Date:  
KiCad E.D.A. kicad 4.0.7

**Rev:**  
Id: 8/21



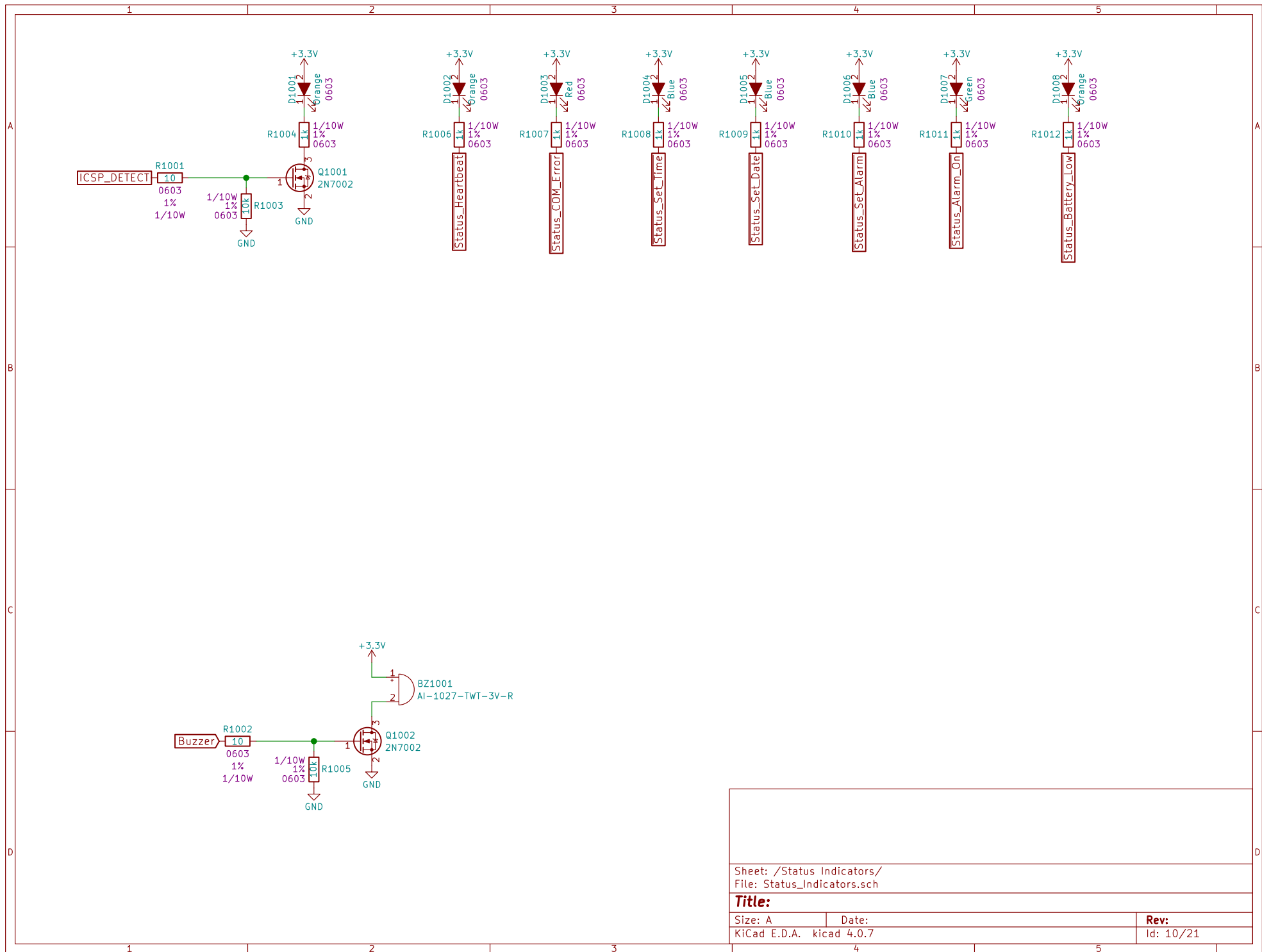


Sheet: /UART Isolation/  
File: UART\_Isolation.sch

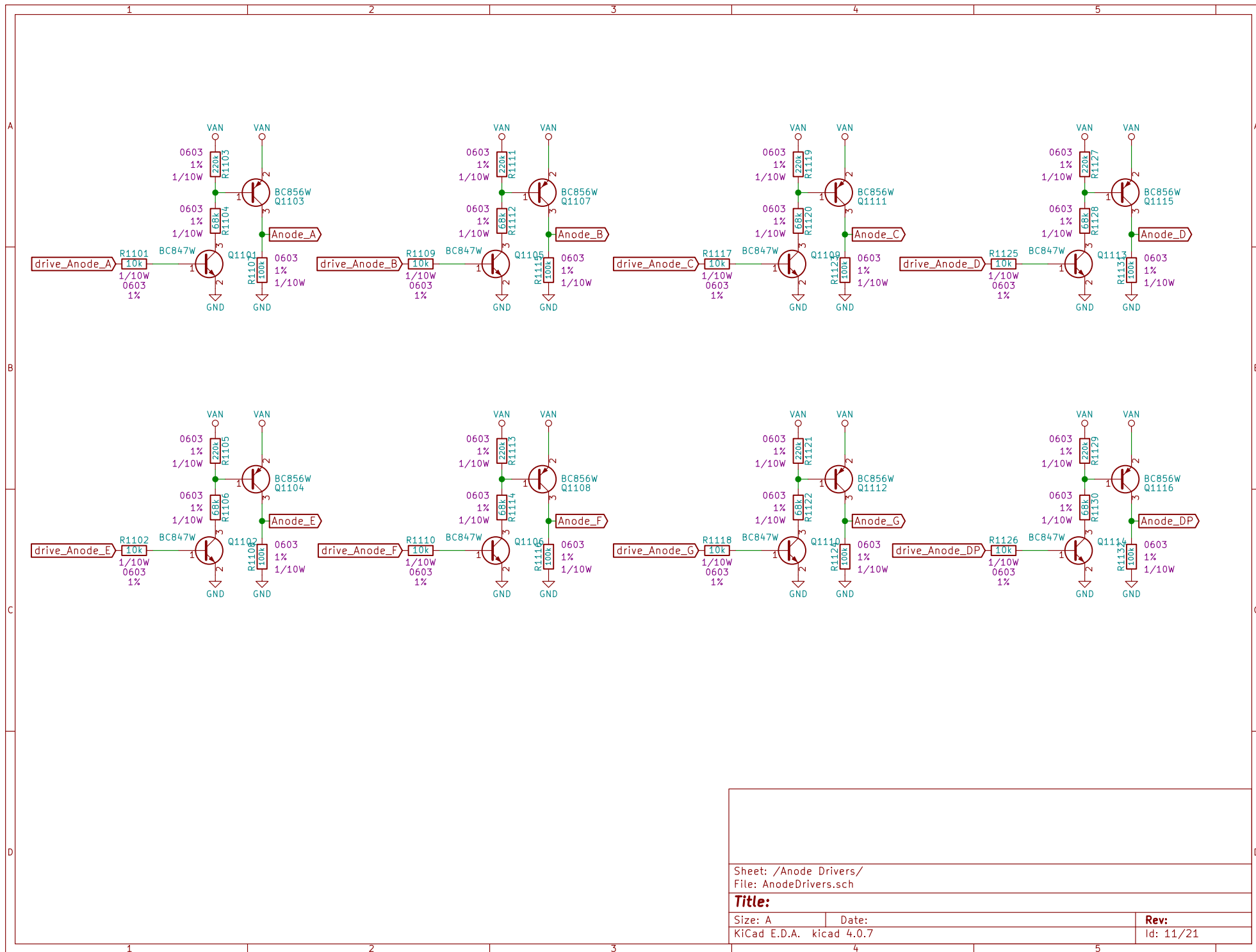
**Title:**

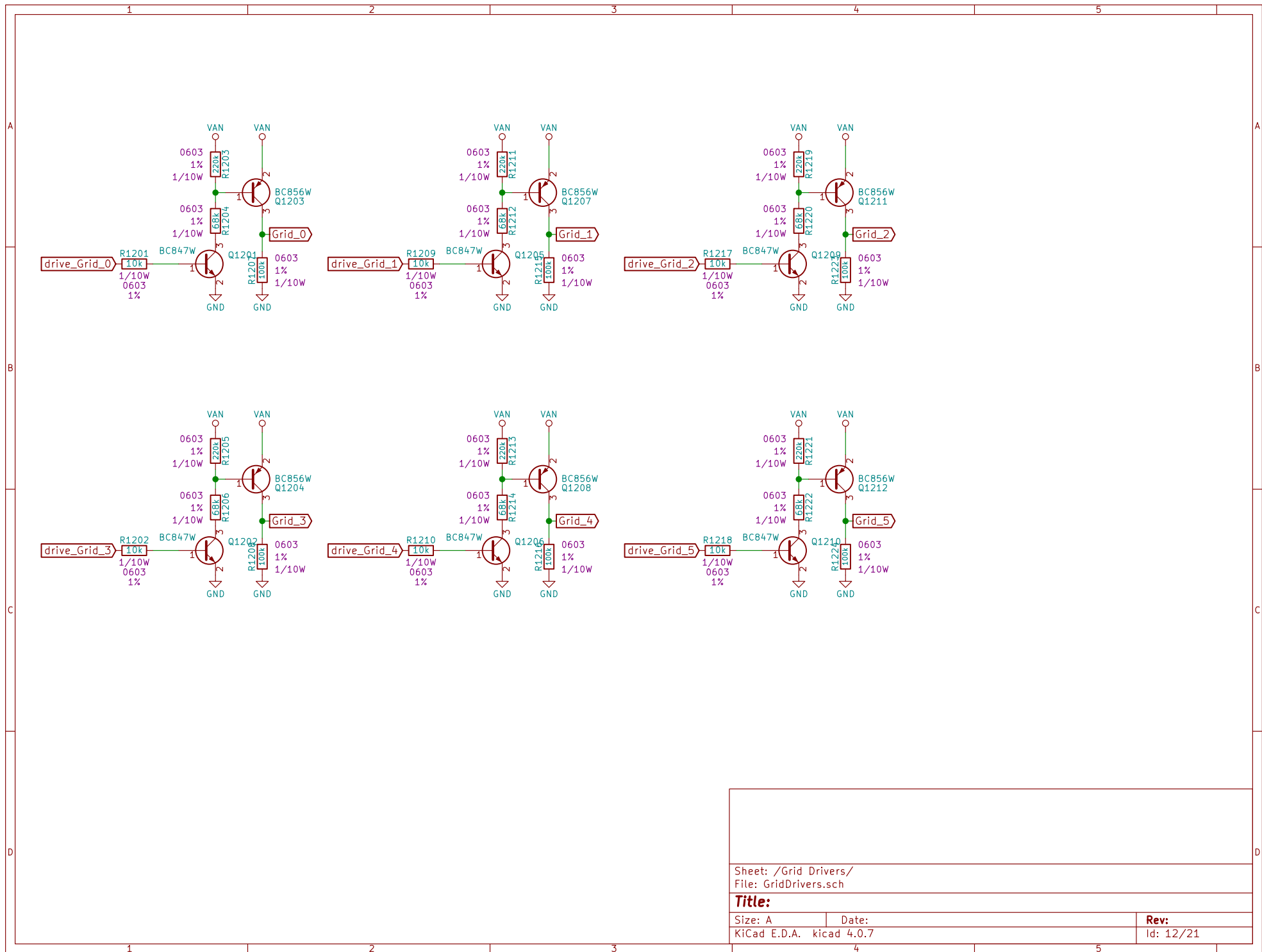
Size: A Date:  
KiCad E.D.A. kicad 4.0.7

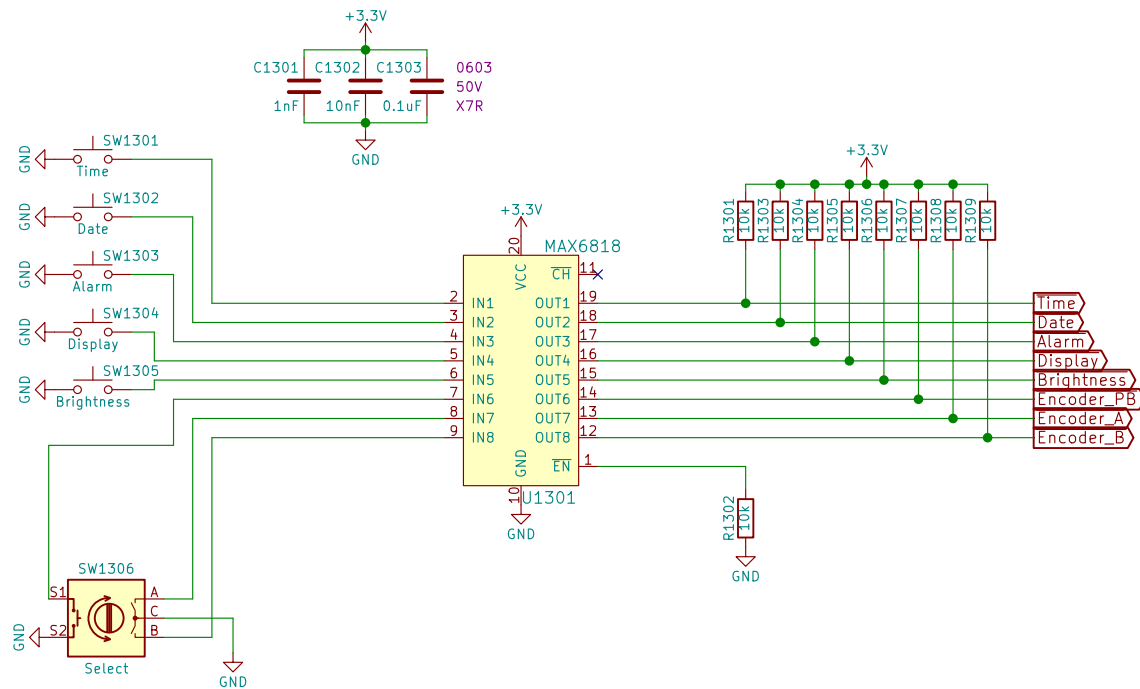
Rev:  
Id: 9/21



Sheet: /Status Indicators/ File: Status_Indicators.sch		
<b>Title:</b>		
Size: A	Date:	Rev:
KiCad E.D.A. kicad 4.0.7		Id: 10/21





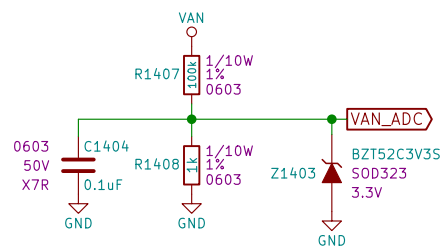
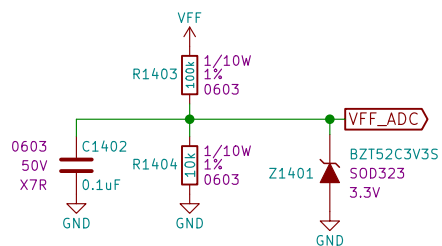
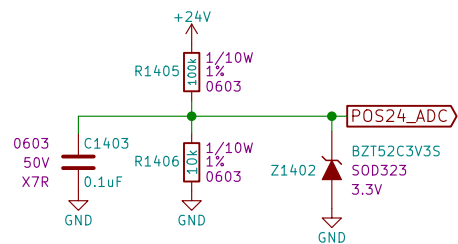
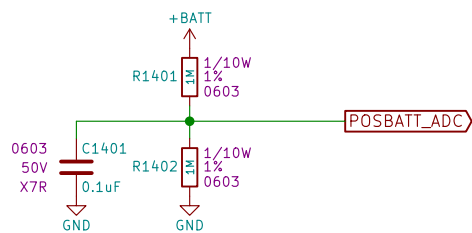


Sheet: /Pushbuttons/  
File: Pushbuttons.sch

**Title:**

Size: A Date:  
KiCad E.D.A. kicad 4.0.7

**Rev:**  
Id: 13/21



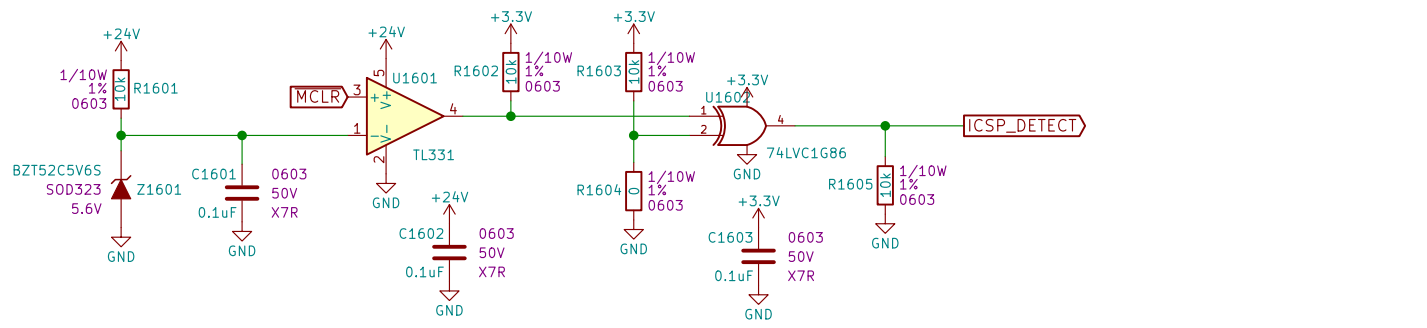
Sheet: /Analog Inputs/  
File: AnalogInputs.sch

**Title:**

Size: A Date:  
KiCad E.D.A. kicad 4.0.7

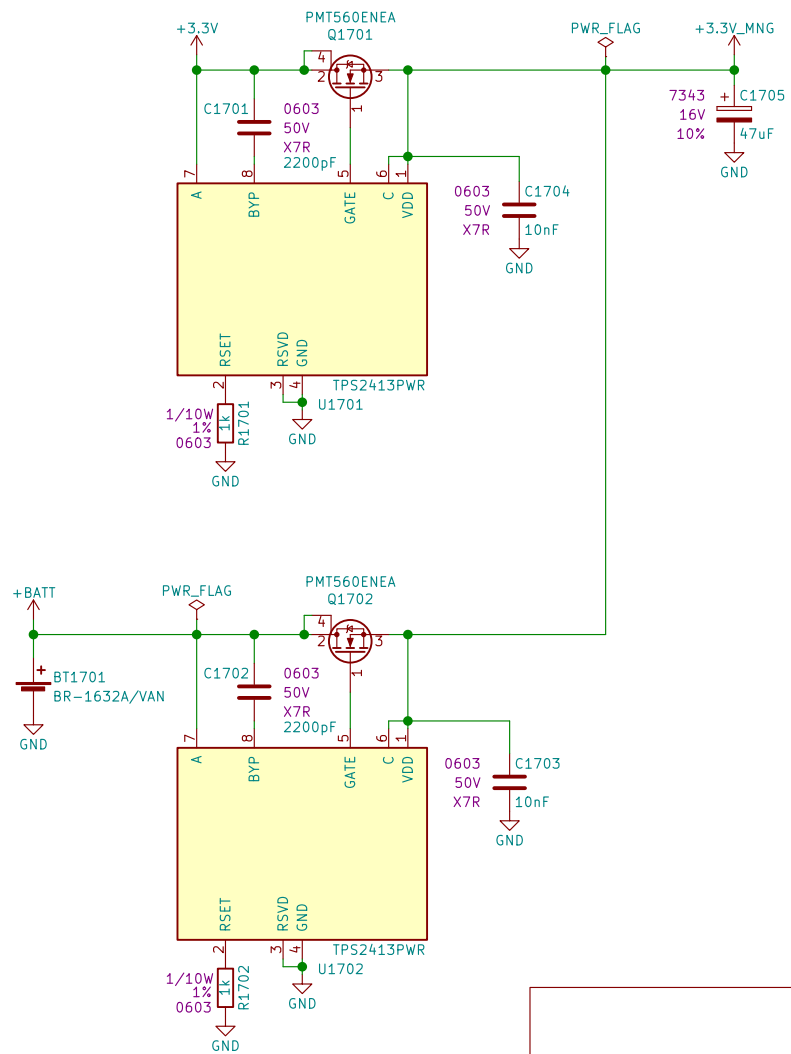
Rev:  
Id: 14/21





Sheet: /Programming/ File: Programming.sch		
Title:		
Size: A	Date:	Rev:
KiCad E.D.A. kicad 4.0.7		Id: 16/21



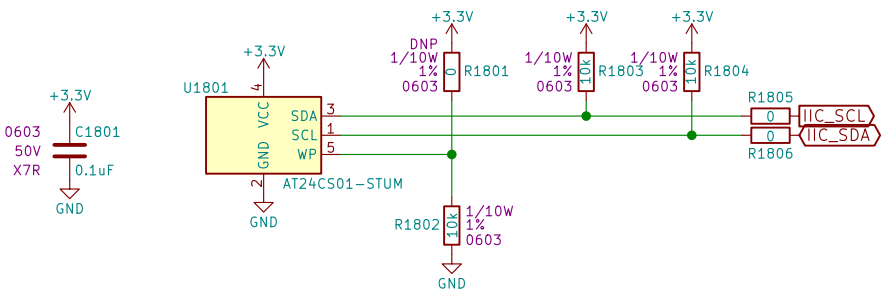


Sheet: /Backup Supply/  
File: BackupSupply.sch

**Title:**

Size: A Date:  
KiCad E.D.A. kicad 4.0.7

**Rev:**  
Id: 17/21



Sheet: /Serial Number/ File: SerialNumber.sch		
Title:		
Size: A	Date:	Rev:
KiCad E.D.A. kicad 4.0.7		Id: 18/21

