

Drew Maatman

Sheet: /Microcontroller/
File: Microcontroller.sch

Title: VFD Clock

Size: A Date: 2019-04-11
KiCad E.D.A. kicad (5.1.0)-1

Rev: A
Id: 2/21

The schematic illustrates a power supply system for a VFD clock. It features two main stages:

- Main Power Stage:** A buck converter using the LT3757 IC. The input is derived from a +12V source through three parallel capacitors (C?, 1206 25V X7R, 10uF). The output is regulated at +60V_VAN by the feedback network (FBX) and monitored by the AD8217 current sense amplifier. Key components include an IRL214TRPBF MOSFET, an MBR0570 diode, and various passive components like resistors (R?) and capacitors (C?).
- Logic Level Conversion:** An LT6700-1-S6 differential line driver converts the +60V_VAN signal to a +3.3V logic level (POS60_VAN_PG00D) for interfacing with the VFD clock.

The design is specified for a maximum output current of 50mA.

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Sheet: //+60V VAN Power Supply/ File: POS60_VAN_Power_Supply.sch	
Title: VFD Clock	
Size: A	Date: 2019-04-11
KiCad E.D.A. kicad (5.1.0)-1	Rev: A Id: 3/21

The schematic illustrates a power supply system for a VFD clock. It features two main stages:

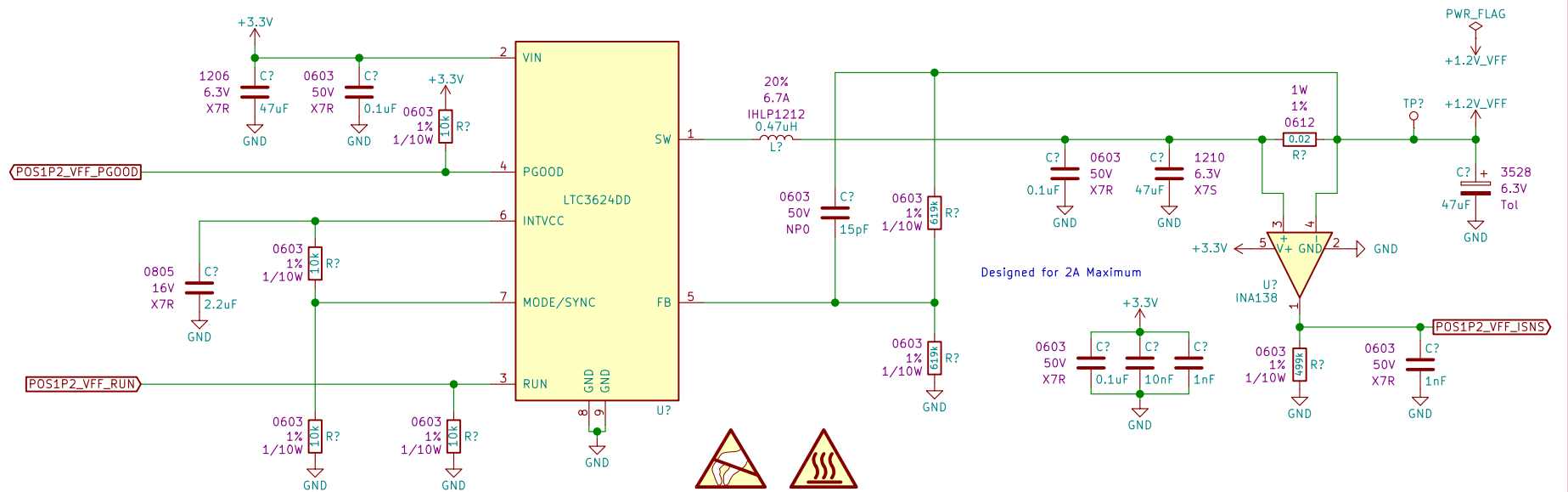
- Main Power Stage:** A buck converter using the LT3757 IC. The input is derived from a +12V source through three parallel capacitors (C?, 1206 25V X7R, 10uF). The output is regulated to +60V_VAN by the feedback network (FBX) and monitored by the AD8217 current sense amplifier. Key components include an IRL214TRPBF MOSFET, an MBR0570 diode, and various passive components like resistors (R?) and capacitors (C?).
- Logic Level Conversion:** An LT6700-1-S6 differential line driver converts the +60V_VAN signal to a +3.3V logic level (POS60_VAN_PG00D) for the microcontroller.

The design is specified for a maximum output current of 50mA.

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Sheet: //+60V VAN Power Supply/ File: POS60_VAN_Power_Supply.sch	
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+1.2V VFF Power Supply



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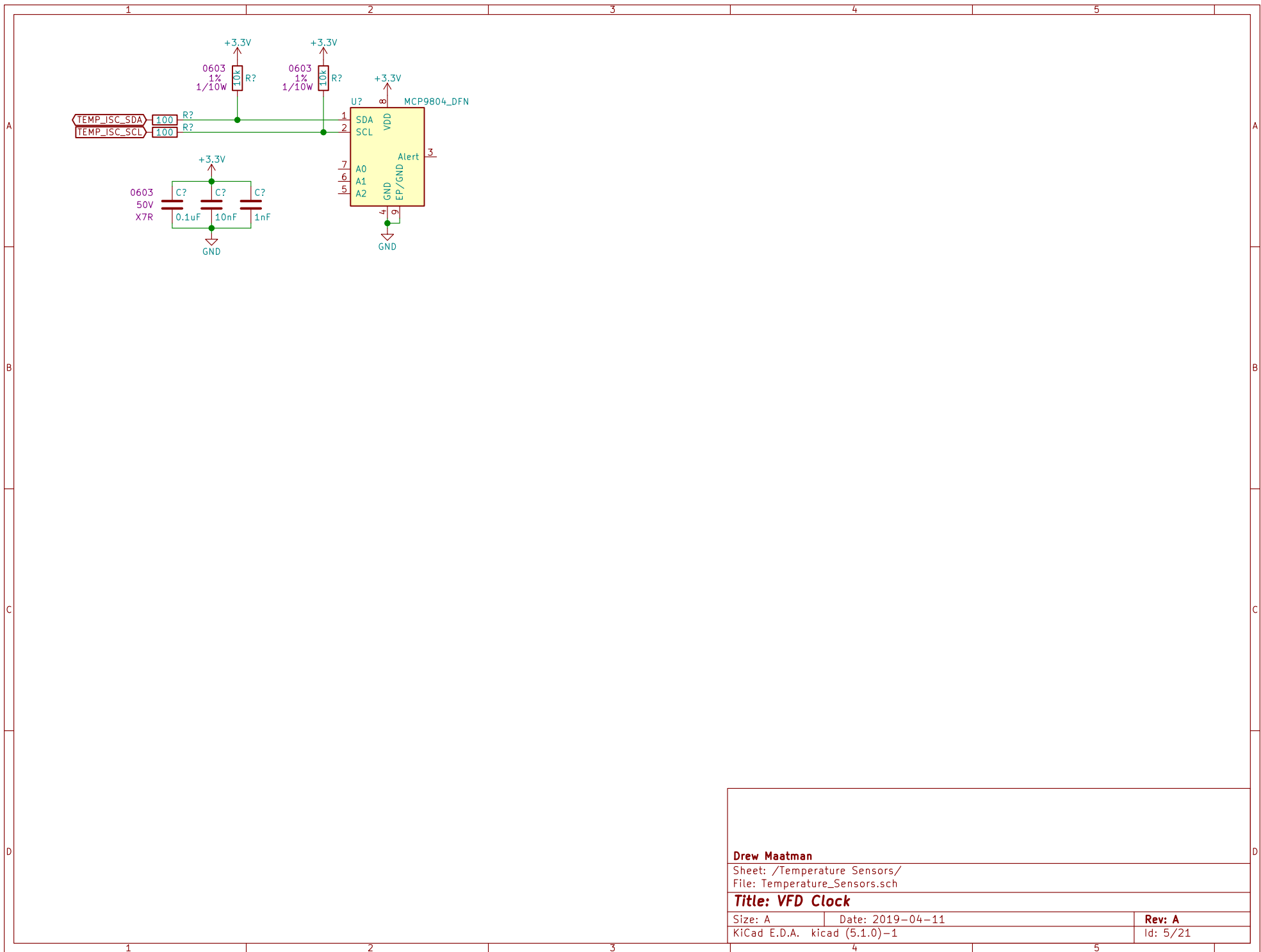
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File: POS1P2_VFF_Power_Supply.sch

Title: VFD Clock

Size: A Date: 2019-04-11
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1.996 V/A



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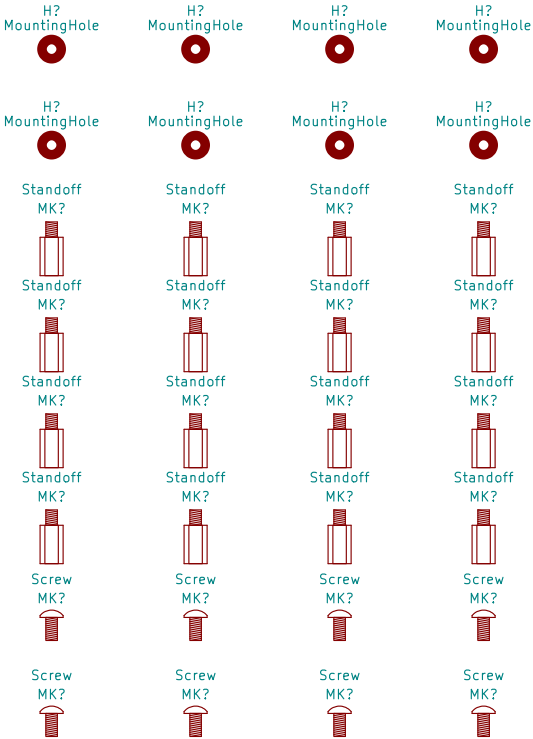
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File: Temperature_Sensors.sch

Title: VFD Clock

Size: A Date: 2019-04-11
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Mechanical Components



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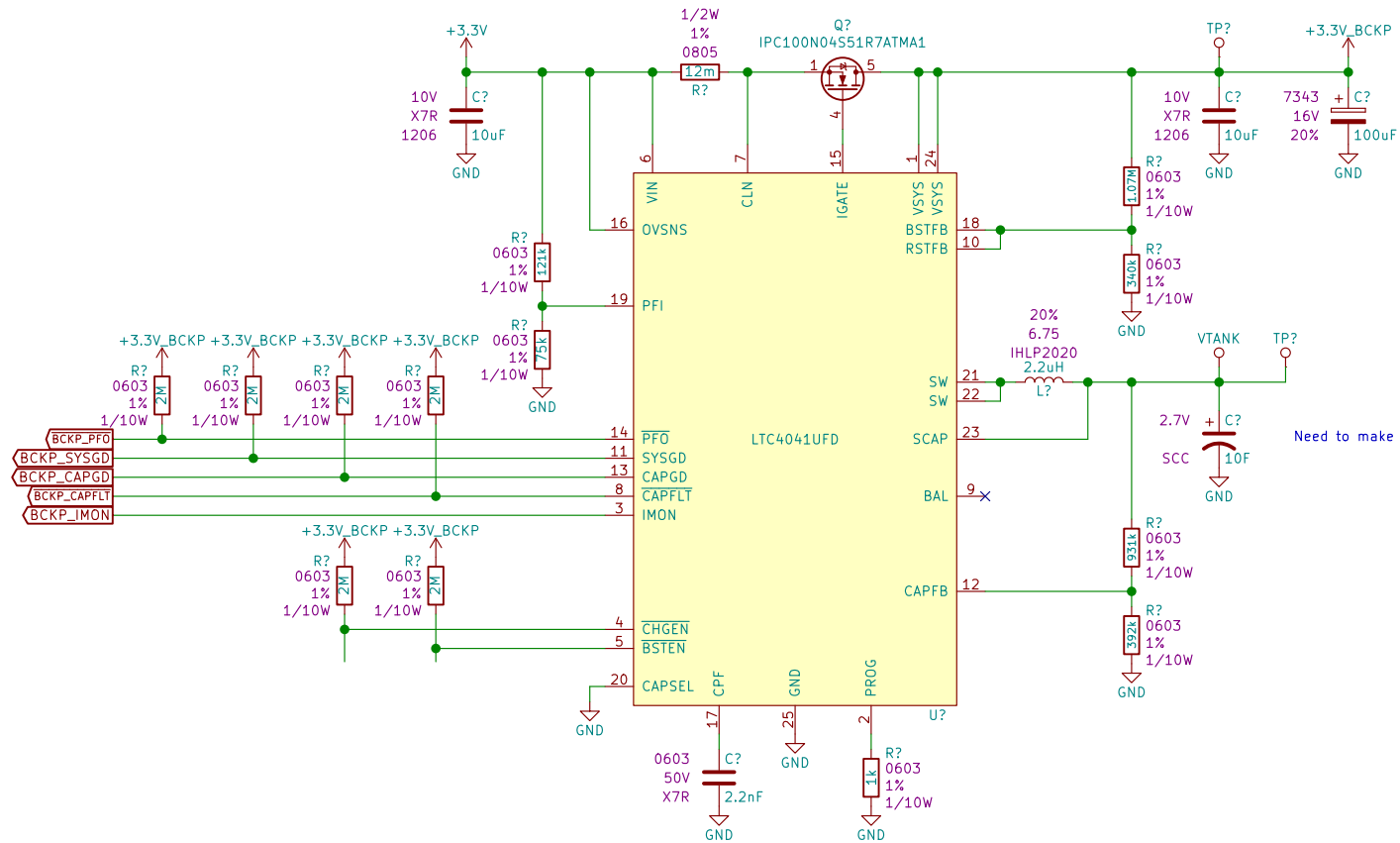
Sheet: /Mechanical/
File: Mechanical.sch

Title: VFD Clock

Size: A Date: 2019-04-11
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Supercap Backup Circuit



Need to make custom horizontal footprint for SCC cap

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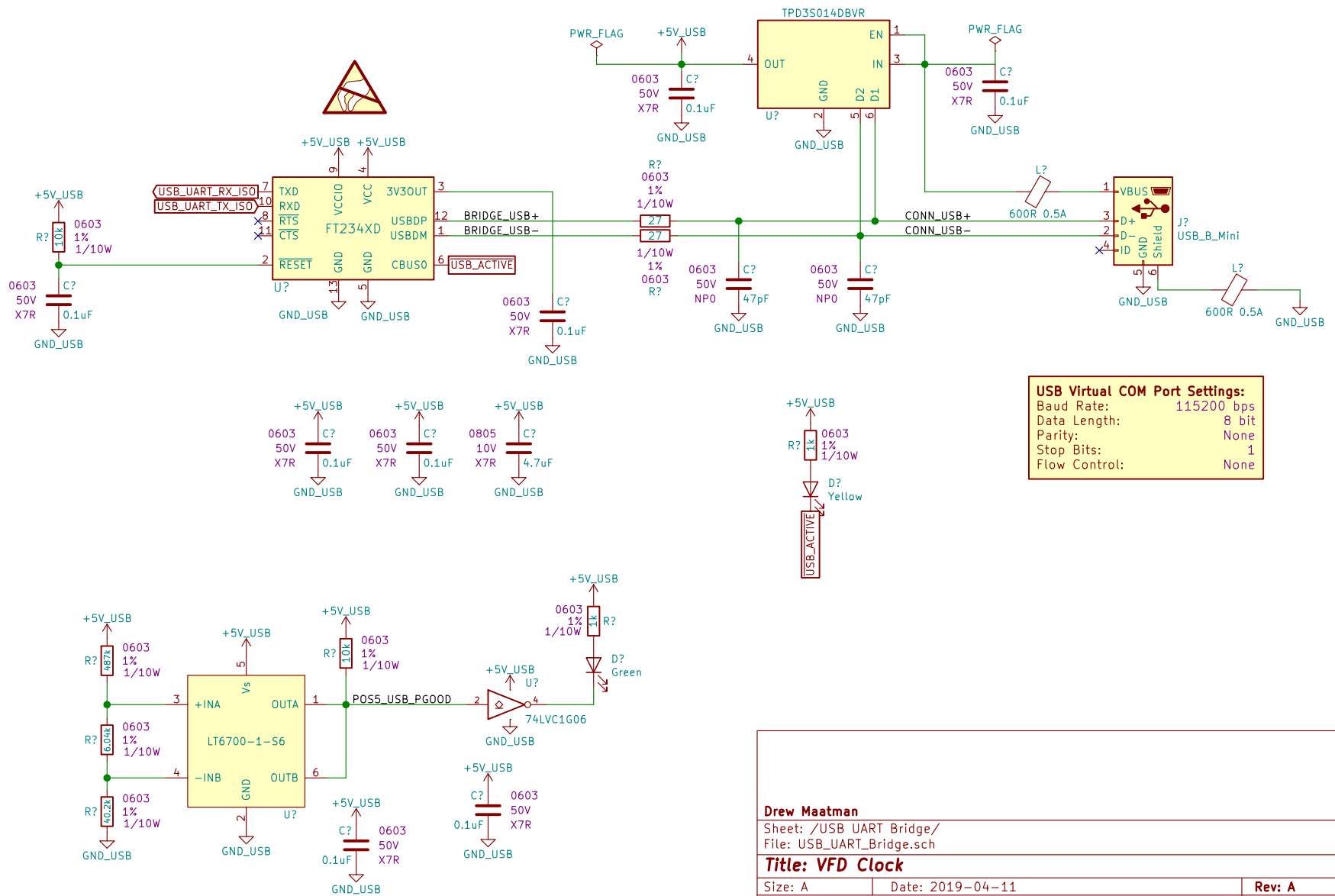
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File: POS3P3_BCKP_Supply.sch

Title: VFD Clock

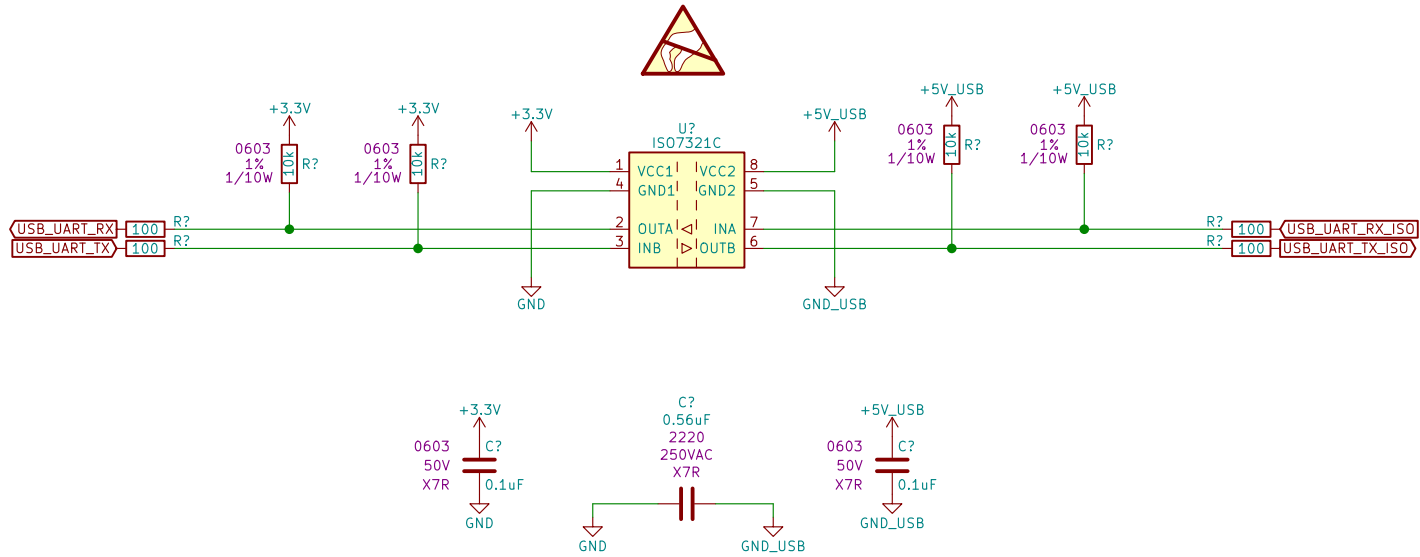
Size: A Date: 2019-04-11
KiCad E.D.A. kicad (5.1.0)-1

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11. USB UART Bridge



10. USB UART Digital Isolation



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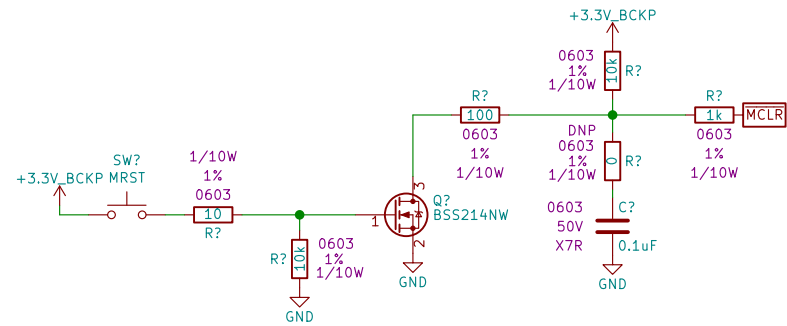
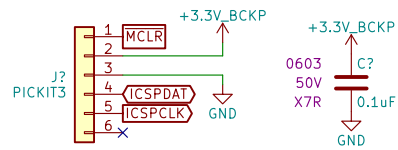
Sheet: /USB UART Isolation/
File: USB_UART_Isolation.sch

Title: VFD Clock

Size: A Date: 2019-04-11
KiCad E.D.A. kicad (5.1.0)-1

Rev: A
Id: 9/21

Microcontroller Programming



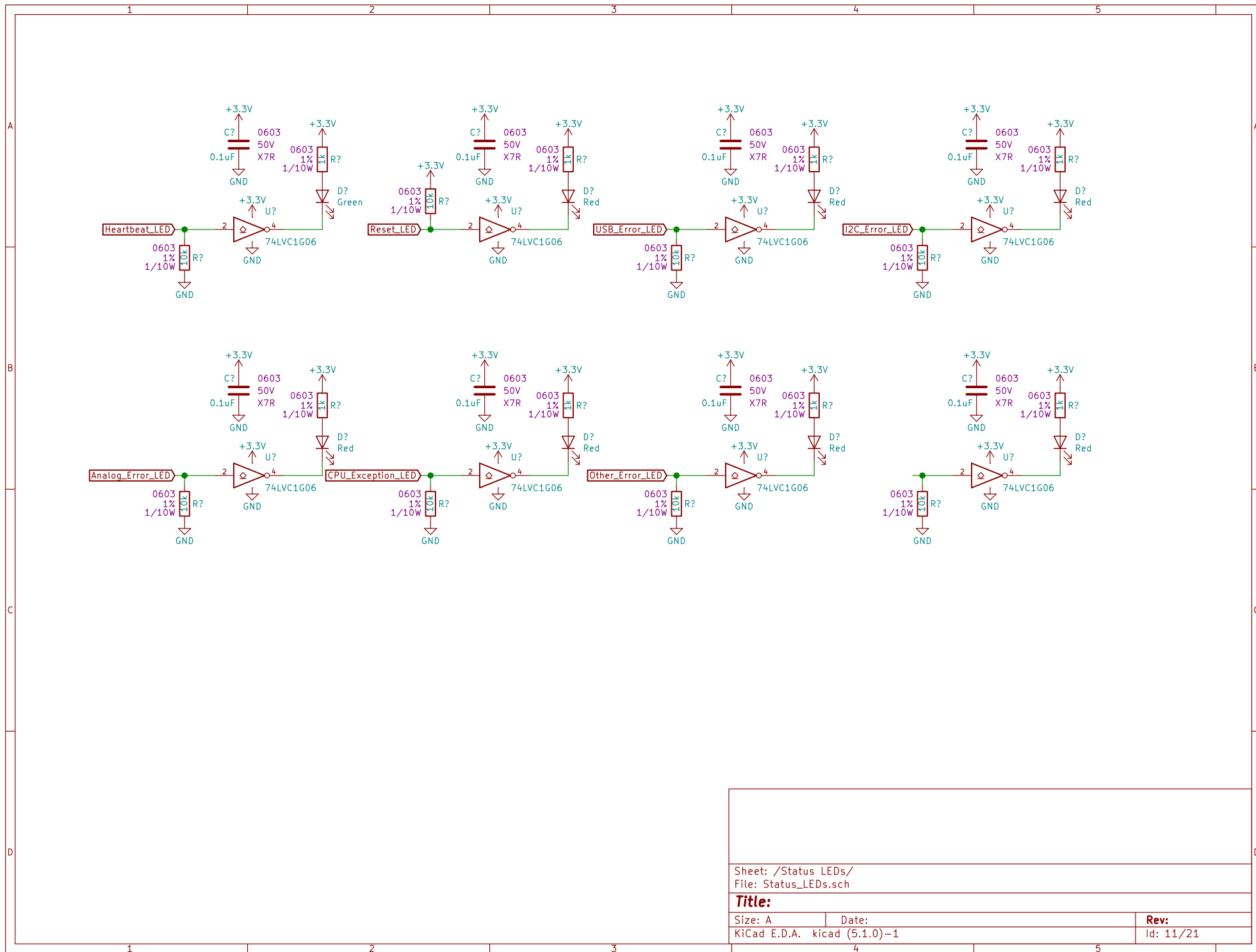
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Sheet: /Microcontroller Programming/
File: Microcontroller_Programming.sch

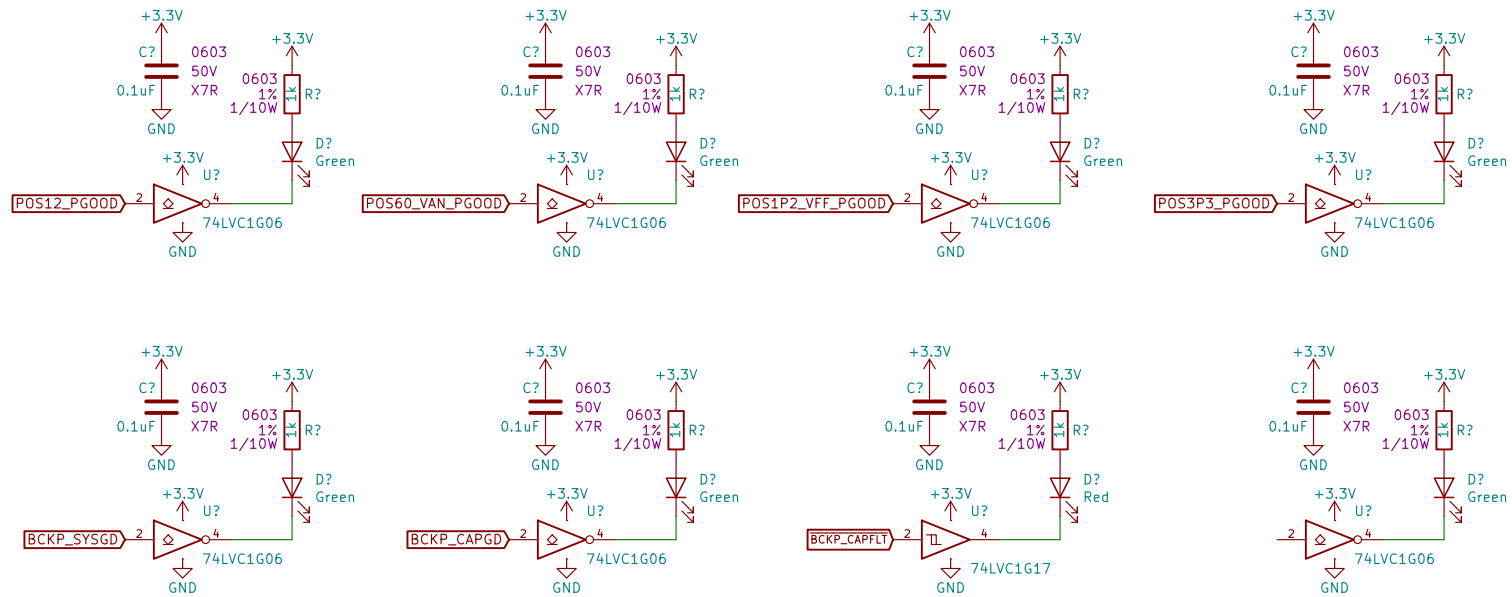
Title: VFD Clock

Size: A	Date: 2019-04-11
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Sheet: /Status LEDs/ File: Status_LEDs.sch		
Title:		
Size: A	Date:	Rev:
KiCad E.D.A. kicad (5.1.0)-1		Id: 11/21



Sheet: /PGOOD LEDs/
File: PGOOD_LEDs.sch

Title:

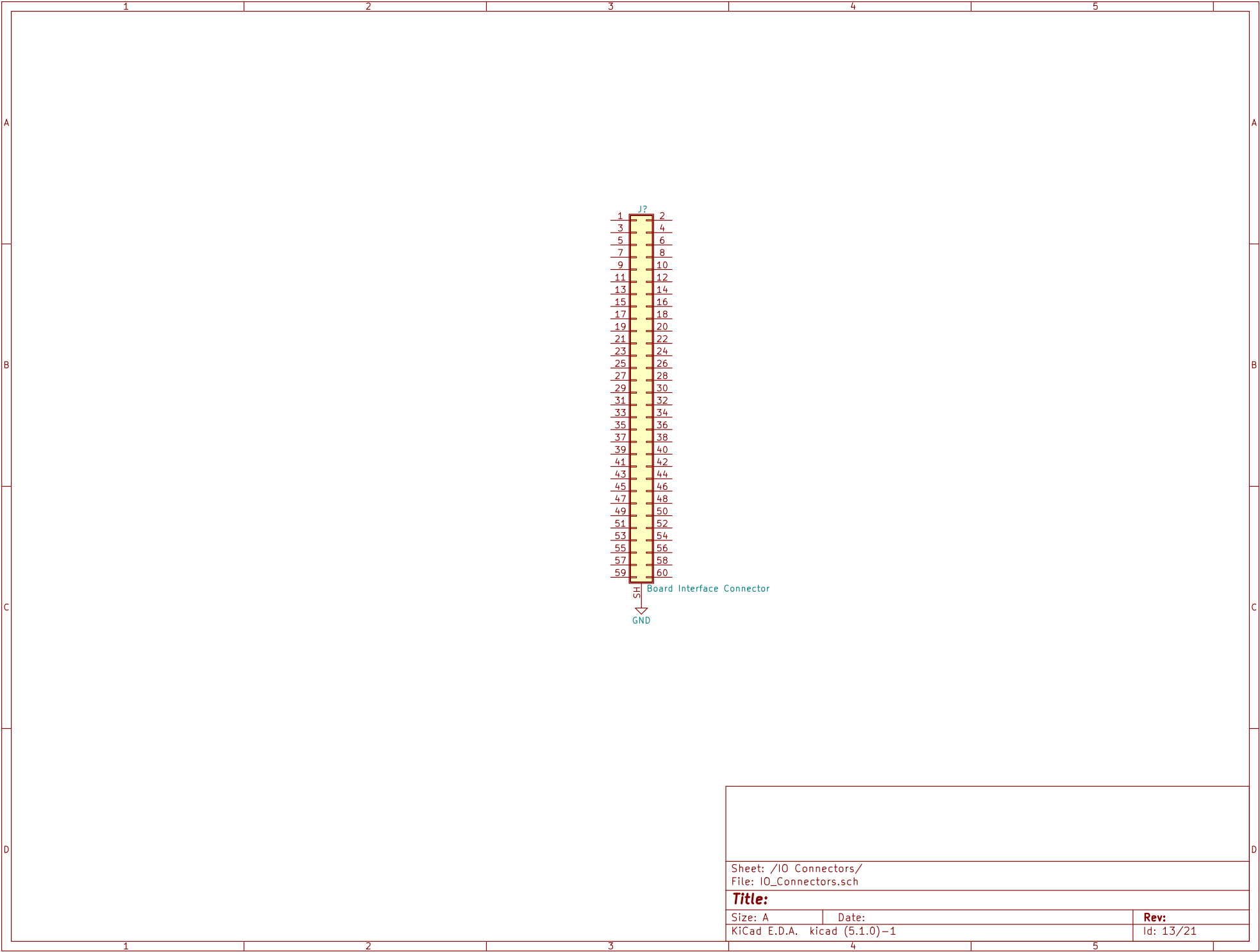
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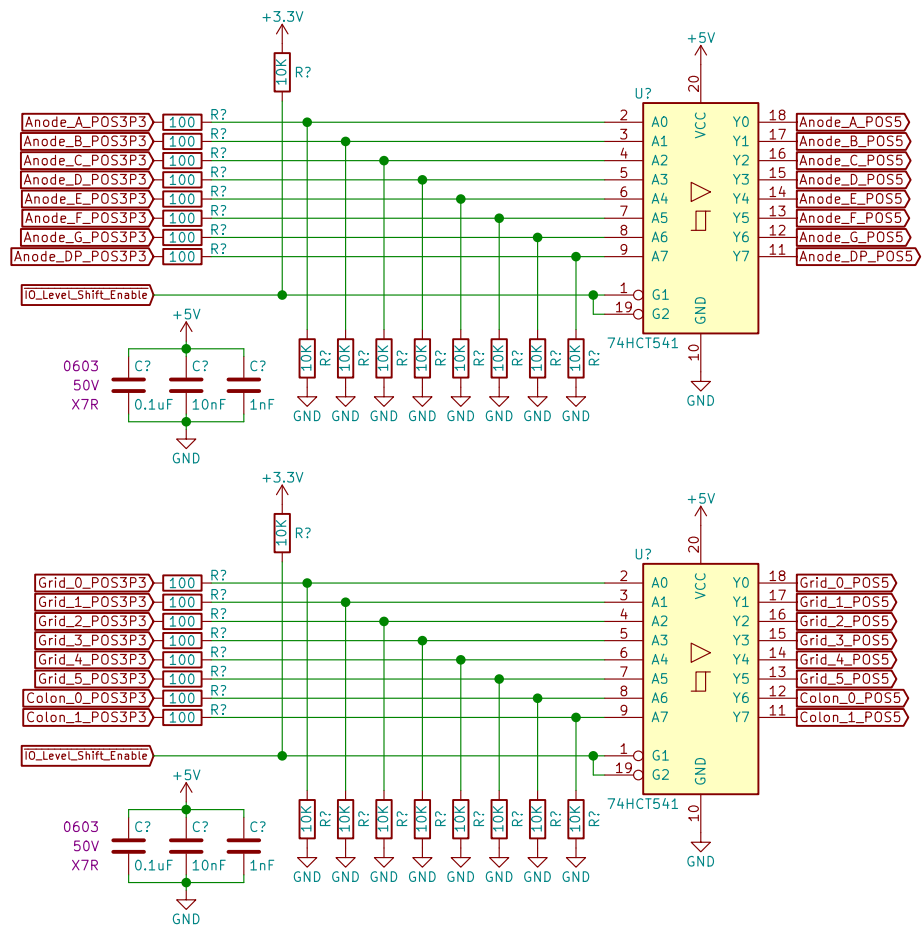
Rev:

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Sheet: /IO Connectors/ File: IO_Connectors.sch		
Title:		
Size: A	Date:	Rev:
KiCad E.D.A. kicad (5.1.0)-1		Id: 13/21

I/O Buffers



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Sheet: /IO Buffers 1/
File: IO_Buffers_1.sch

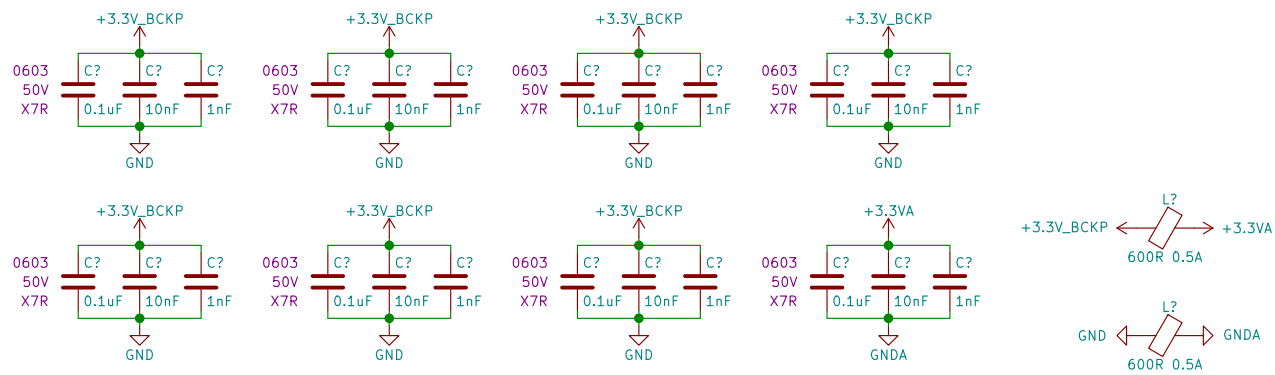
Title: Electronic Display Logic Board

Size: A Date: 2018-12-15

KiCad E.D.A. kicad (5.1.0)-1

Rev: A

Id: 14/21



Sheet: /Microcontroller Bypass/
File: Microcontroller_Bypass.sch

Title:

Size: A

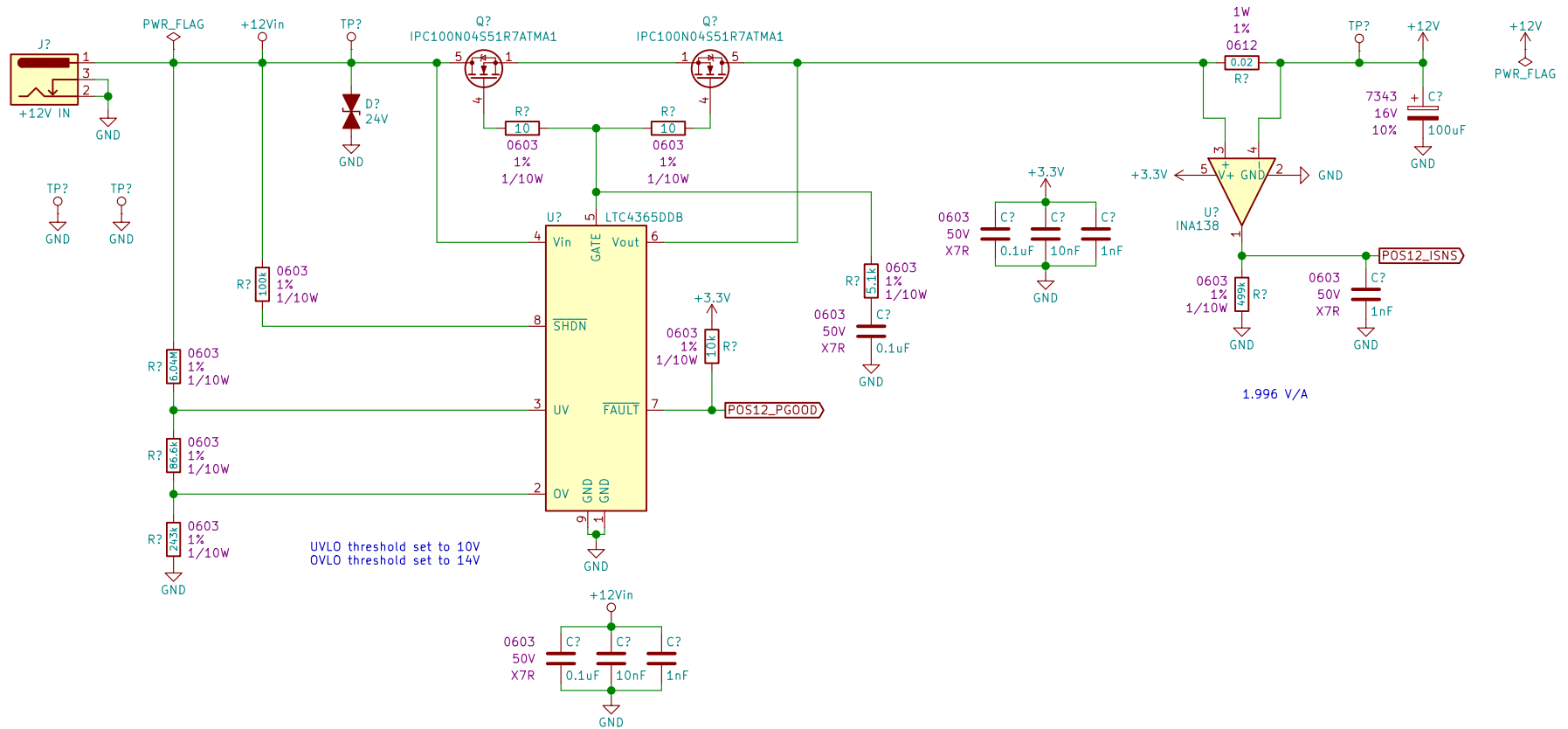
Date:

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Rev:

Id: 15/21

02. +12V Input

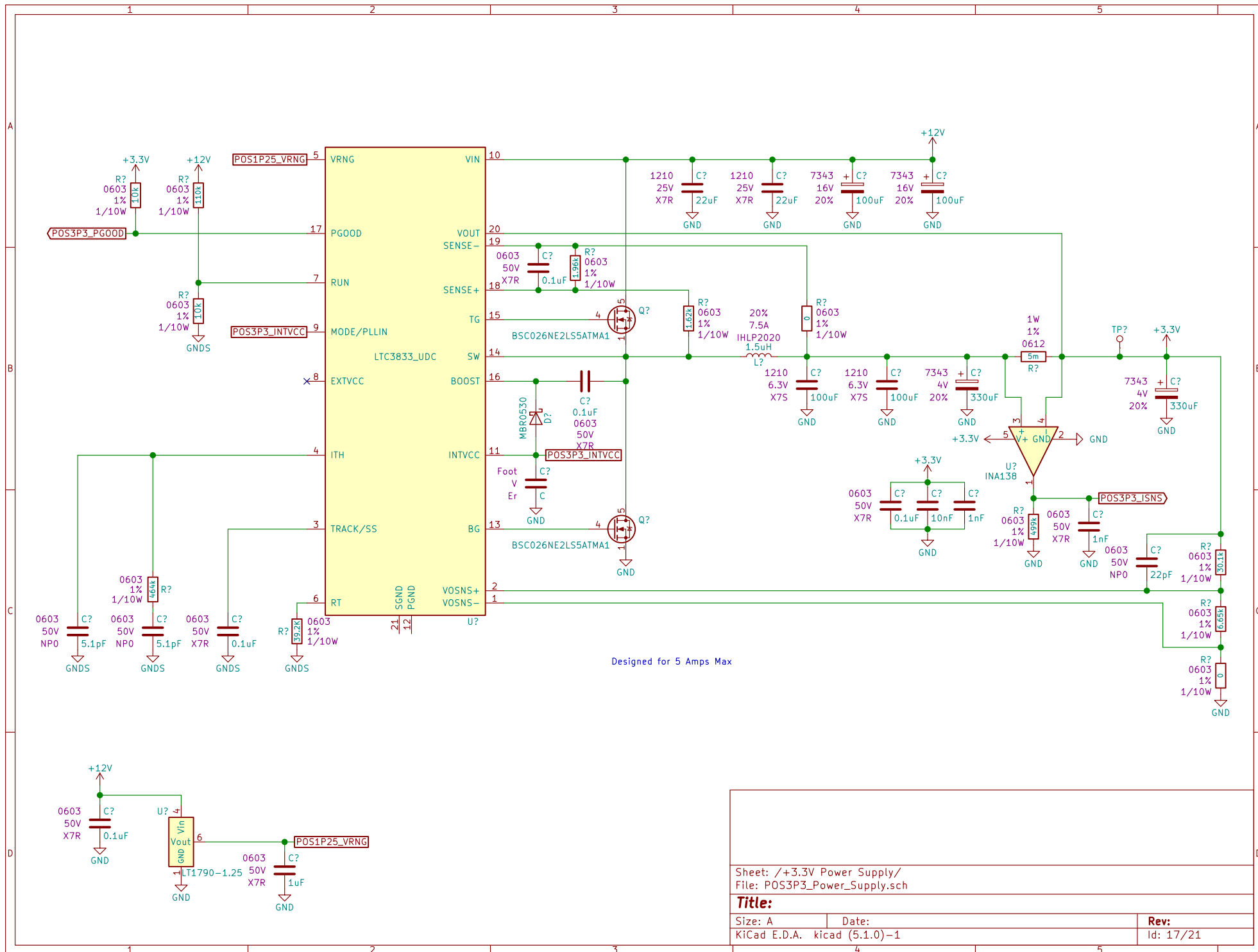


Sheet: /+12V Input/
File: POS12_Input.sch

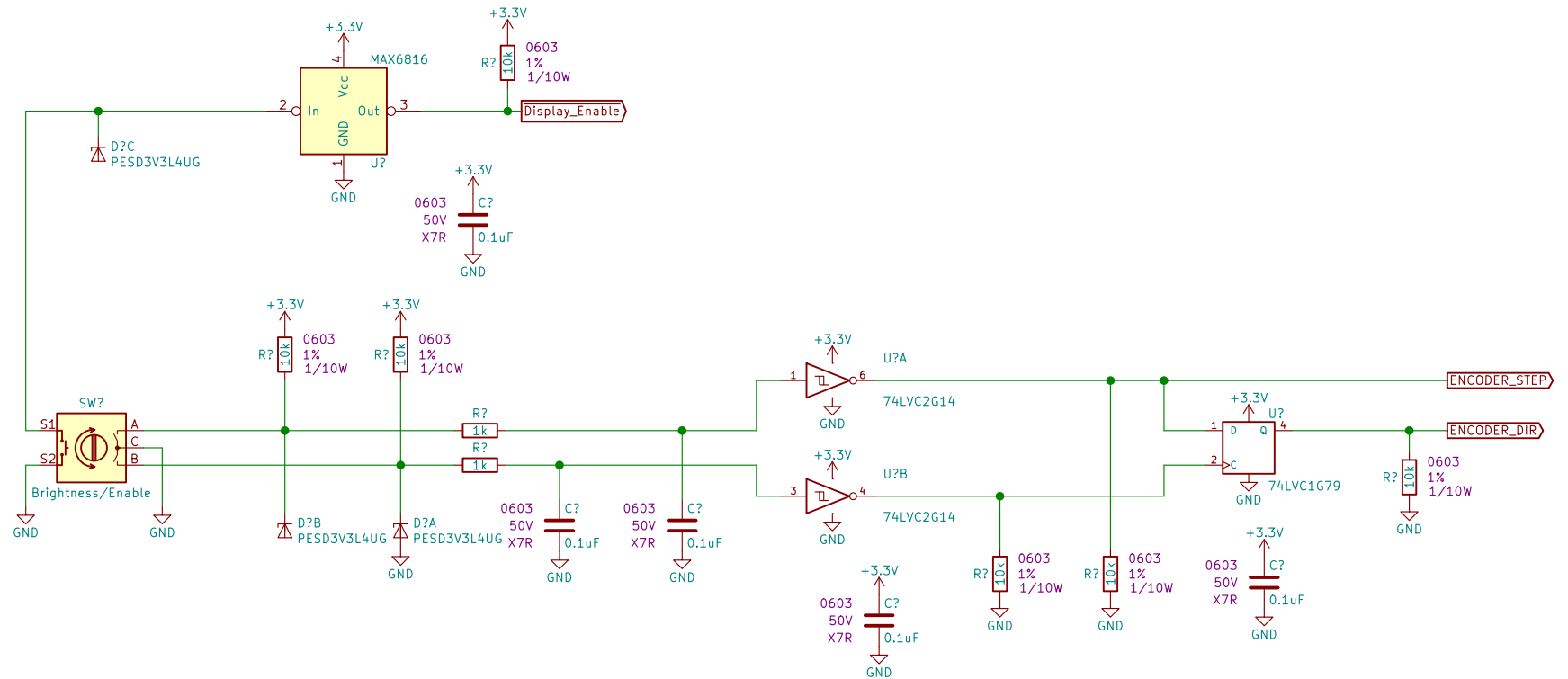
Title: Qi Charger

Size: A Date: 2019-01-03
KiCad E.D.A. kicad (5.1.0)-1

Rev: A
Id: 16/21



Rotary Encoder



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Sheet: /Rotary Encoder/

File: Rotary_Encoder.sch

Title: Electronic Display Logic Board

Size: A Date: 2018-12-15

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Rev: A

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1					2					3					4					5					
A																									A
B																									B
C																									C
D																									D
1					2					3					4					5					

Sheet: /Analog Conditioning/
File: Analog_Conditioning.sch

Title:

Size: ADate:KICad E.D.A. kicad (5.1.0)–1

Rev:Id: 19/21

Sheet: /Analog Conditioning/ File: Analog_Conditioning.sch																								
Title:																								
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A						
B						
C						
D						
	1	2	3	4	5	6

Sheet: /+5V Power Supply/ File: POS5_Power_Supply.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.0)-1		Id: 20/21

