

# VFD Clock Logic Board

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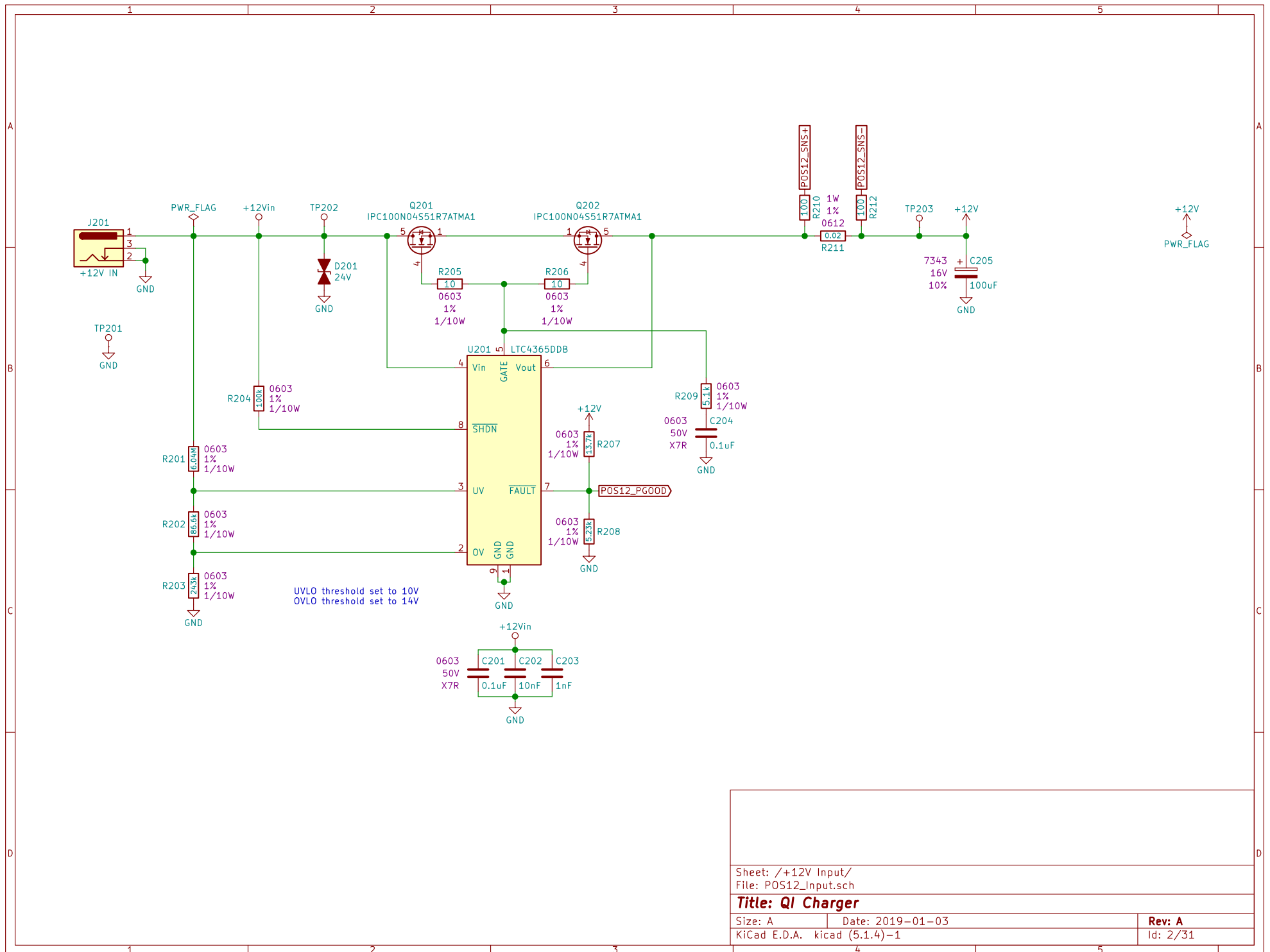
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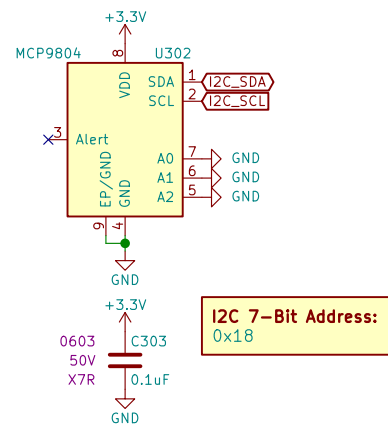
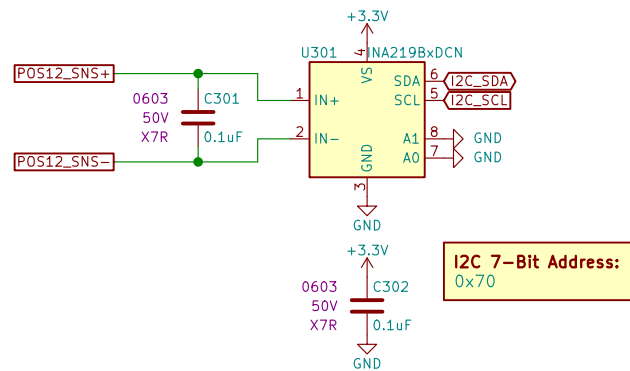
Size: A Date: 2019-04-11

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Sheet: /+12V Telemetry/  
File: POS12\_Telemetry.sch

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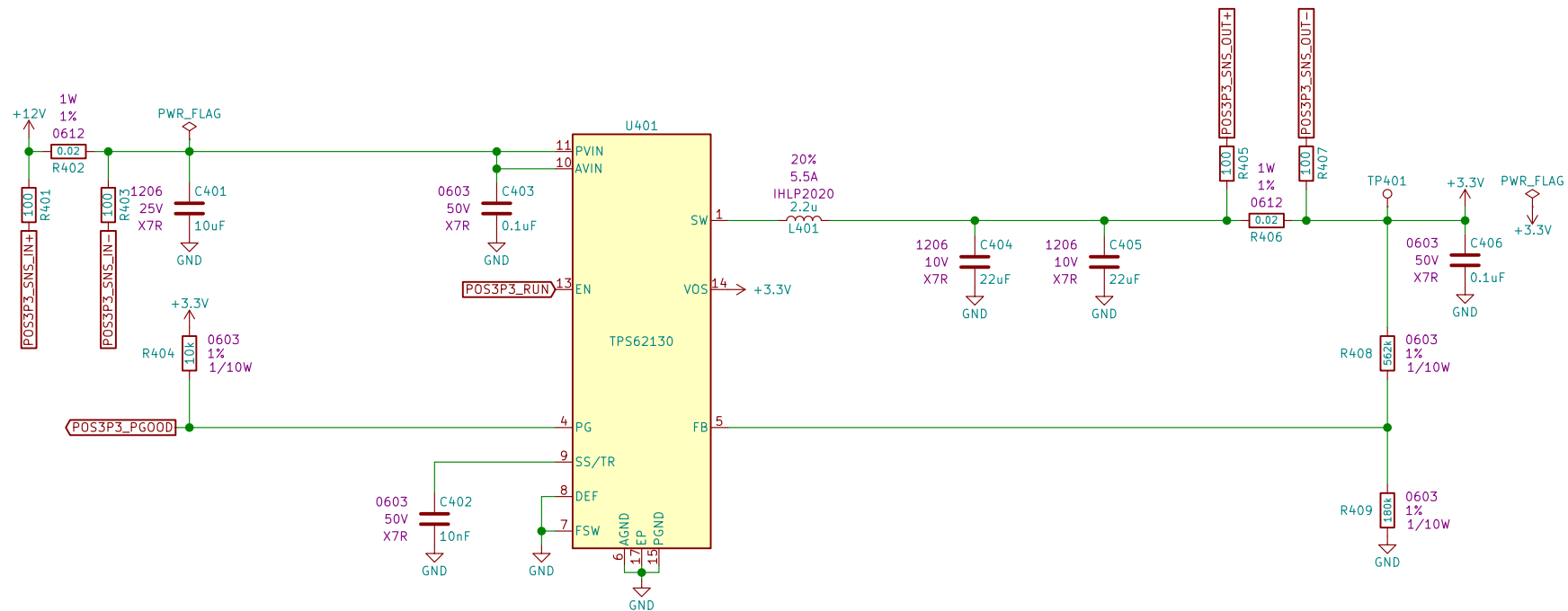
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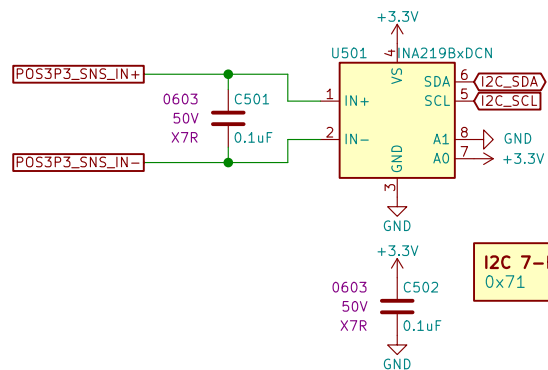


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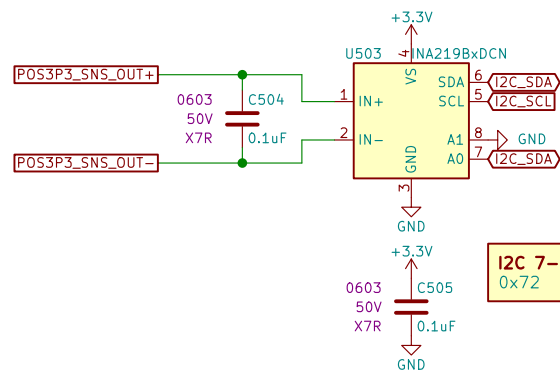
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Size: A Date: 2019-01-03  
KiCad E.D.A. kicad (5.1.4)-1

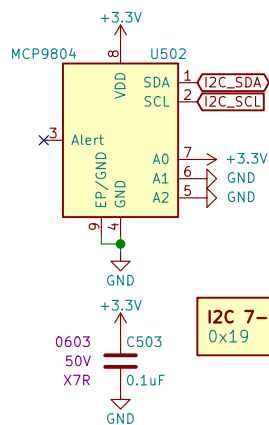
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**I2C 7-Bit Address:**  
0x71



**I2C 7-Bit Address:**  
0x72



**I2C 7-Bit Address:**  
0x19

Sheet: /+3.3V Telemetry/  
File: POS3P3\_Telemetry.sch

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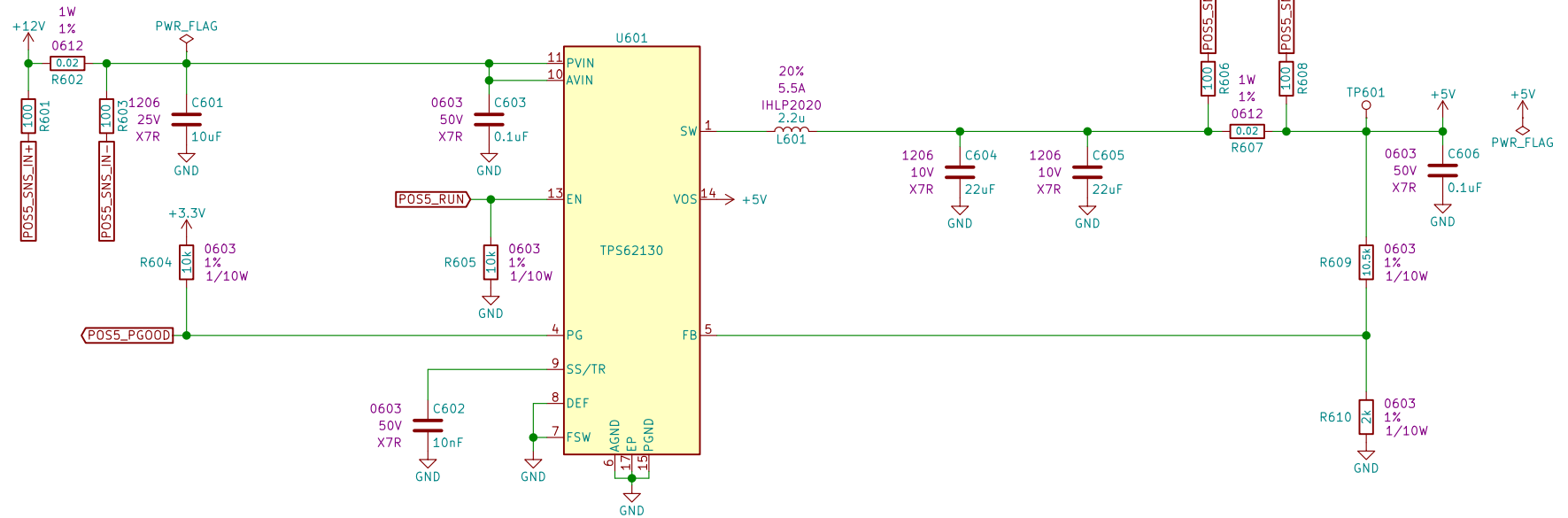
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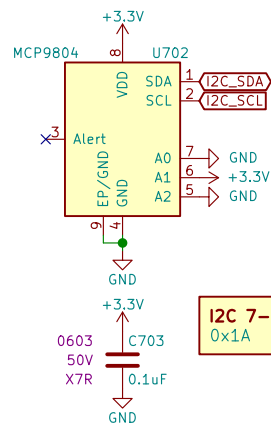
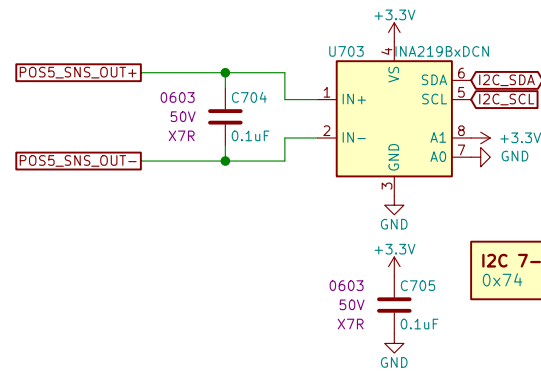
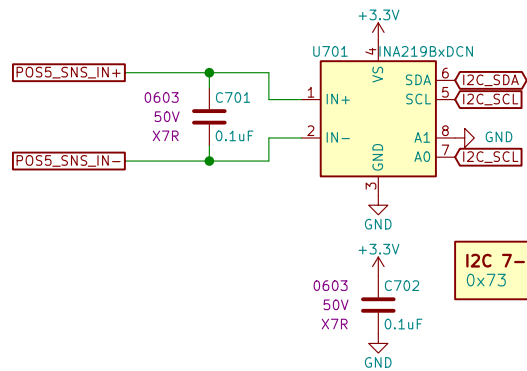


Sheet: /+5V Power Supply/  
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**Title: QI Charger**

Size: A Date: 2019-01-03  
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Sheet: /+5V Telemetry/  
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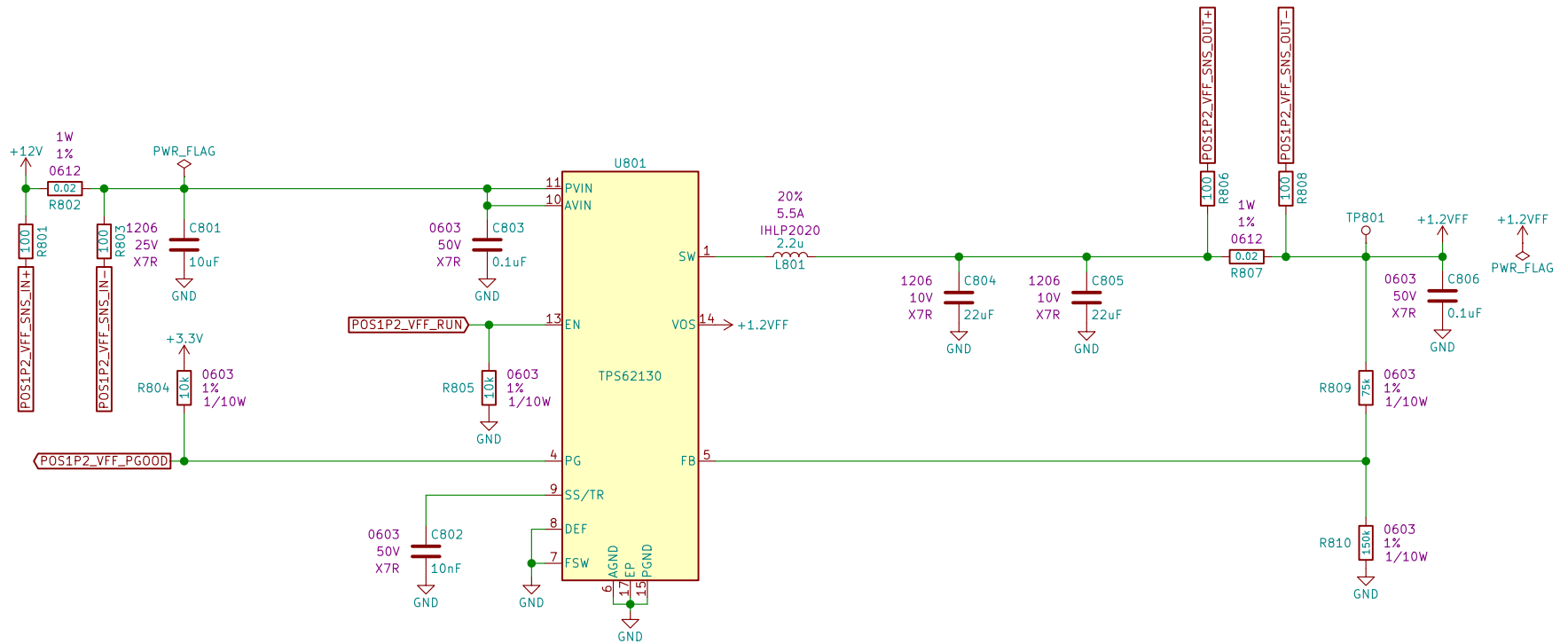
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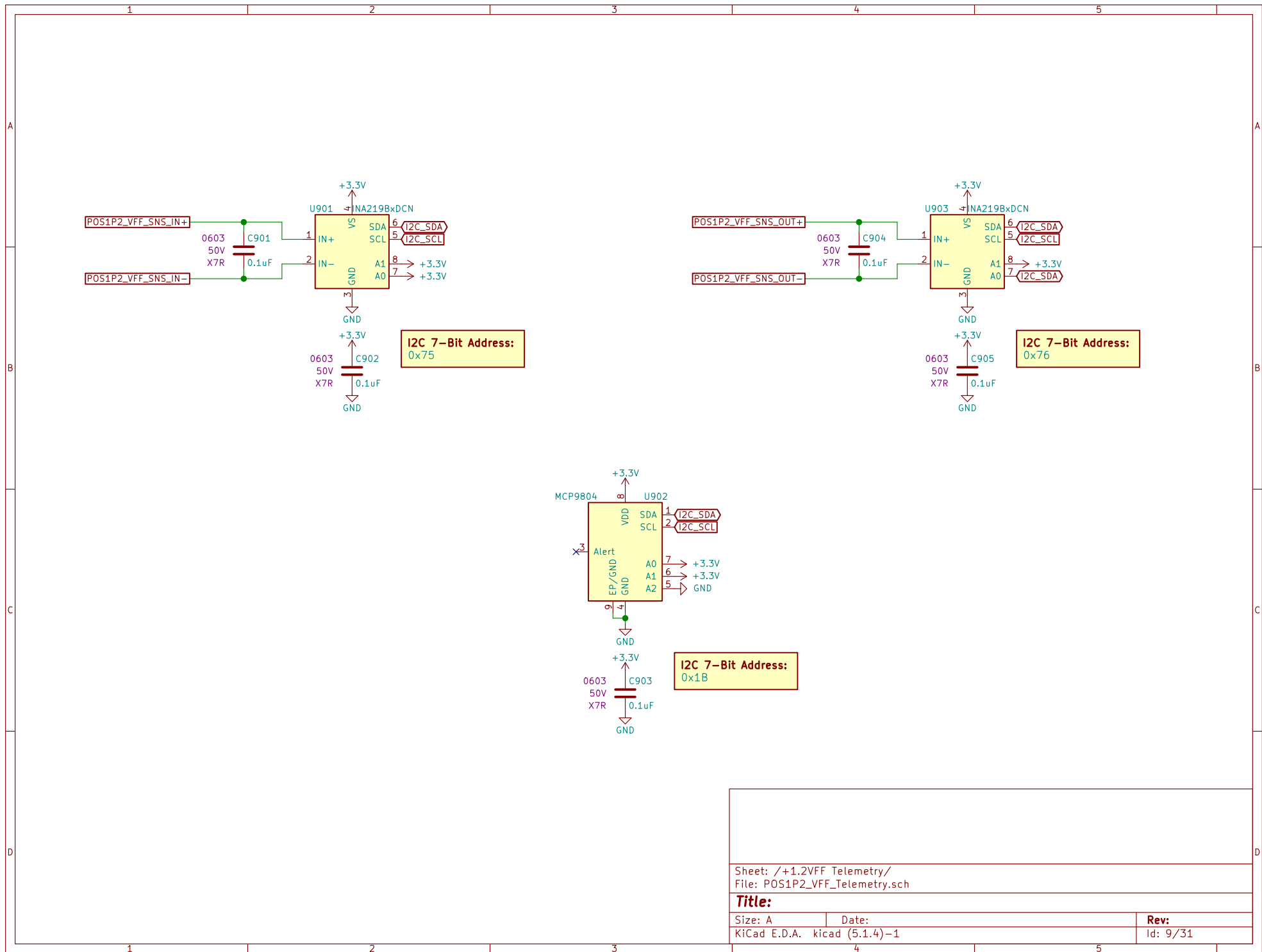
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# **Title: QI Charger**

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Sheet: /+1.2VFF Telemetry/  
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Size: A

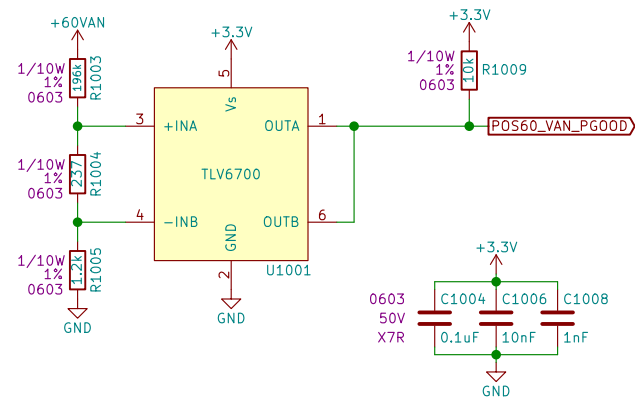
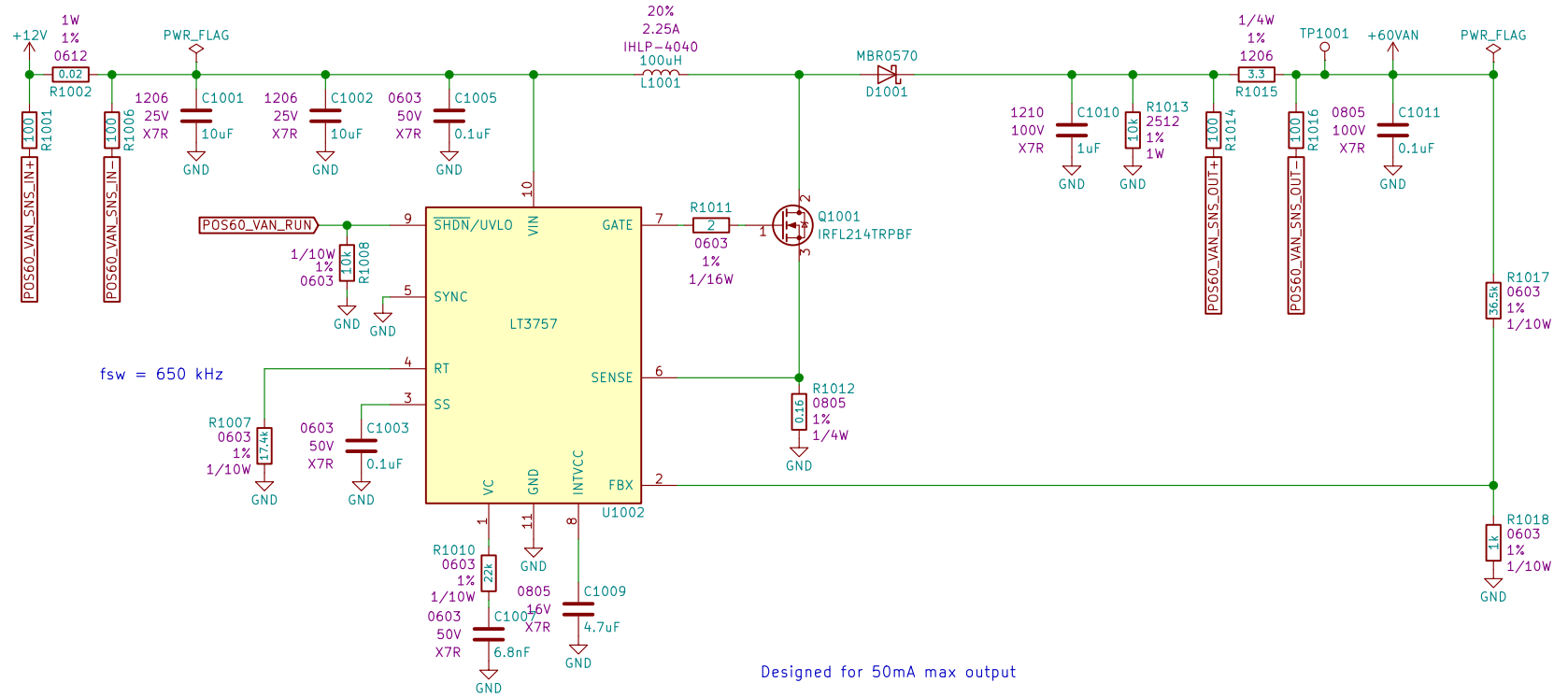
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## Anode/Grid +60V, 50mA Power Supply



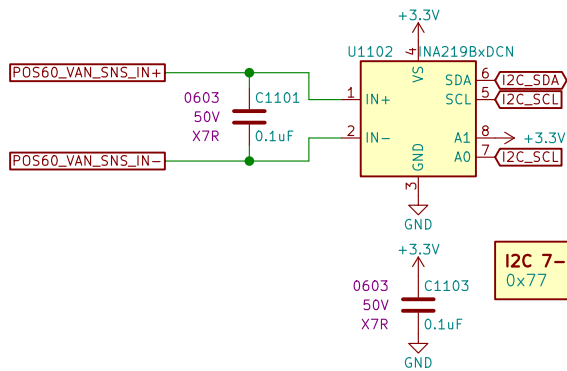
Drew Maatman

Sheet: //+60VAN Power Supply/  
File: POS60\_VAN\_Power\_Supply.sch

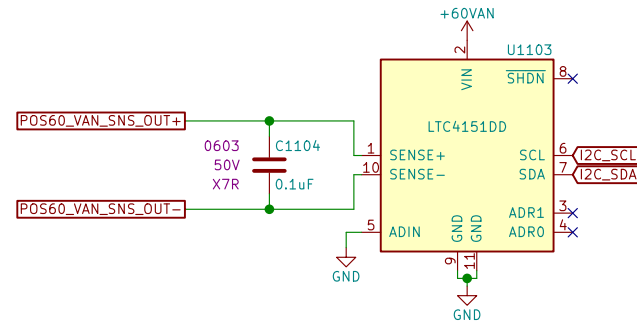
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Size: A Date: 2019-04-11  
KiCad E.D.A. kicad (5.1.4)-1

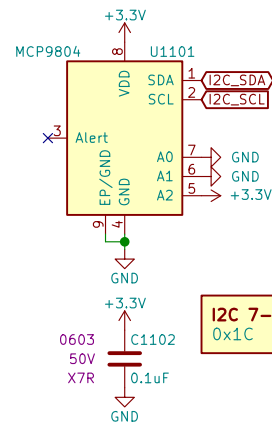
Rev: A  
Id: 10/31



**I2C 7-Bit Address:**  
0x77



**I2C 7-Bit Address:**  
0x6A



**I2C 7-Bit Address:**  
0x1C

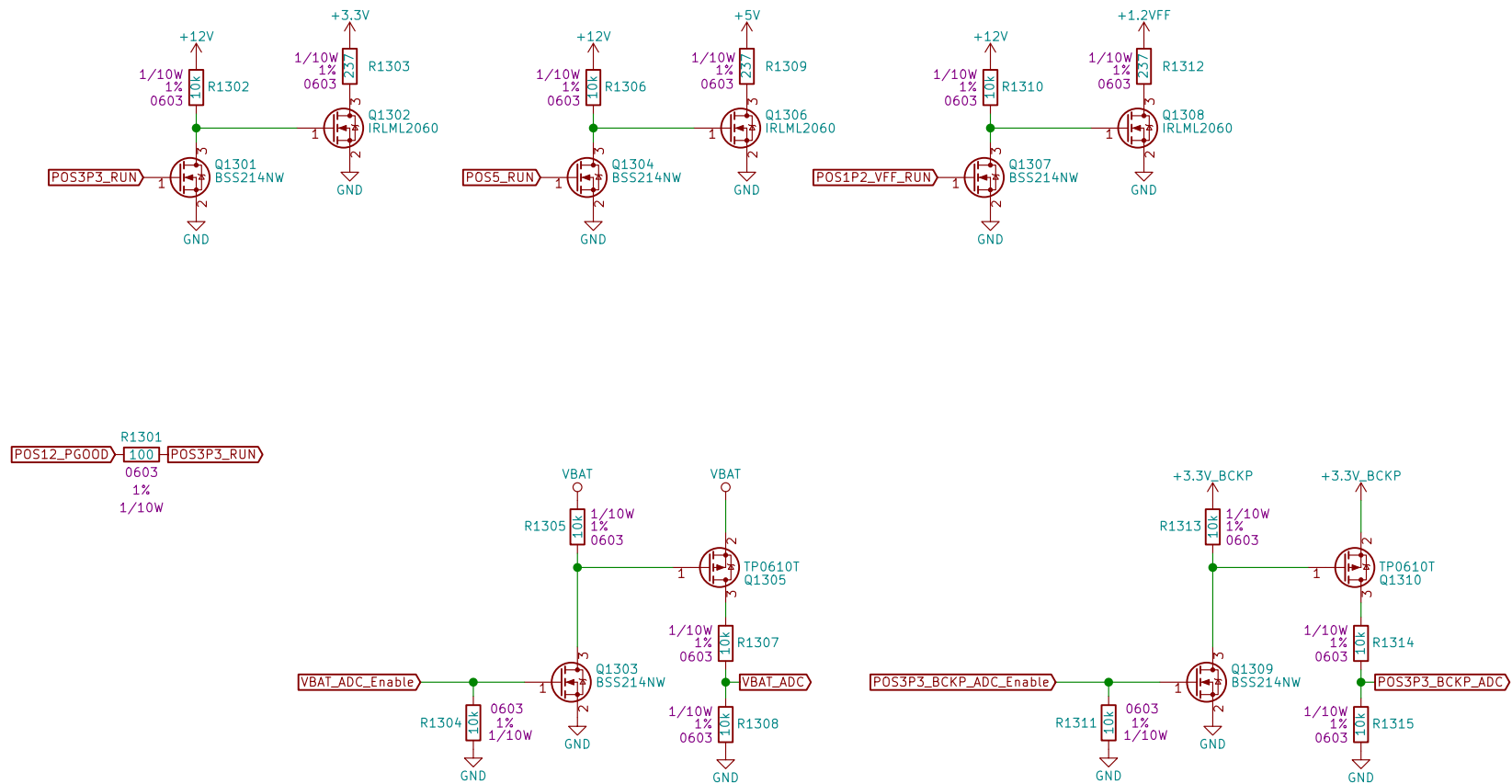
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**Rev:**  
Id: 11/31





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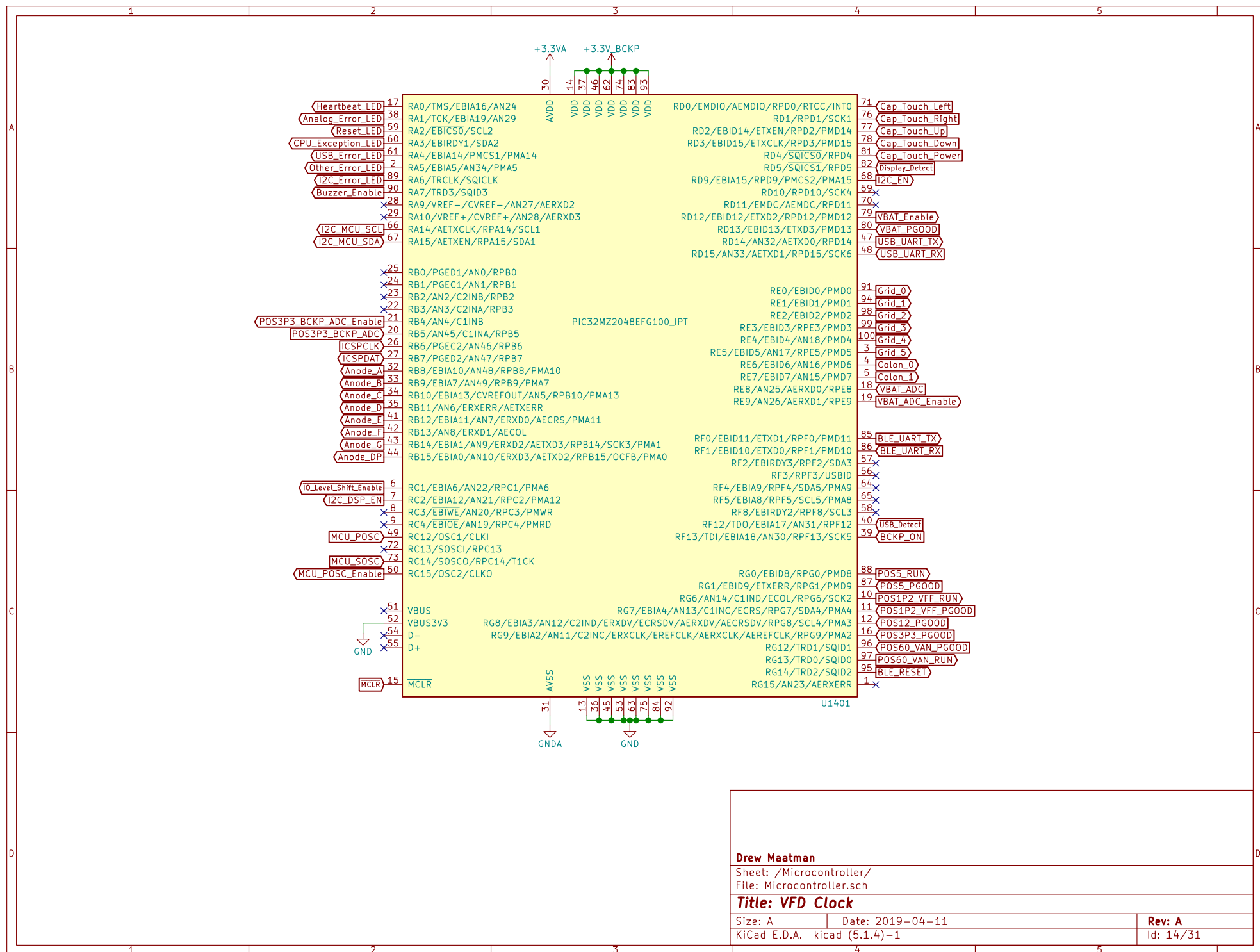
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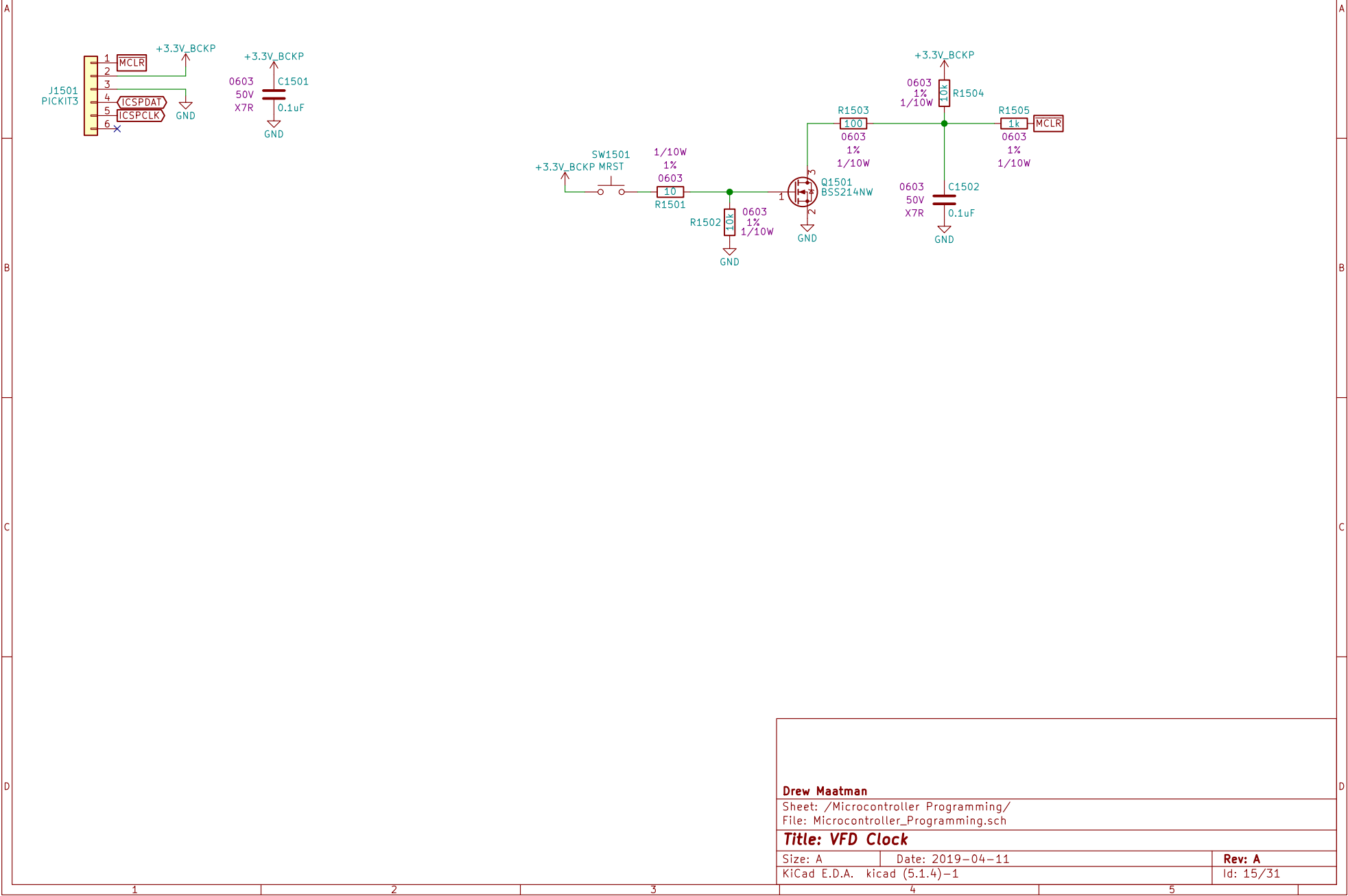
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Rev:

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Microcontroller Programming



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File: Microcontroller\_Programming.sch

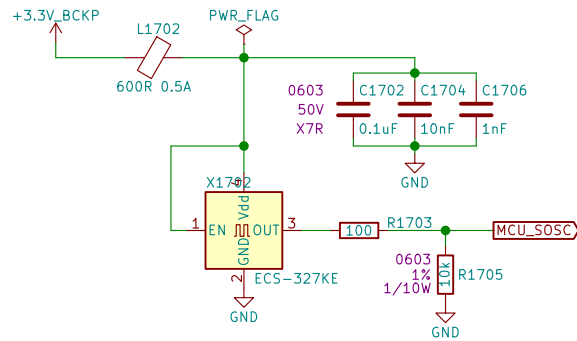
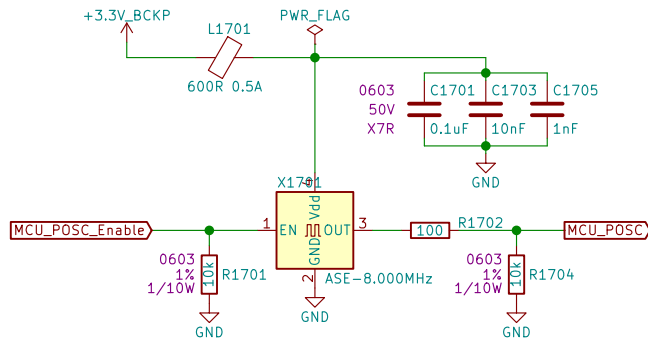
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Rev: A  
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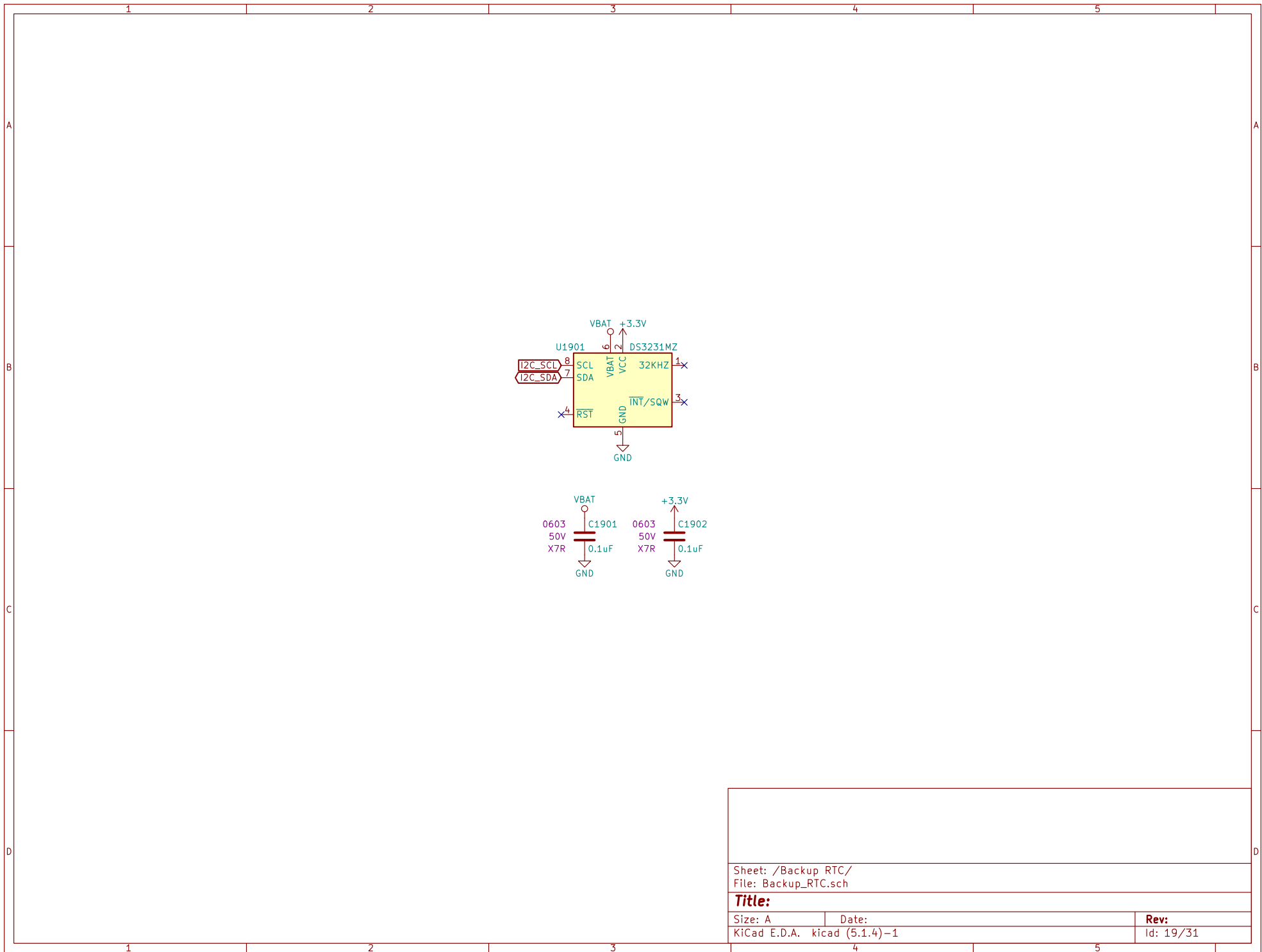


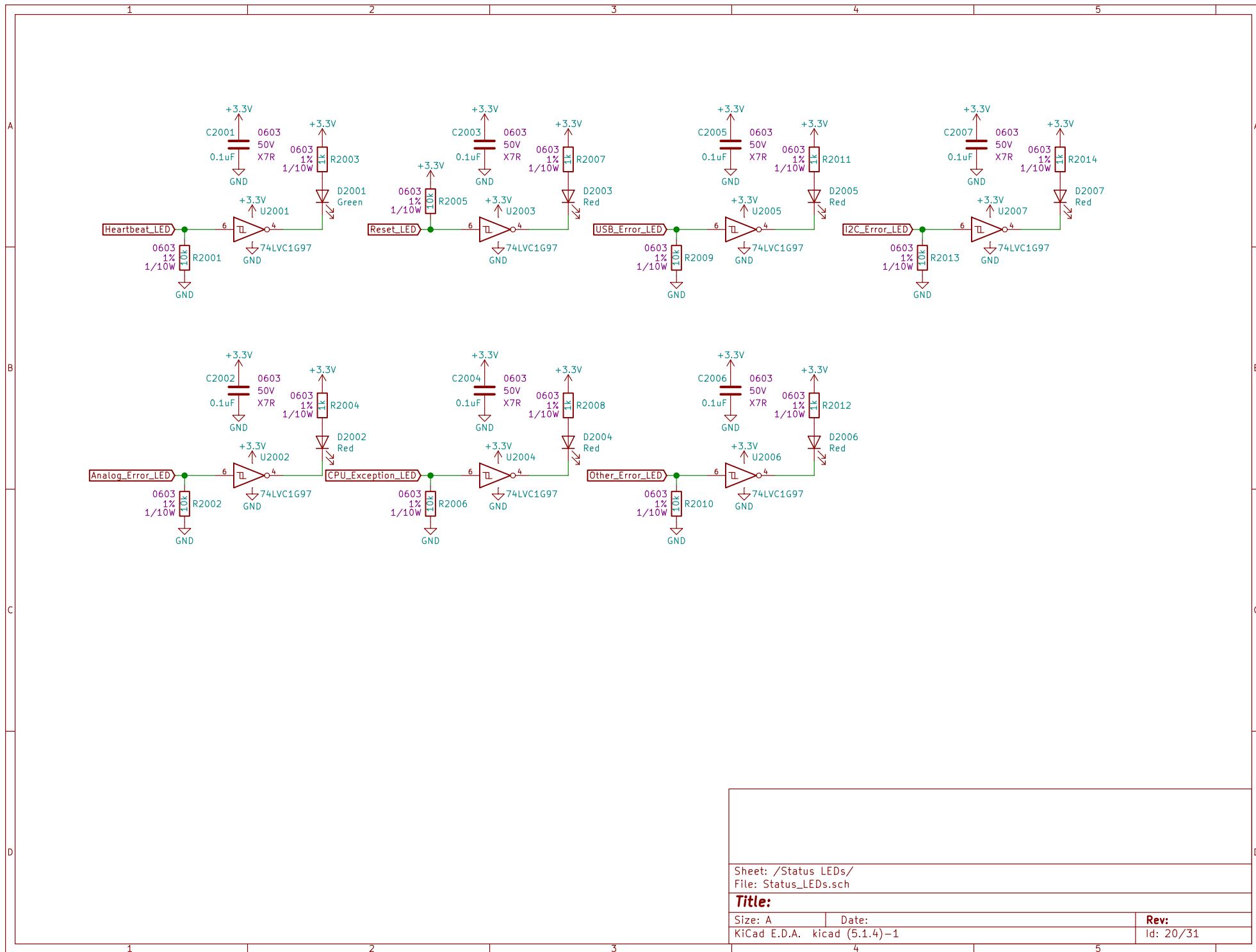
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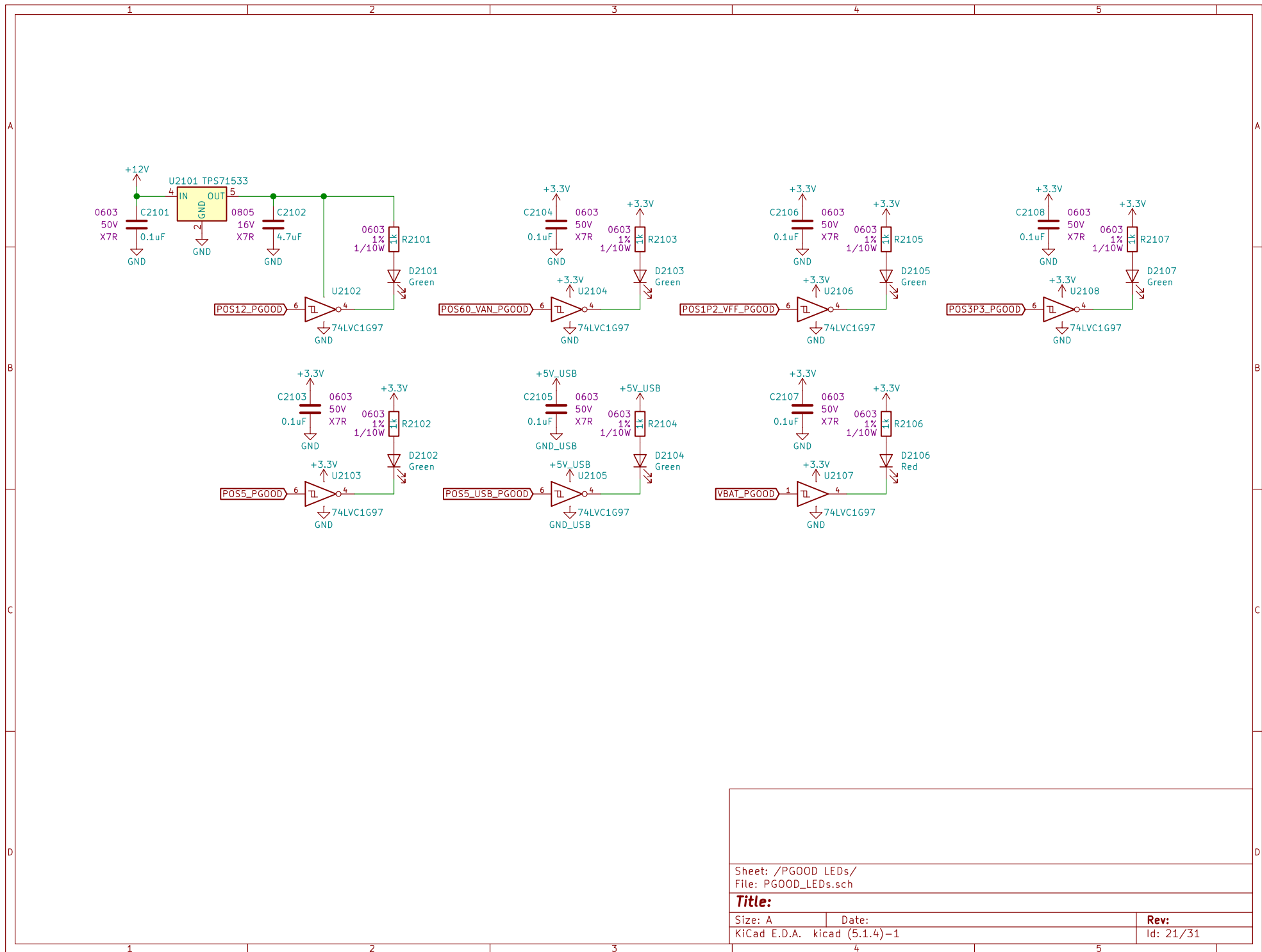
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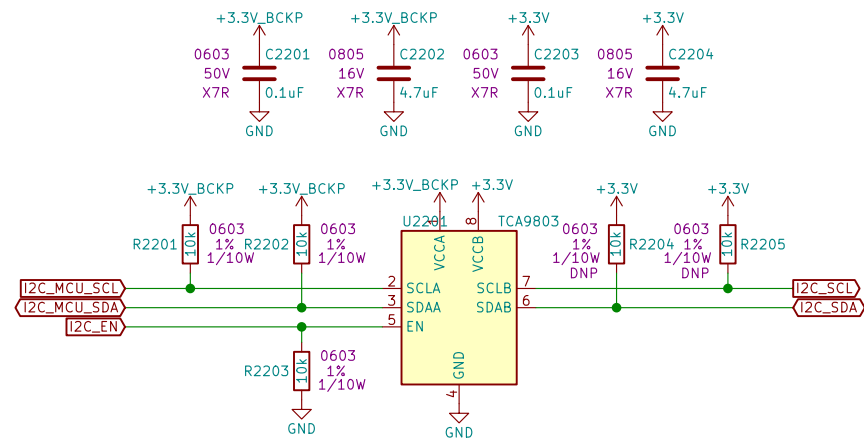
Size: A	Date:	Rev:
KiCad E.D.A. kicad (5.1.4)-1		Id: 17/31











Sheet: /I2C Buffer/  
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**Title:**

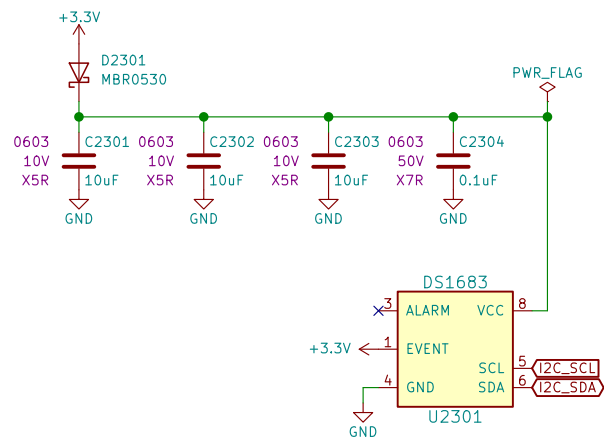
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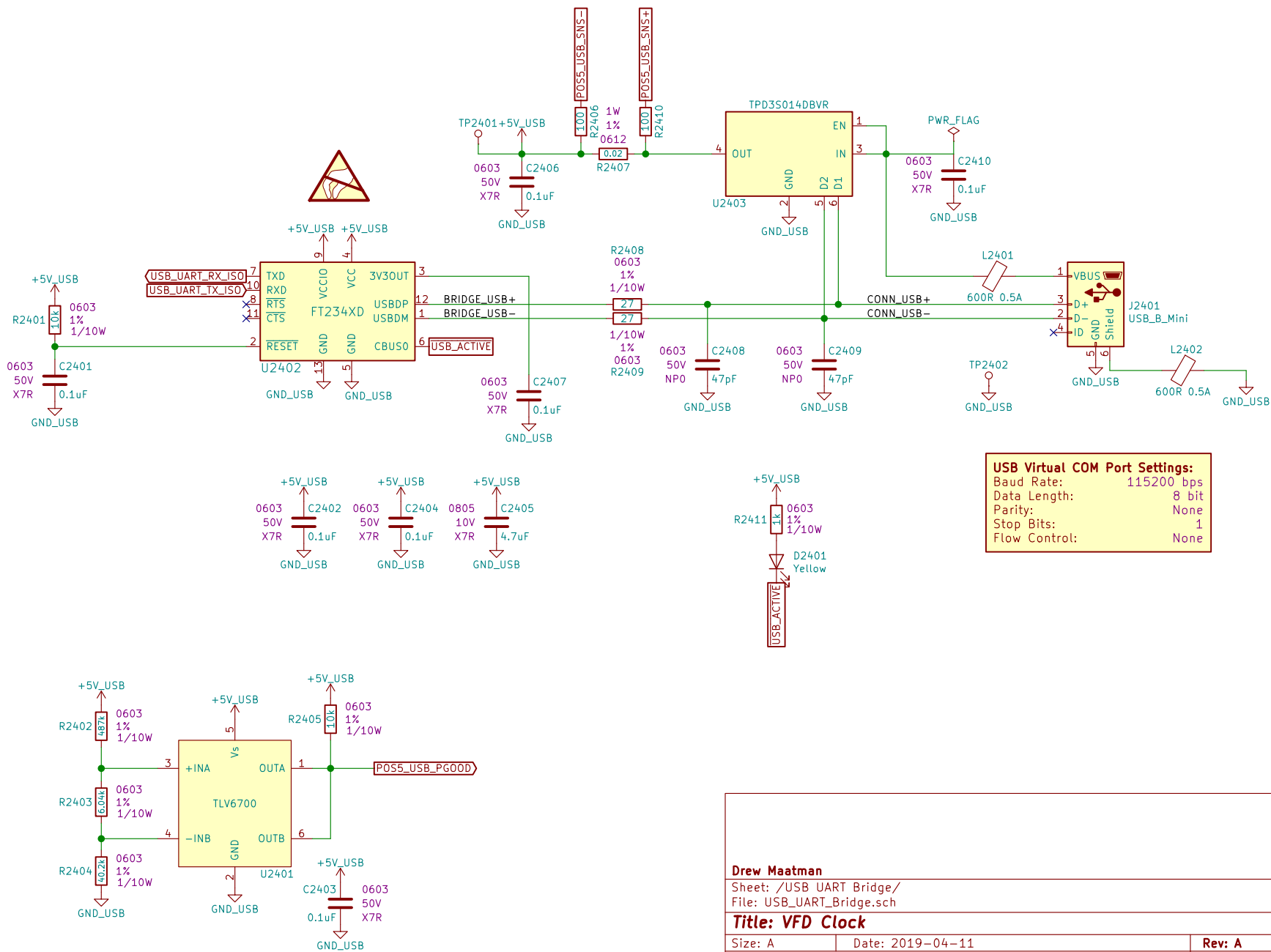
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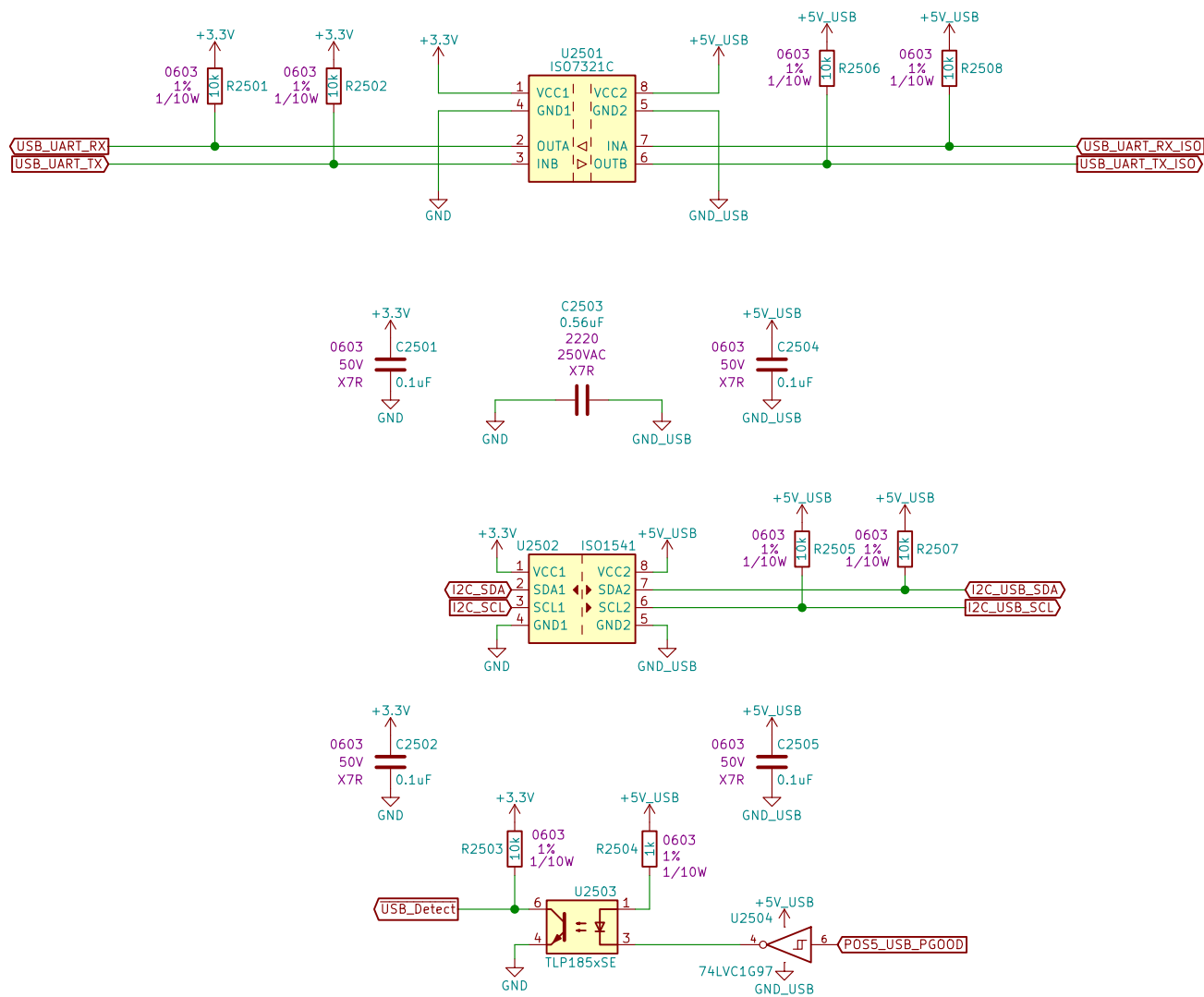
I2C 7-Bit Address:  
0x3B

Sheet: /Time of Flight/ File: Time_of_Flight.sch		
Title:		
Size: A	Date:	Rev:
KiCad E.D.A. kicad (5.1.4)-1		Id: 23/31

## 11. USB UART Bridge







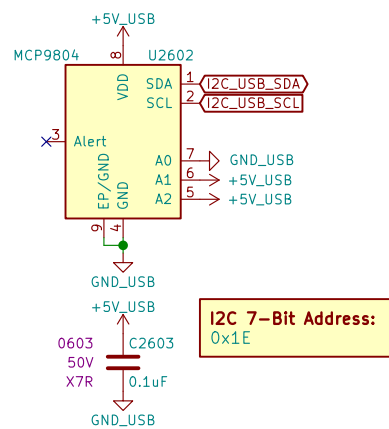
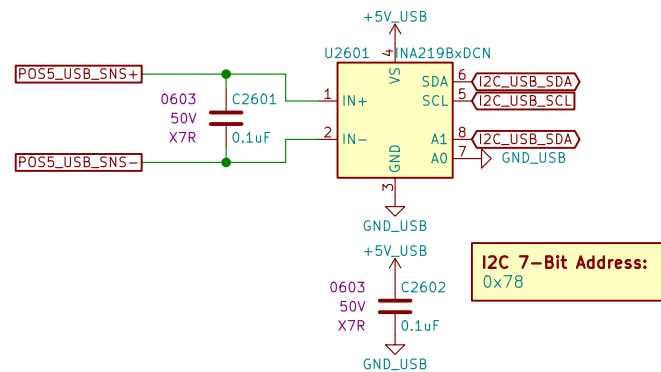
**Drew Maatman**

Sheet: /USB UART Isolation/  
File: USB\_UART\_Isolation.sch

**Title: VFD Clock**

Size: A Date: 2019-04-11  
KiCad E.D.A. kicad (5.1.4)-1

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Sheet: /USB Telemetry/  
File: USB\_Telemetry.sch

**Title:**

Size: A

Date:

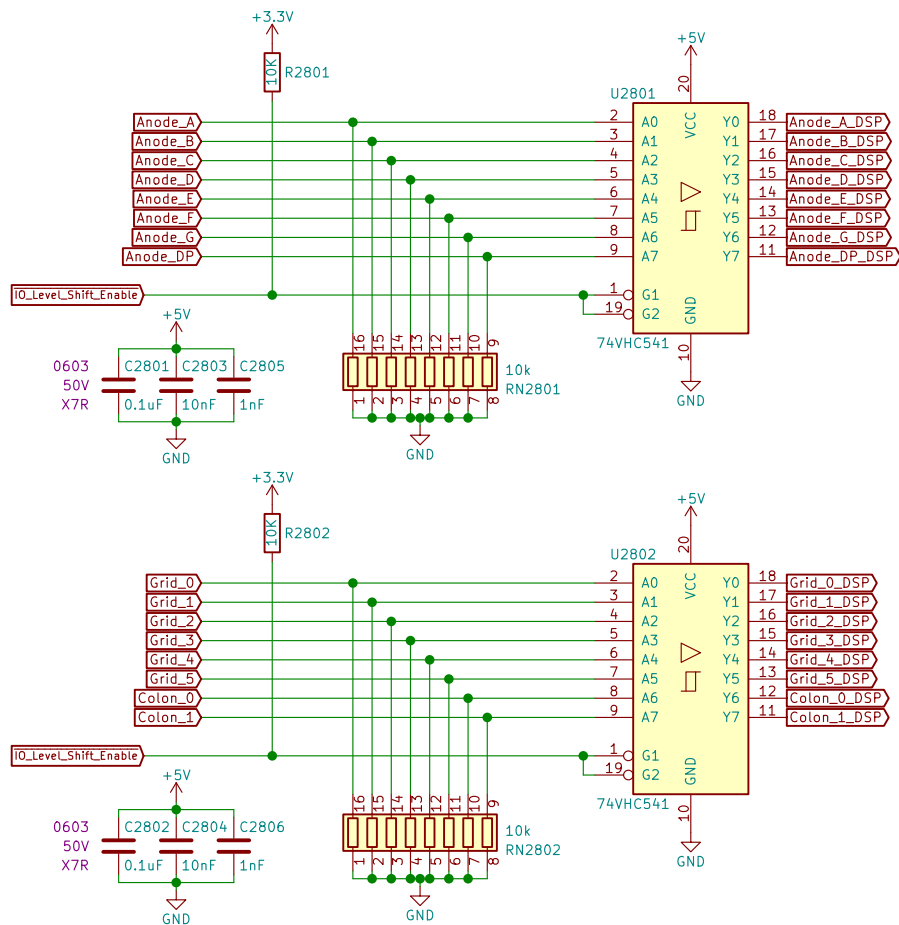
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**Rev:**

Id: 26/31



# I/O Buffers



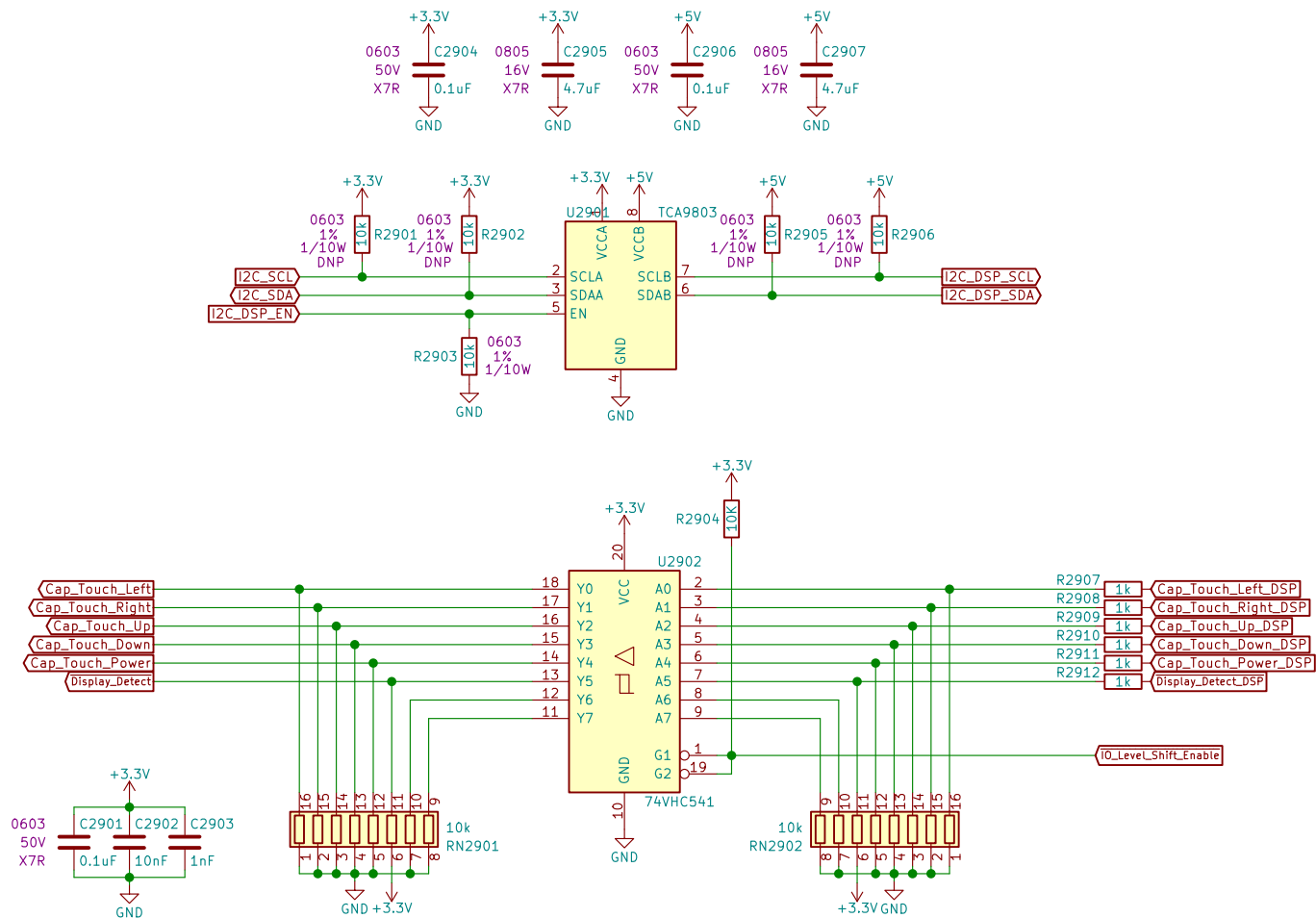
Marquette University Senior Design 2018/2019 Group E44

Sheet: /IO Buffers 1/  
File: IO\_Buffers\_1.sch

**Title: Electronic Display Logic Board**

Size: A Date: 2018-12-15  
KiCad E.D.A. kicad (5.1.4)-1

Rev: A  
Id: 28/31



Sheet: /IO Buffers 2/  
File: IO\_Buffers\_2.sch

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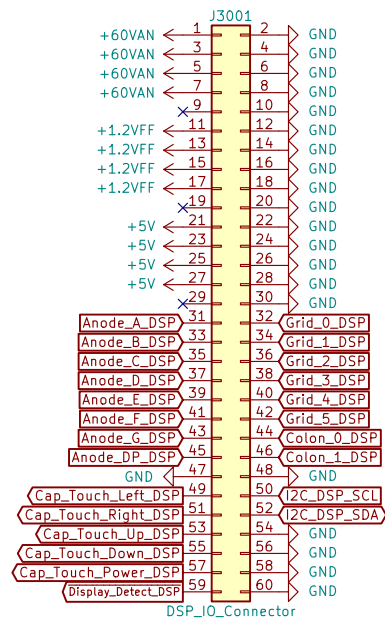
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**Rev:**

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Sheet: /IO Connectors/  
File: IO\_Connectors.sch

**Title:**

Size: A

Date:

KiCad E.D.A. kicad (5.1.4)-1

**Rev:**

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