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Ecsc. Choup 4

The reasons for going for a full fisc. Courter 10 design methodology, com ke as follows 3 =

- . The designer is working to develop the entire this from scrakely, so that he is she has tall breadom in designing the IC.
- · By going ter a full autom IC design, the designer can aim took higher pertormance, lower power dissipation and better area utilization than other design styles.
- . There is no constraint regarding time, as the full austom based IC design takes the highest design ettorit.
- (2) It a particular component is missing and the time to market is a union, the IC designer can go ton a ff GrA based design, where all the logical blocks and their interconnections are already present, and the designer only need to program the interconnections.

 The time taken and the design etterk are low but the downside is that the pertarmance and etteriors are lower than other design styles.
- (3) It a particular component is unawijable but the time to market is not a concern, then the IC designer com go for a bull custom IC based design, where the designer has to design all the logical blocks from scratch. That will take higher design elbert, but will not cause any problem. as time to market is not a concern, it will not cause any problem. On top of that, the designer has the appulantity to increase the performance and elleviency of the IC, compared to other design skyles.

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