33rd International Conference on VLSI Design
19th International Conference on Embedded Design
January 4 - 8, 2020, Leela Palace, Bengaluru, India

VLSI SOCIETY OF INDIA

Conference Theme: Connecting Intelligent Systems to New Age Transistors

Industry Forum

Objective

VLSI Design Conference 2020 will be held during Jan 4th to Jan 8th, 2020 in Bengaluru. The organizing committee will host an **Industry Forum** co-located with the conference. The purpose of this forum is to invite speakers to talk and promote the latest developments and offerings from their companies to a wider audience comprising industry leaders, technologists and enthusiasts. The session will enable a peer-to-peer platform to discuss, discover, define and dissipate knowledge on the industry roadmap. Industry Forum will also serve as a platform to academia for research and career opportunities.

A panel discussion involving experts is also planned during the forum, details of which will be announced later.

Eligibility

Nominated speakers from participating companies, start-ups and government organizations are eligible to participate.

Process

Interested companies are required to submit a proposal containing Name/Brief Bio of Speaker along with topic/abstract of presentation by **Nov 8, 2019**. It would be helpful to highlight the connection of abstract to the conference theme and the overall endeavor of the VLSI Industry for Responsible Research & Innovation. Start-up companies will be supported who can connect to "Make in India" theme of Government of India.

Presentation / Participation

Forum will have slots of 20 minutes for each presentation, spread across 2 days to minimize the conflict with conference technical program. The 20-minute length allows attendees to enjoy multiple presentations in a short time span and runs at a fast pace to keep this event more interesting. Only abstract will appear in conference proceedings. Since the forum is part of VLSID 2020 program, there is no additional registration required.

Schedule

TBD