

Radio Telescope Control System

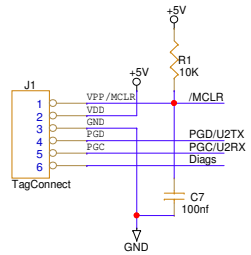
Aaron Olsen, Zach Martin, Loc Pham

Revision & Notes:

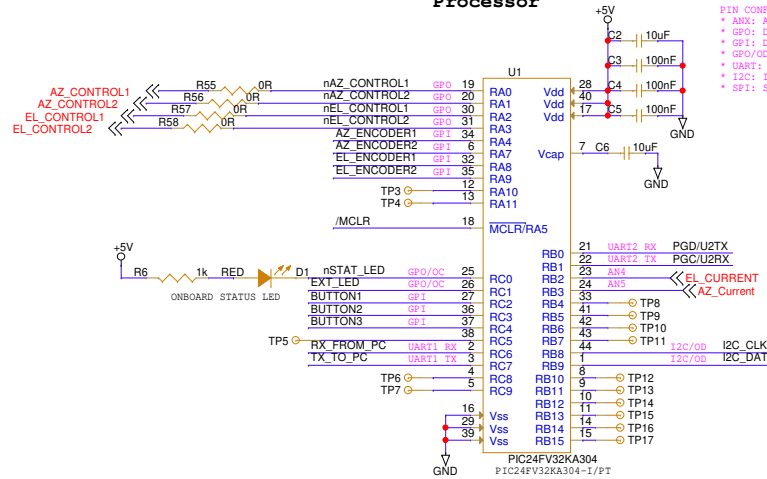
* 11/9/2022: Initial Schematic, Zach Martin

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Program/Debug Port

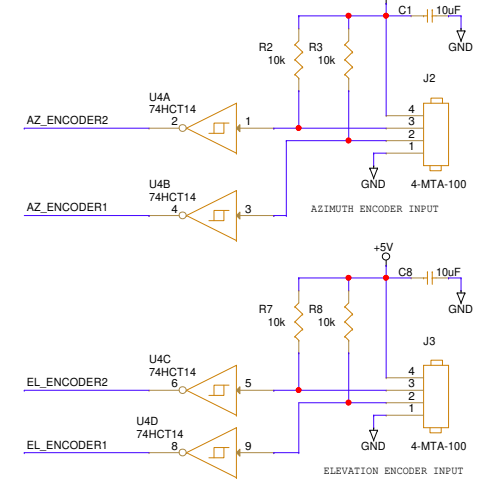


Processor

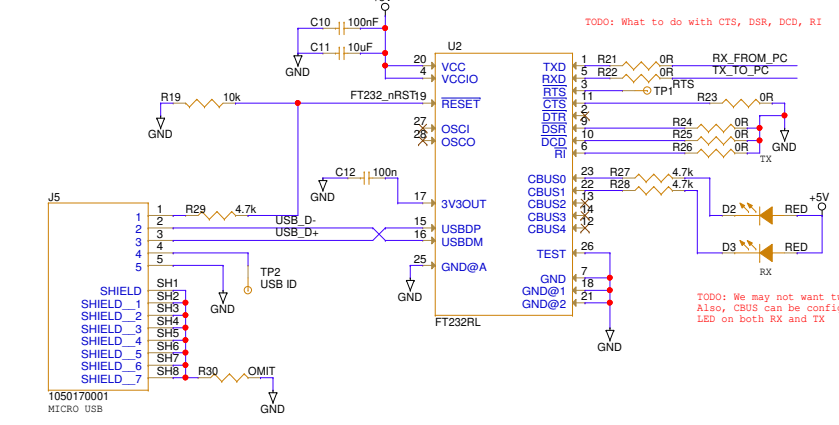


PIN CONFIGURATIONS:
 * ANX: Analog Input/Channel X
 * GPO: Digital Output
 * GPI: Digital Input
 * GPO/OD: Digital Output, Open Drain
 * UART: UART Peripheral I/O
 * I2C: I2C peripheral I/O
 * SPI: SPI peripheral I/O

ROTARY ENCODERS

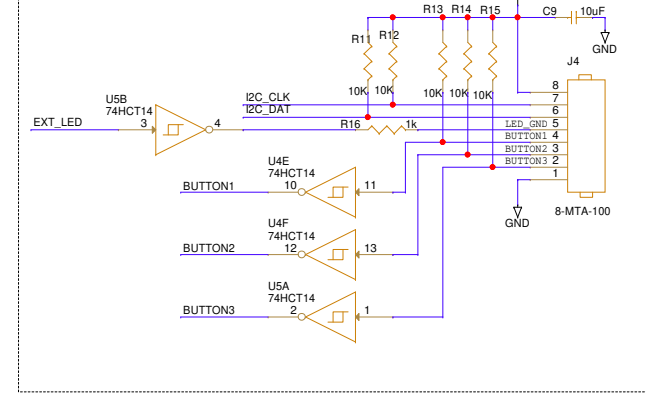


USB-UART Interface



TODO: What to do with CTS, DSR, DCD, RI
 TODO: We may not want two LEDs here. Also, CBUS can be configured to blink an LED on both RX and TX

LOCAL CONTROL INTERFACE



Place Max 74HCT14s

Make sure VCC rails are tied together

Make sure GND nets are tied together

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Main Electronics		
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