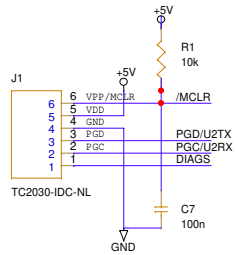


5	4	3	2	1	
<h1>Radio Telescope Control System</h1> <p>Aaron Olsen, Zach Martin, Loc Pham</p> <p>Revision & Notes:</p> <p>* 11/9/2022: Initial Schematic, Zach Martin</p>					
D					D
C					C
B					B
A					A
5	4	3	2	1	

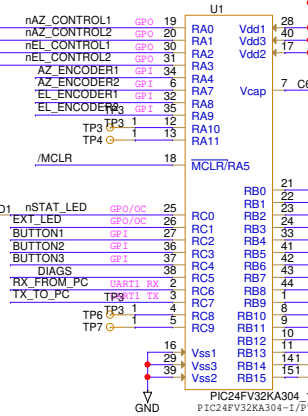
Title		
Title Page		
Size	Document Number	Rev
A	radiotelescope-PCB	0
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Sheet 1 of 3		

Program/Debug Port



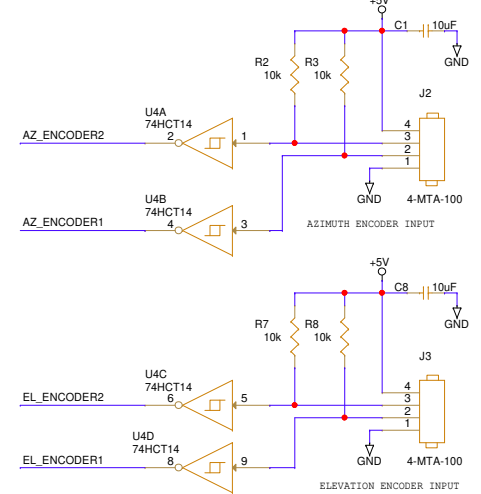
Processor

AZ_CONTROL1
AZ_CONTROL2
EL_CONTROL1
EL_CONTROL2

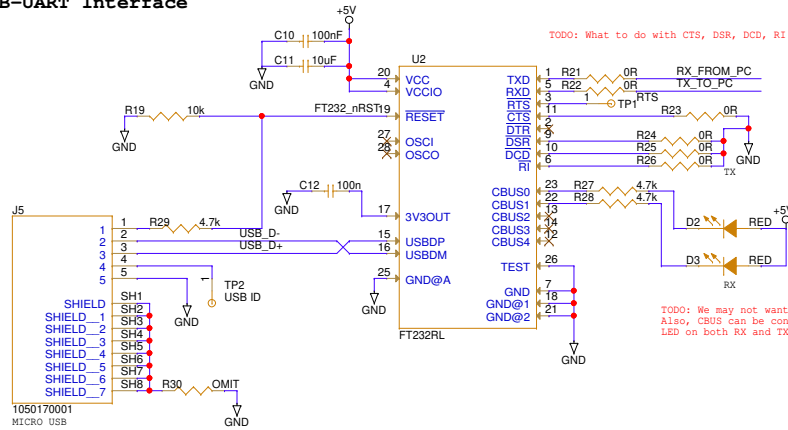


PIN CONFIGURATIONS:
* ANX: Analog Input/Channel X
* GPO: Digital Output
* GPI: Digital Input
* GPO/OD: Digital Output, Open Drain
* UART: UART Peripheral I/O
* I2C: I2C peripheral I/O
* SPI: SPI peripheral I/O

ROTARY ENCODERS



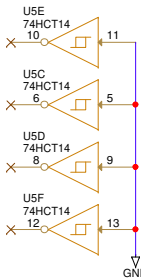
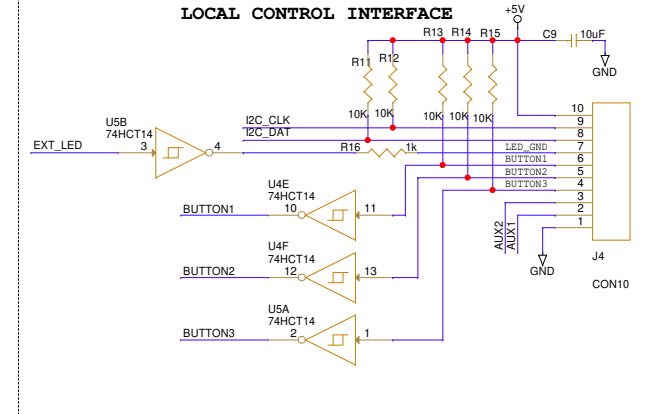
USB-UART Interface



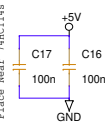
TODO: What to do with CTS, DSR, DCD, RI

TODO: We may not want two LEDs here. Also, CBUS can be configured to blink an LED on both RX and TX

LOCAL CONTROL INTERFACE



Place Max 74HCT14s



Make sure VCC rails are tied together

Make sure GND nets are tied together

Title		
Main Electronics		
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B	radiotelescope-PCB	0
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