



CS/CNVST is pin 10 for the SPI+CCD connector,  
and pin 9 for the SPI only connector.

Note: All of the logic lines are 3.3V.  
The T4 is not 5V tolerant.

Power switch for peripherals. Limits current to 1A and ramps in ~1ms.  
The peripherals typically have capacitive and inductive power filters.

OC (output) is asserted low for over current condition.  
Connected to pin 23, with pull-up to 3.3V