



For 300 nsec sampling window, 16 bits, we need RC seen by the sampling caps (inside the ADC) to be smaller than $300\text{nsec}/(16 \times \ln(2)) = 27\text{nsec}$

Single pixel line generates an additional kickback, now from the charge reservoir (C31/2). We tame this with the RC of the feedback in the FDA.

Mitchell C Nelson, PhD (c) 2024, 2025 Patent Pending

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