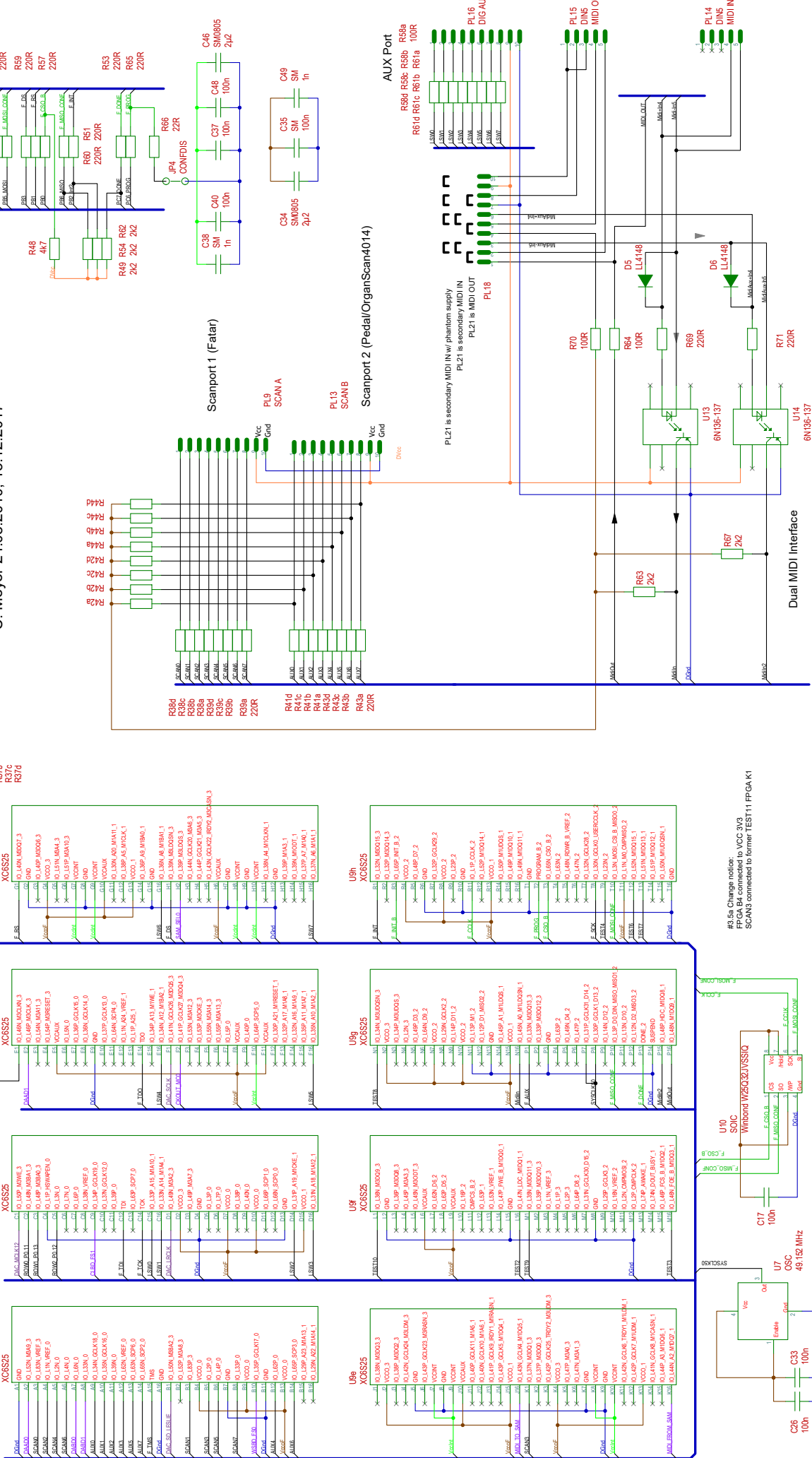


HX3.5 FPGA (SAM5504B)

C. Meyer 24.08.2010, 13.12.2017



43.5u Charge editor
FPGA B4 connected to VCC 3V3
SCAN3 connected to former TEST11 FPGA K1

OSC
49.152 MHz