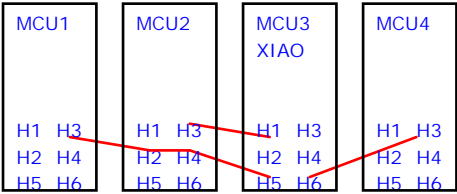


Distance between headers 15.24 mm
Connections between different PCBs
Clock In/Out

AND (input buffer)

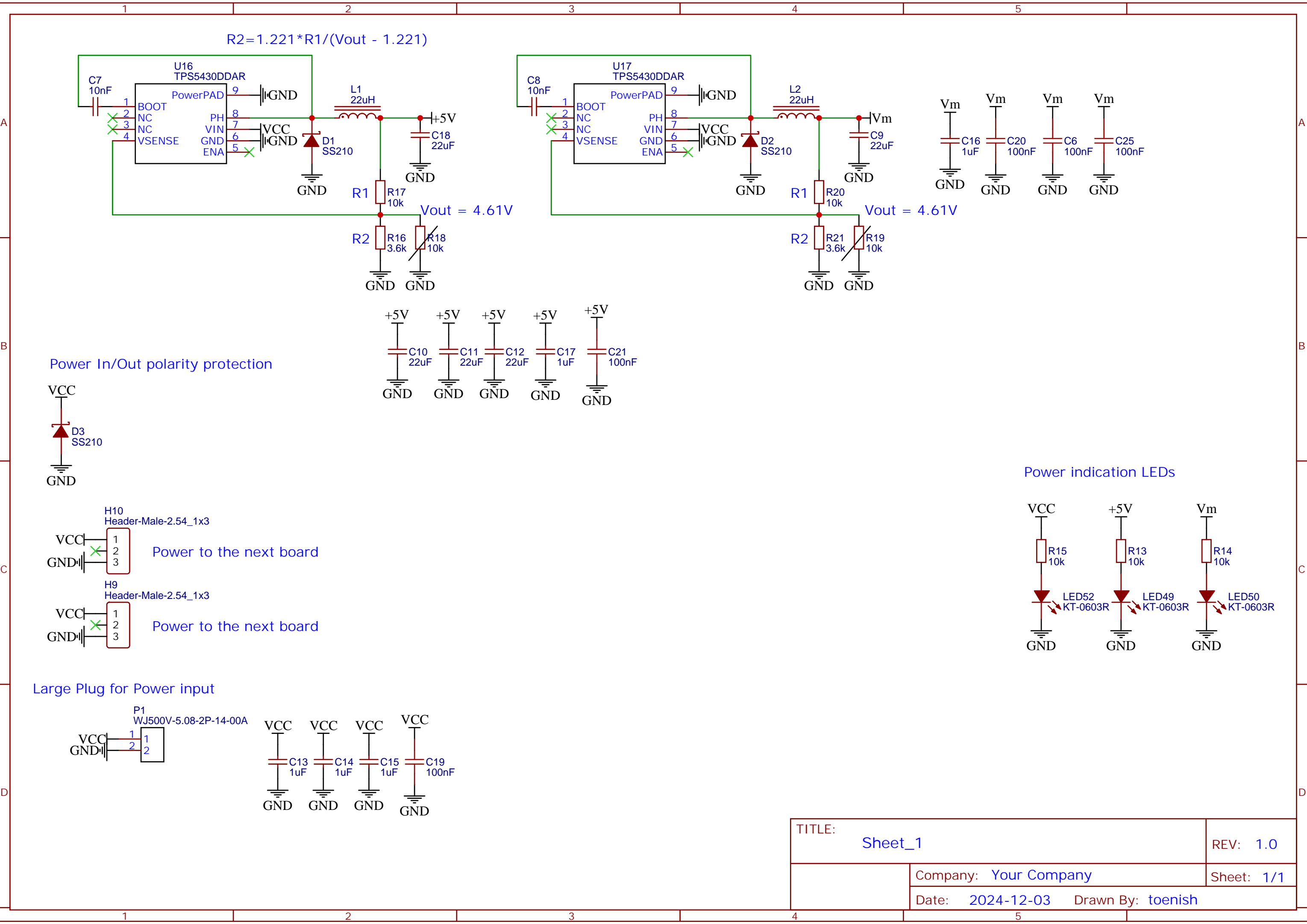
first shift register - J1 + J2

last shift register - J7



H2 and H4 are connected together
H1 - out H3 - input
H5 - out
H6 - out
H7 - clock signal (in/out)
H8 - clock signal (in/out)

TITLE: Sheet_1		REV: 1.0
	Company: Your Company	Sheet: 1/1
	Date: 2024-12-03	Drawn By: toenish



$R2 = 1.221 * R1 / (V_{out} - 1.221)$

Vout = 4.61V

Vout = 4.61V

Power In/Out polarity protection

Power indication LEDs

Large Plug for Power input

TITLE: Sheet_1		REV: 1.0
	Company: Your Company	Sheet: 1/1
	Date: 2024-12-03 Drawn By: toenish	