Nathan Donaldson/U0632803

-What is the purpose of the NVIC peripheral?

Enable and disable interrupts, indicate requests waiting to be serviced, cancel pending interrupt requests, and set how multiple interrupts interact through configurable priorities.

-What is the difference between interrupt tail-chaining and nesting?

Interrupt handlers are never interrupted in tail-chaining but they are in nesting when it is a lower level priority handler.

-In what file are the CMSIS libraries that control the NVIC?

core_cm0.h file.

-What is the purpose of the EXTI peripheral?

allows non-peripheral sources to trigger interrupts.

-What is the purpose of the SYSCFG pin multiplexers?

These multiplexers group external pins by their orderings within the GPIO peripherals.

-What file has the defined names for interrupt numbers?

In the IRQn_Type enumeration within the stm32f072xb.h file.

-What file has the Vector table implementation?

startup_stm32f072xb.s