Libreverse - parserGenerator

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 $[\mathit{CPU}, \mathit{Table}, \mathit{Token}, \mathit{Rule}, \mathit{NIL}, \mathit{Bit}]$

```
\_InternalData\_
  cpu: CPU
  tableList: \mathbb{P} Table
  tokenList: \mathbb{P}\ Token
  ruleList: \mathbb{P} \, Rule
  _ INIT ____
  cpu = NIL
  tableList = \varnothing
  tokenList = \emptyset
  ruleList = \varnothing
  \_setCPU\_
  \Delta cpu
  newCPU?:CPU
  newCPU? \neq cpu
  cpu' = newCPU?
  addTable_{-}
  \Delta(tableList)
  table?: Table
  table? \not\in tableList
  tableList' = tableList \cup \{table?\}
   addToken .
  \Delta(tokenList)
  token?: Token
  token? \not\in tokenList
  tokenList' = tokenList \cup \{token?\}
  \_addRule \_
  \Delta(ruleList)
  rule?: Rule
  rule? \not \in ruleList
  ruleList' = ruleList \cup \{rule?\}
```

```
CPU \_
registers : \mathbb{P} Register
INIT \_
registers = \emptyset
-addRegister \_
reg? : Register
reg? \not\in registers
registers' = registers \cup \{reg?\}
```

```
\_Register\_
  bits : seq Bit
  indexes: String \rightarrow \mathbb{N}
  maxLength: \mathbb{N}
  name: String
  endian: Endian
  \#bits == maxLength
  endian ::= little \mid big
  INIT _
  name?: String
  index?: \mathbb{N}
  bits = \operatorname{seq} Bits
  index? > 0
  indexes = name? \rightarrow index?
  maxLength = index?
  endian = little
  \_addSubIndex\_
  \Delta indexes
  name?: String
  index?: \mathbb{N}
  name? \not\in dom\ indexes
  0 \le index? \le maxLength
  indexes' = indexes \cup \{name? \rightarrow index?\}
  \_getBitSequence\_
  name?: String
  result! : seq Bits
  name? \in dom indexes
  ((endian = little) \land (result! = reverseSubSeq(bits, indexes(name?))) \lor
  ((endian = big) \land (result! = SubSeq(bits, indexes(name?)))
  \#result! = indexes(name?)
  reverseSubSeq: (seq Bits \times \mathbb{N}) \rightarrow seq Bits
```