Time Division Multiplexing of Network Access by Security Groups in High Performance Computing Environments

Joshua Ferguson

No Institute Given

Abstract. As the scale of data used in computation increases the utility of High Performance Computing as a Service increases in tandem. Security concerns in such a domain are still being solved using traditional Operating System mechanisms of memory protection and file access privileges [3]. However there exist scenarios where customers demand more intuitive and rigorously verifiable security solutions. This paper presents a robust and application transparent network security solution that is both intuitive and easily verifiable. This paper's main contribution is a mechanism designed to enforce high level time division multiplexing of network access according to security groups. By dividing network access into time windows, interactions between applications over the network can be prevented in an easily verifiable way.

1 Introduction

High Performance Computing (HPC) systems are comprised of numerous individual computing systems networked and administrated together such that they can be used as a single system. Popular examples of these systems include some custom made such as the Cray I and the modern IBM Sequoia [7], though more common are simple Computer Clusters in which Commercial Off The Shelf (COTS) equipment is utilized [3]. Security within these systems is often enforced through traditional Operating System (OS) mechanisms of memory protection and file access privileges [3].

-*-*-*-*-BEGIN new paragraph-*-*-*-*-

Application developers for this domain of systems spans a broad spectrum, ranging from undergraduate students learning concurrent programming to defence contractors executing classified simulations. Moving towards the most demanding end of the spectrum, security concerns among application-side stakeholders increase and additional methods are employed to enforce information security. At some point along this spectrum stakeholders decide physical separation to satisfy security concerns. The reasons behind this can be numerous, but likely stem from two major goals: simplicity of implementation and verification; and risk aversion/management. It is undeniable that physical separation provides a level of information security that is hard to replicate through the use

of software, however the financial costs are significant - devoting entire HPC systems to running jobs sequentially.

-*-*-*-*-END new paragraph-*-*-*-*-

This paper presents Time Division Multiplexing (TDM) of network access as a viable alternative to physical separation. By modulating network access between application security groups we can provide an intuitive security mechanism capable of being verified in real-time. Furthermore by implementing this mechanism at the operating system level it becomes transparent to user applications, meaning no modification to existing application code is necessary. [Mention we have built a prototype, and mention formal proof.]

2 Related Work

3 Problem Definition

We begin by defining an abstract HPC environment through which the general case of our security challenge is shown.

3.1 An Abstract HPC Environment

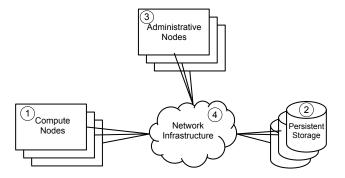


Fig. 1. An abstract HPC environment.

There are four basic resources in most HPC systems represented in Figure 1 as:

 compute nodes - independent computing devices designated to run user submitted applications. These devices are capable of storing temporary data locally. They send and receive data across network infrastructure for three main purposes:a) storing or accessing data on the persistent storage devices; b) relaying data between other compute nodes working in tandem on the same user application; c) and sending or receiving commands (or reports,

- as the case may be) from the administrative nodes, through which users interact.
- 2. **persistent storage** storage devices (usually of much higher capacity than the compute nodes) capable of storing large quantities of user application data. User data is commonly co-located across disks using RAID (redundant array of inexpensive disks) technology [5] for higher storage efficiency and redundancy.
- 3. administrative nodes computing devices where a) both administrators and users interact with the system, common tasks of which include issuing job or system commands, accessing reports and results, and performing maintenance; b) resource management software is centrally located and executed [6], common examples include IBM's Tivoli Workload Scheduler and the MOAB Cluster Suite by Adaptive Computing [1][4].
- 4. **network infrastructure** devices facilitating the transmission of data between nodes within the HPC system. Mediums vary widely and include copper, optical, and wireless. The most common technologies used in HPC environments are Ethernet and InfiniBand [2][8].

3.2 Security Challenge

Board Posix **Hardware Location** Security Challenge Separation | IME Permissions Local Data Storage Compute Nodes Local Data Processing Persistent Storage Co-location of user data Administrative Node Accept and Schedule User Jobs Transmit intra-application data between compute nodes Transmit data to/from compute Network Infrastructure nodes and persistent storage

Solutions

Table 1. Security challenges and technology used to solve them

Transmit commands from scheduler to compute nodes

The security fear of customers with sensitive data is that a different customer could, through chance or intention, acquire or manipulate their data. There exist three main mediums across which data may be exposed.

4 Design Goals

Here we describe the major goals in the design and implementation of the TDM security mechanism.

4.1 Mechanism and Policy Separation

Envisioning the tool as enabling a more secure form of HPCaaS, the type, number, and scale of jobs assigned to any of the systems can be widely varied. To handle this, at the least, the tool must be capable of receiving and modifying policy at the start of each new job. Furthermore, to provide more fine-grained control an online policy modification scheme is desirable for managing performance tradeoffs between security groups. We define the mechanism and policy split of our tool to be an ability to receive and actuate policy changes at the finest granularity in which the tool operates. [Mechanism Policy split is really along the difference between the formal notion that nodes must be transparently separated from the medium and how it is accomplished (with a queue, at the TCP layer, on ethernet, etc.)]

4.2 Network Fabric Agnostic

Two major technologies are used to network HPC systems: Ethernet and Infini-Band. Any tool for improving security across the broad spectrum of HPC systems must be capable of operating in each. Further, numerous network topologies exist within these technologies; switched fabric and tree structures are the most common for InfiniBand and Ethernet, respectively. For our purposes we define this tool to be network fabric agnostic if the tool is conceptually capable of being implemented in either Ethernet or InfiniBand networks.

4.3 User Application Transparent

A fundamental requirement of the tool is that it be transparent to user applications. Applications written for HPC environments are often quite complex and it is likely that customers would be reluctant to make even minor modifications, especially to programs written in the past that are under re-use. For our purposes we define user application transparency as the ability to run an application without modification to successful end on an HPC system using our tool, given that it can also do so on a system not using our tool.

4.4 Provably Secure

The final and most important requirement of this tool is that it provably perform its task.

5 Implementation

As a proof of concept we have implemented a version of the tool for the Linux operating system using C++11. In this section we will describe the tool's architecture, operation, and how it adheres to the design goals from the previous section.

Algorithm 1 Window Controller opening and closing network access windows.

```
1: function Open_Windows(Scheduler)
2:
       Scheduler.initialize();
3:
       while End\_Command\_Not\_Received do
4:
           Security\_Group \leftarrow Scheduler.get\_next\_group();
5:
           Security\_Group.state \leftarrow STATE.OPEN;
6:
           for each node \in Security\_Group do
7:
               send(node.address,
                    Security_Group.crypto_sign(COMMAND.OPEN));
8:
               node.state \leftarrow \text{STATE.OPEN};
9:
           while Security\_Group.state == STATE.OPEN do
10:
              node\_response \leftarrow block\_on\_receive\_message();
               if node\_response.state == STATE.CLOSED then
11:
12:
                  node.state \leftarrow \text{STATE.CLOSED};
13:
               else
14:
                  throw ERROR.UNCLOSED_NODE;
15:
               Security\_Group.state \leftarrow STATE.CLOSED;
16:
               for each node \in Security\_Group do
                  if node.state == STATE.OPEN then
17:
18:
                      Security\_Group.state \leftarrow STATE.OPEN;
```

Algorithm 2 Node Control Mechanism opening and closing access to the network.

```
1: function Node_Control_Mechanism
 2:
       Queue \leftarrow initialize\_queue;
 3:
       while exit\_command\_not\_received do
           state \leftarrow Close\_Network\_Access(Queue);
 4:
 5:
           while open_command_not_received do
 6:
               message \leftarrow block\_on\_receive\_message();
 7:
           state \leftarrow Open\_Network\_Access(Queue);
 8:
           sleep(message.time);
 9:
           state \leftarrow Close\_Network\_Access(Queue);
10:
           send\_acknowledgement(state);
11: \ \mathbf{function} \ Close\_Network\_Access(Queue)
12:
        state.egress \leftarrow Network\_Egress.enqueue(Queue);
        state.ingress \leftarrow Network\_Ingress.drop\_packets();
13:
14:
        return state
15: function Open_Network_Access(Queue)
        state.ingress \leftarrow Network\_Ingress.accept\_packets();
16:
17:
        state.egress \leftarrow Queue.process\_packets\_to\_network();
18:
       {f return}\ state
```

5.1 Overview

The tool is composed of four major components: the window scheduler, ingress controller, egress controller, and the state controller. The window scheduler can be located on any administrative node within the system, preferably co-located with the system job scheduler. The remaining controllers are located throughout the HPC environment, with a copy on each compute node that is designated to execute user applications.

The system provides enhanced security by time dividing access to the network according to security groups. As jobs are scheduled on the system, the window scheduler must be informed of the intended location and their assigned security group. As jobs are run on compute nodes throughout the system the window scheduler communicates with the state controller on each node to designate time windows. During any individual time window only one security group has authorization to access the network. For any given time window in which a security group does not have access to the network, outgoing network packets are stored in a local queue while incoming packets are just ignored and deleted. The window scheduler is tasked with alternating time window authorization between security groups.

The following subsections describe each components operations in further detail.

5.2 State Controller

The state controller has three major tasks:

- 1. Securely send and receive communication with the window scheduler for the system ${\bf r}$
- 2. Transit both the ingress and egress controllers between states of network access and denial
- 3. Collect and store performance data on the egress queue's memory usage

5.3 Ingress Controller

The ingress controller is a firewall of incoming network packets and has two states:

- 1. Open access of network packets to applications on the node
- 2. Closed to network packets except for those from explicitly allowed sources (a whitelist style of firewall)

During the open state incoming packets are processed normally. During the ingress closure, incoming packets, except those allowed by the whitelist, are dropped. The whitelist is designated to allow only necessary infrastructure communications such as Network Time Protocol (NTP), performance measurements, and especially packets from the window scheduler.

5.4 Egress Controller

The egress controller, similar to the ingress controller, has two major states:

- 1. Open flow of packets onto the network
- 2. Diversion of outgoing packets into a blocked queue

5.5 Window Scheduler

The window scheduler has three major tasks:

Determine system network access states for the next time window Validate the closure of the previous time window Communicate the next time window states to compute nodes To determine the network access states for the next time window the scheduler must run a scheduling algorithm on a few historical inputs. The base case scheduling algorithm is round robin (i.e., equal window size for each security group). To improve performance, a number of heuristics have been considered for the creation of a dynamic priority scheduling algorithm: the egress controller memory usage of compute nodes, number of TCP timeouts, and externally imposed priorities.

6 Case Study

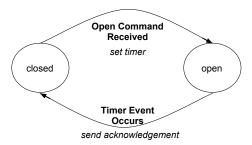


Fig. 2. State diagram of a compute node.

6.1 Performance

6.2 Security Validation

7 Formal Definition

Suppose S is a finite set of security groups, s.t. each security group $s \in S$ is made up of a number of compute nodes. Given S, the language our mechanism

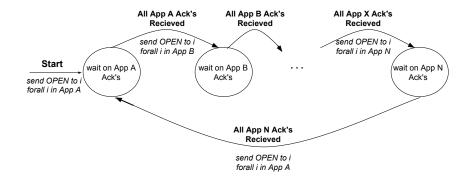


Fig. 3. State diagram of an example window controller.

operates on can be generated by a context free grammar. Because the language is dependent on the security groups S, this grammar must be generated based on it. This is done in two steps:

First, we define the base grammar:

$$\begin{array}{ll} G^1 = (V^1, \varSigma^1, R^1, \mathcal{A}), \, \text{where} \\ V^1 = \{\mathcal{A}, W\} & \text{non-terminal symbols} \\ \varSigma^1 = \{\emptyset\} & \text{terminal symbols} \\ R^1 = \{ \begin{array}{c} \mathcal{A} \to \varepsilon, \\ \mathcal{A} \to W \mathcal{A} | W \} \end{array} \end{array}$$
rules of production

This base grammar, through the non-terminal symbols and production rules, establishes a means of generating the base language form of unordered windows $(W \in V^1)$ in an arbitrary length such as WW or WWWWW.

Next, we generate the S specific definitions. To do so it is first necessary to define notation for two special terminal symbols and three special sets:

```
o_{s,i} - an open command issued to node i within security group s, a_{s,i} - an acknowledgement received from node i within security group s, \theta_s - the set of all o_{s,i} terminals for security group s, \alpha_s - the set of all a_{s,i} terminals for security group s, and \pi(A) - the set of all permutations of the set A.
```

These definitions allow us to define a final, special set:

$$\Lambda_s = \pi(\theta_s) \times \pi(\alpha_s)$$

Intuitively, Λ_s is a set of ordered sets expressing each permutation of θ_s matched with each permutation of α_s . For example, given a security group s made up of two elements s.t. $s = \{1, 2\}$, Λ_s is defined:

$$\Lambda_s = \{ (o_{s,1}, o_{s,2}, a_{s,1}, a_{s,2}), \quad (o_{s,2}, o_{s,1}, a_{s,1}, a_{s,2}), \\ (o_{s,1}, o_{s,2}, a_{s,2}, a_{s,1}), \quad (o_{s,2}, o_{s,1}, a_{s,2}, a_{s,1}) \}$$

The sets within Λ_s represent all legitimate command sequences within a window (W) for security group s. The key property of the sets within Λ_s is that each node within the security group is issued an open command, in any order, followed by acknowledgements from each node within the security group, once again in any order.

With these definitions established we can now formally define an S specific grammar:

$$\begin{split} G^2 &= (V^2, \varSigma^2, R^2, \emptyset), \text{ where } \\ V^2 &= \{W\} \\ \varSigma^2 &= \{[o_{s,i}, a_{s,i}] : \forall i \in \forall s \in S\} \\ R^2 &= \{[W \to \lambda] : \forall \lambda \in \varLambda_s : \forall s \in S\} \end{split}$$

These definitions add new terminal symbols and the necessary production rules to generate them. Note the use of Λ_s in the production rules. These rules provide every possible command sequence possible for any window W s.t. every node issued an open command is required to report back with an acknowledgement before continuation onto another window.

Finally, the language our mechanism accepts for security group S can be formed using the union of the previous two grammars:

$$G = (V, \Sigma, R, \mathcal{A}), \text{ where }$$

$$V = V^1 \cup V^2$$

$$\Sigma = \Sigma^1 \cup \Sigma^2$$

$$R = R^1 \cup R^2$$

8 Conclusion

8.1 Further Work

Implement dynamic scheduling algorithm on the window scheduler using memory usage statistics from compute nodes.

References

- V.S. Arackal, B. Arunachalam, MB Bijoy, BB Prahlada Rao, B. Kalasagar, R. Sridharan, and S. Chattopadhyay. An access mechanism for grid garuda. In *Internet Multimedia Services Architecture and Applications (IMSAA)*, 2009 IEEE International Conference on, pages 1–6. IEEE, 2009.
- M. Bozzo-Rey, M. Jeanson, M.N. Nguyen, C. Gauthier, M. Barrette, P. Vachon, K. Gaven-Venet, H.Z. Lu, S. Allen, and A. Veilleux. Design, deployment and bench of a large infiniband hpc cluster. In *High-Performance Computing in an Advanced Collaborative Environment*, 2006. HPCS 2006. 20th International Symposium on, pages 8–8. IEEE, 2006.

- 3. R. Buyya. *High Performance Cluster Computing: Architectures and Systems*, volume 1. Prentice Hall, Upper SaddleRiver, NJ, USA, 1999.
- 4. D.B. Jackson. On-demand access to compute resources, April 7 2006. US Patent App. 11/279,007.
- 5. R.H. Katz, G.A. Gibson, and D.A. Patterson. Disk system architectures for high performance computing. *Proceedings of the IEEE*, 77(12):1842 –1858, 1989.
- 6. A. Keller and A. Reinefeld. Anatomy of a resource management system for hpc clusters. *Annual Review of Scalable Computing*, 3(1):1–31, 2001.
- N. Leavitt. Big iron moves toward exascale computing. Computer, 45(11):14–17, 2012.
- 8. B. Madai and R. Al-Shaikh. Performance modeling and mpi evaluation using westmere-based infiniband hpc cluster. In *Computer Modeling and Simulation* (EMS), 2010 Fourth UKSim European Symposium on, pages 363–368. IEEE, 2010.