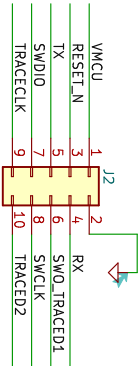
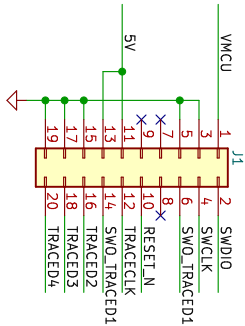
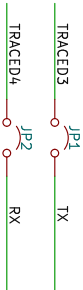


Using a single Trace pin (SWO) may cause the CPU to stall during certain instruction sequences. Be aware of this change in the CPU flow if using a single pin. Recommendation is to use 2 pins or ideally all 4.



NOTE! The Minisimplicity header normally connects PT1 to pins 9 and 10. The DUT must be wired with the TRACE pins instead. Typically the Ziffer is a separate standalone device and the DUT PT1 pins are not used. Thus, they are better utilized as TRACE pins. Recommend a solder jumper be used to choose the pinout during debug.



Optionally connect all 4 Trace pins using the VCOM pins. In most situations, 2 data pins are sufficient with minimal stalling. The VCOM pins are handy for debug PRINTF which is recommended.



Optional connect +5V to VMCU with a solder bridge to power DUT from JTRACE. NOTE! DANGER! This is +5V! VMCU is normally +3.3V! The DUT must handle VMCU at +5V which is not typical.

Use this small PCB adapter to plug directly into the Segger J-Trace Trace port and then connect to a Minisimplicity header with customized pinouts.

DzWave

Sheet /

File: J12Mini.kicad.sch

Title: J-Trace to Minisimplicity adapter

Size: A Date: 2023-06-08

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