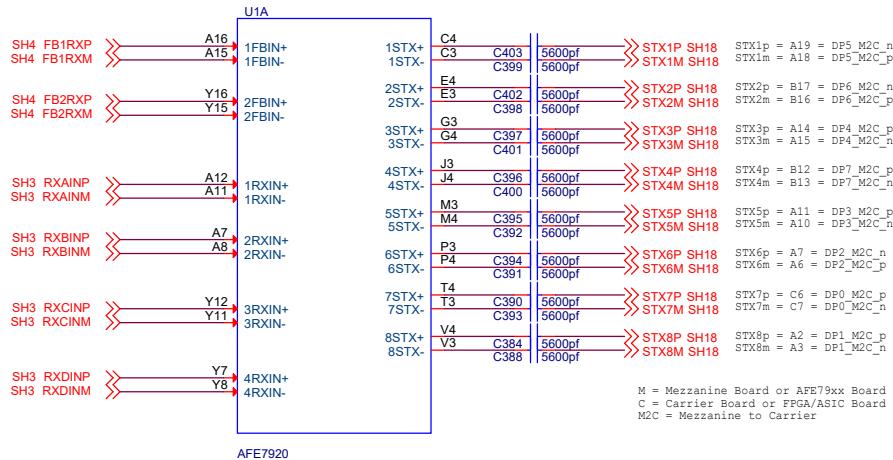
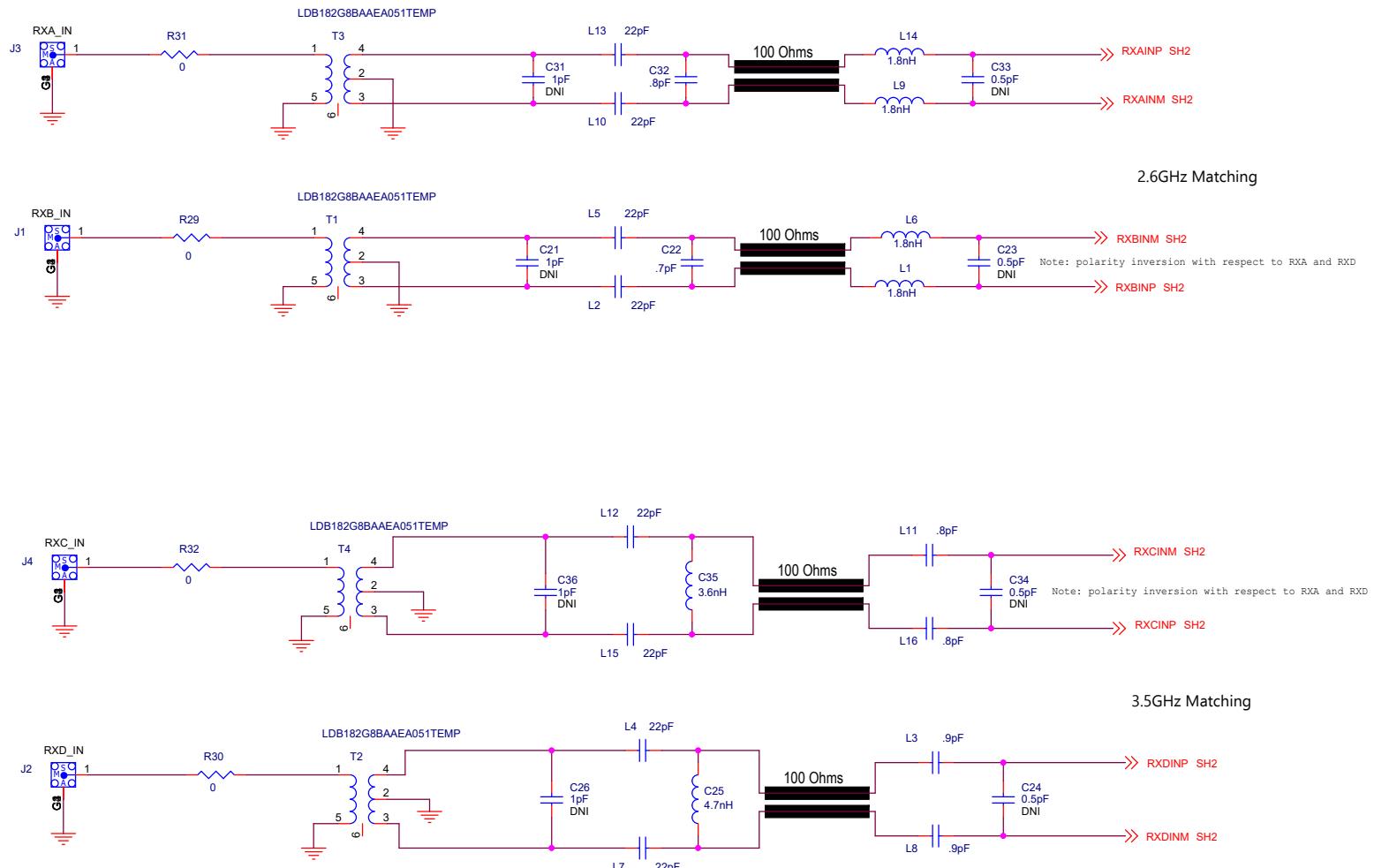
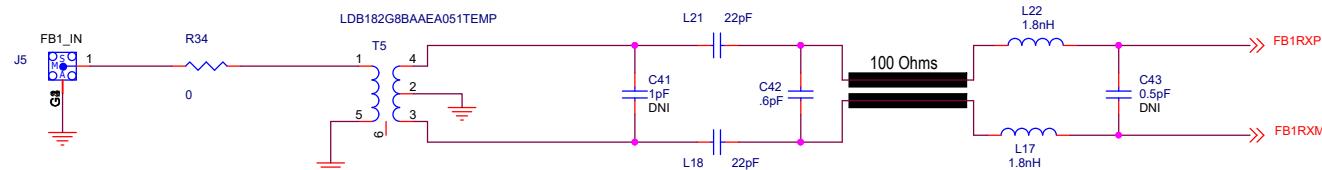


| Title         |                            |               |
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| BLOCK DIAGRAM |                            |               |
| Size          | Document Number            | Rev           |
| B             | DC101_AFE7920EVM           | A             |
| Approved:     | SRINIVAS 08/2019           |               |
| Date:         | Monday, September 23, 2019 | Sheet 1 of 32 |

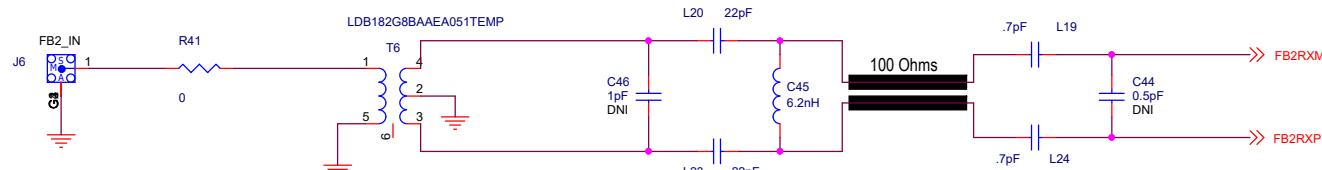


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|                                  |                 | <b>TEXAS INSTRUMENTS</b> |         |
| Title                            |                 | ADC                      |         |
| Size<br>B                        | Document Number | DC101_AFE7920EVM         |         |
| Date: Wednesday, August 28, 2019 |                 | Sheet                    | 2 of 32 |
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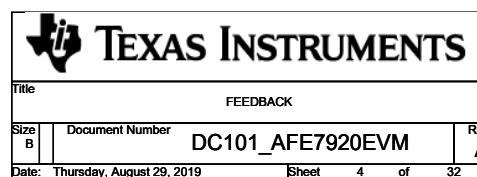


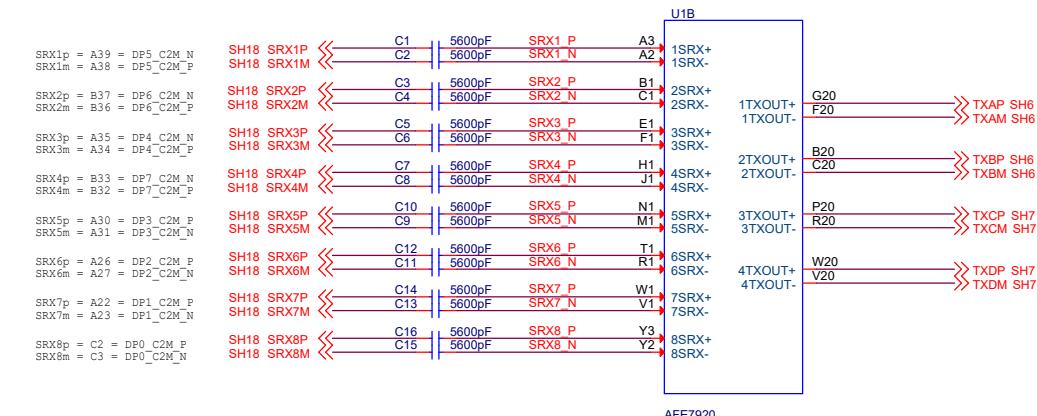
2.6GHz Matching



3.5GHz Matching

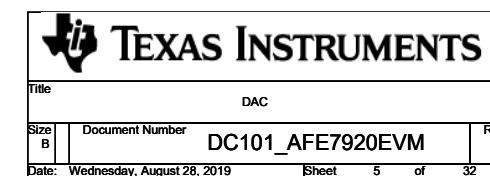
Note: polarity inversion with respect to FB1

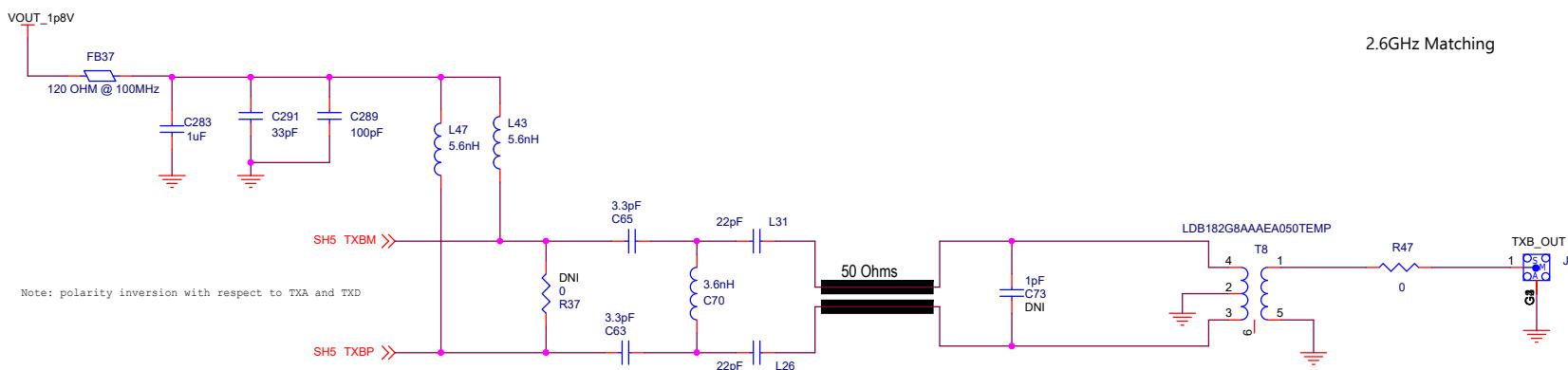
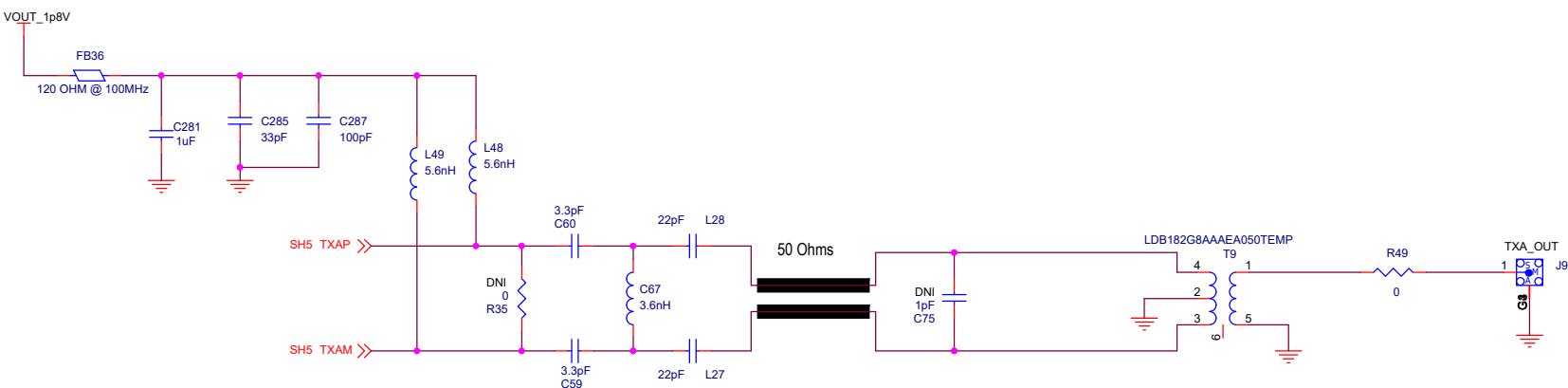




M = Mezzanine Board or AFE79xx Board  
C = Carrier Board or FPGA/ASIC Board  
M2C = Mezzanine to Carrier

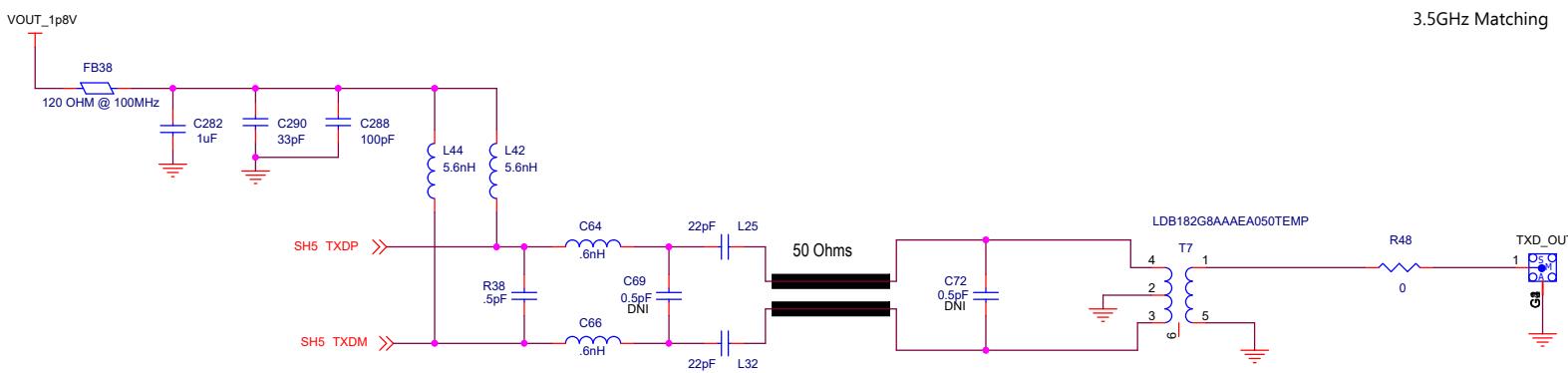
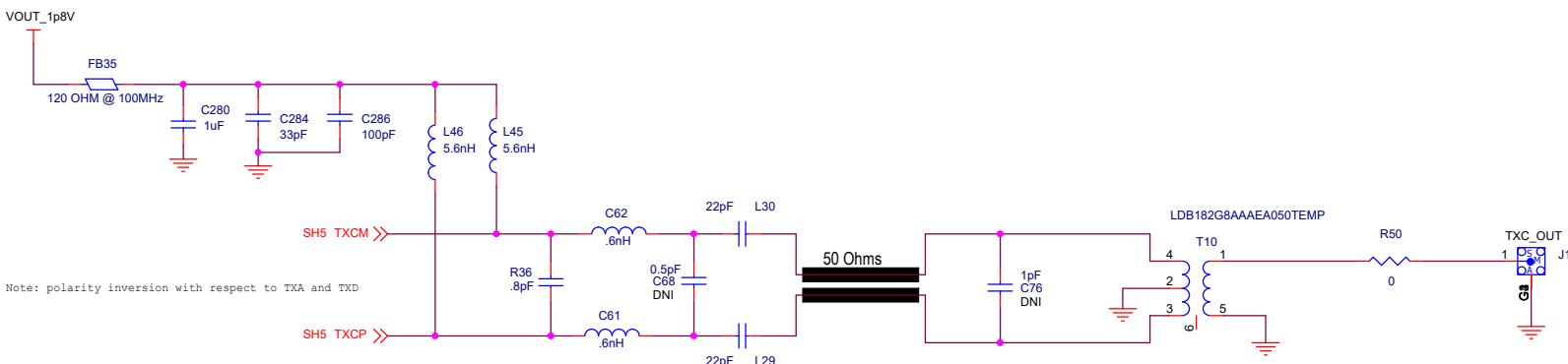
AFE7920

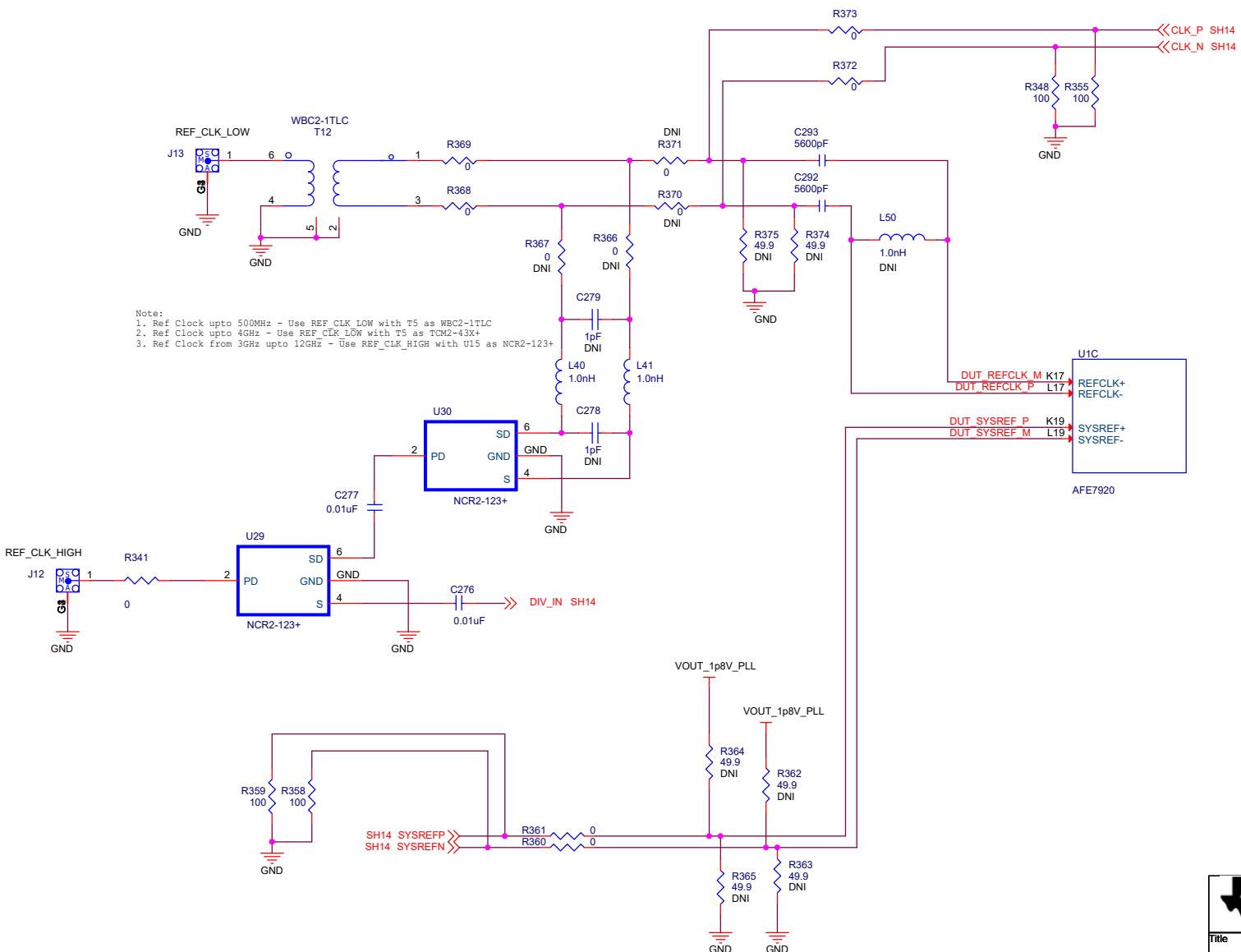


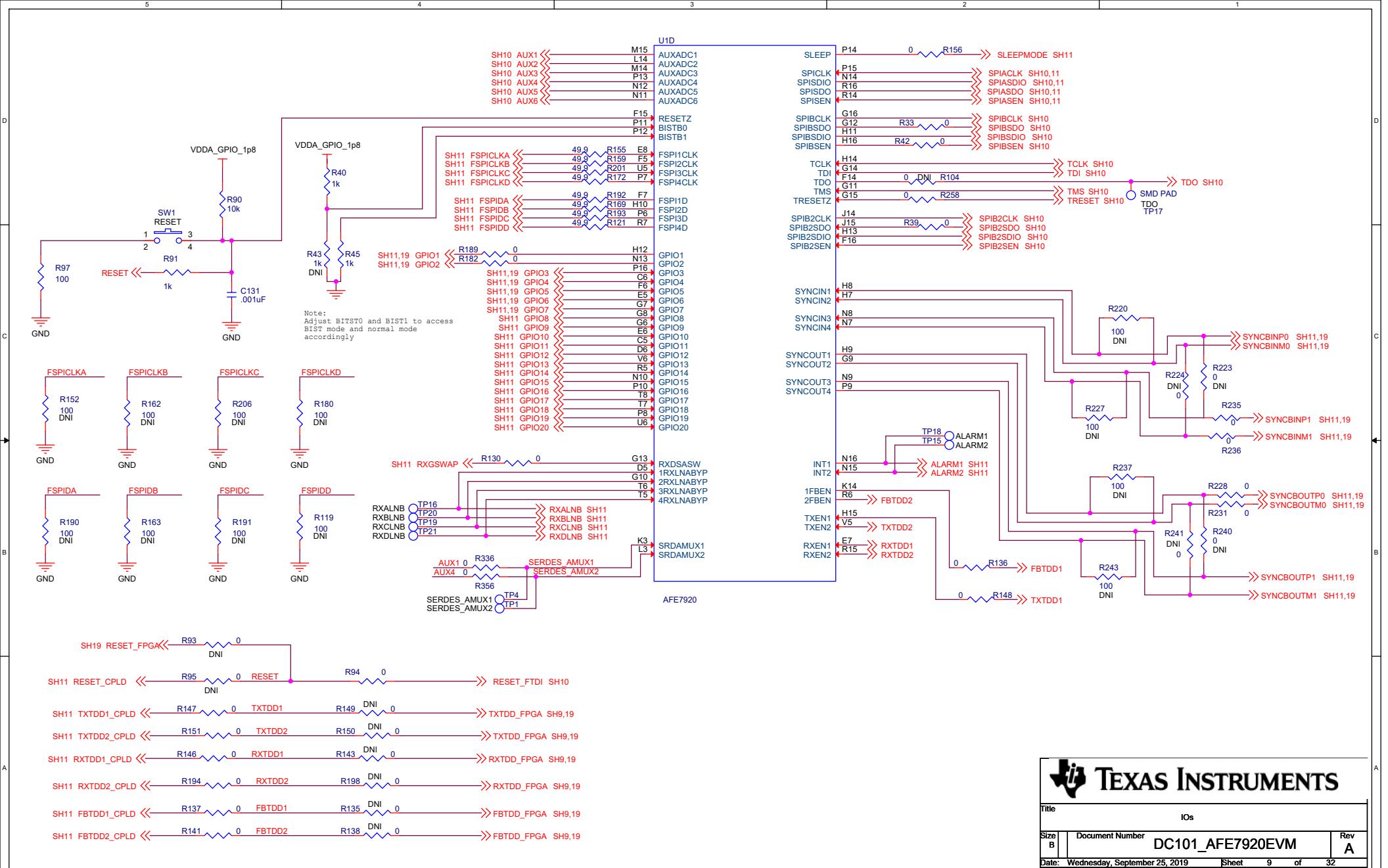


| Title     |                  |     |
|-----------|------------------|-----|
| TXA & TXB |                  |     |
| Size      | Document Number  | Rev |
| B         | DC101_AFE7920EVM | A   |

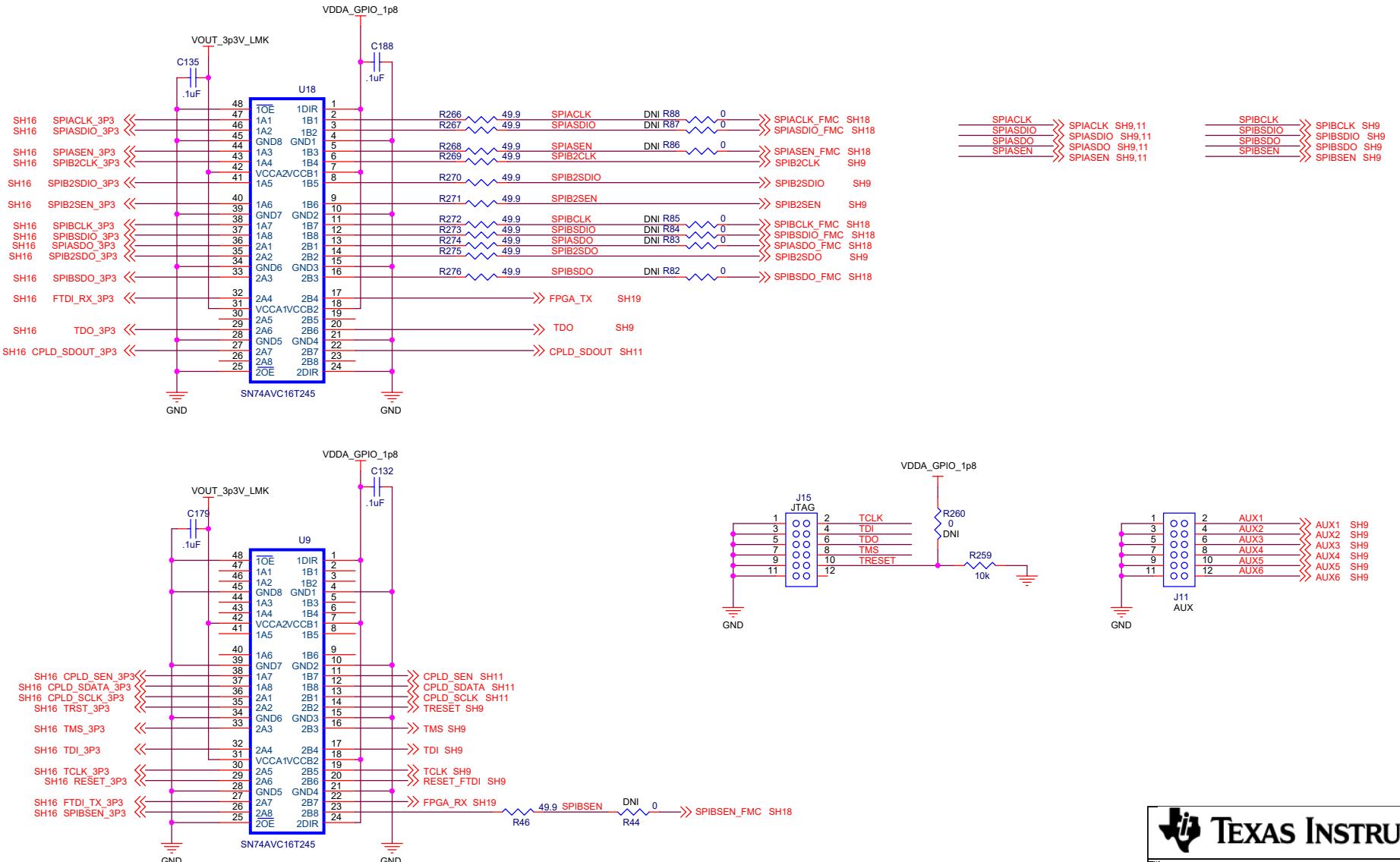
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| Title                               |                  | IOs |
|-------------------------------------|------------------|-----|
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| B                                   | DC101_AFE7920EVM | A   |
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**Title**

LEVEL SHIFTERS

Size

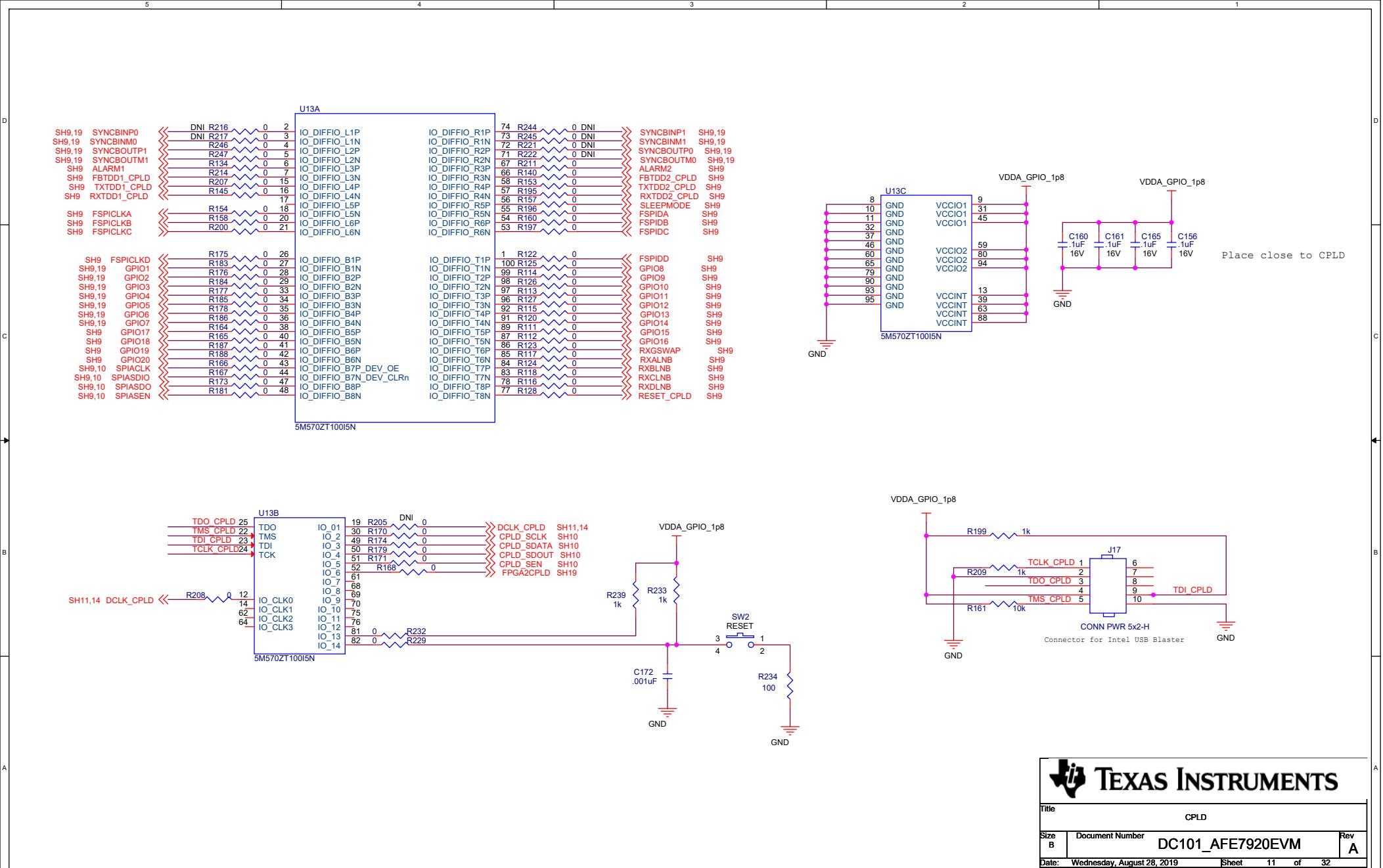
B DC101\_AFE7920EVM Date: Thursday, August 20, 2010 Sheet 10 of 32

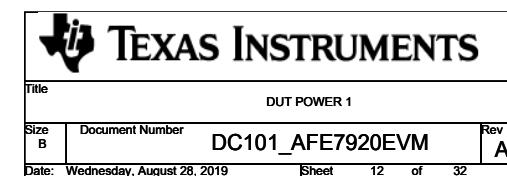
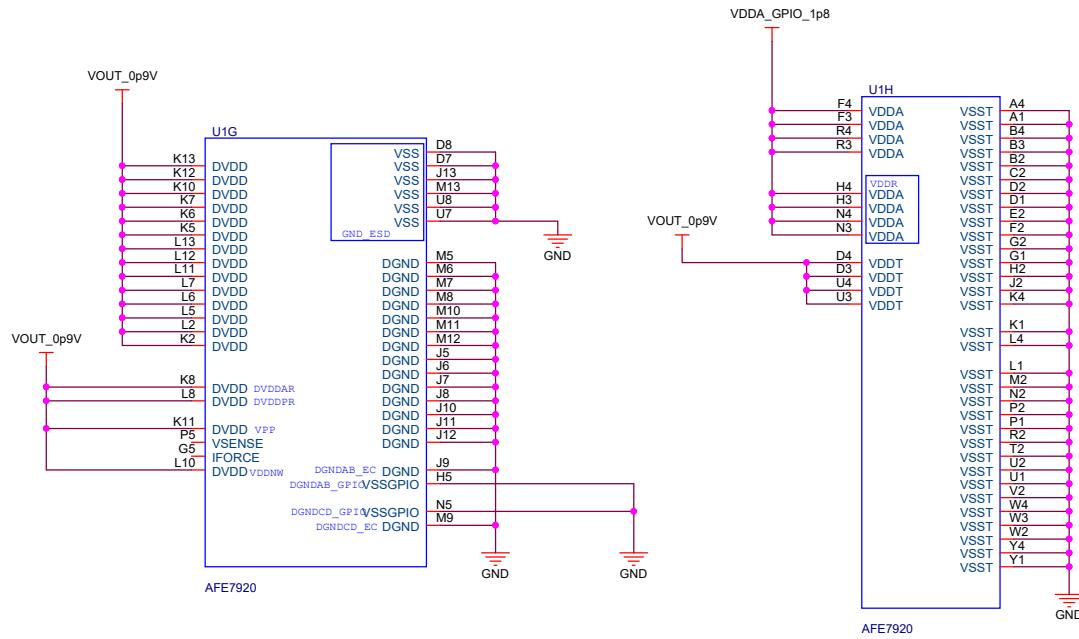
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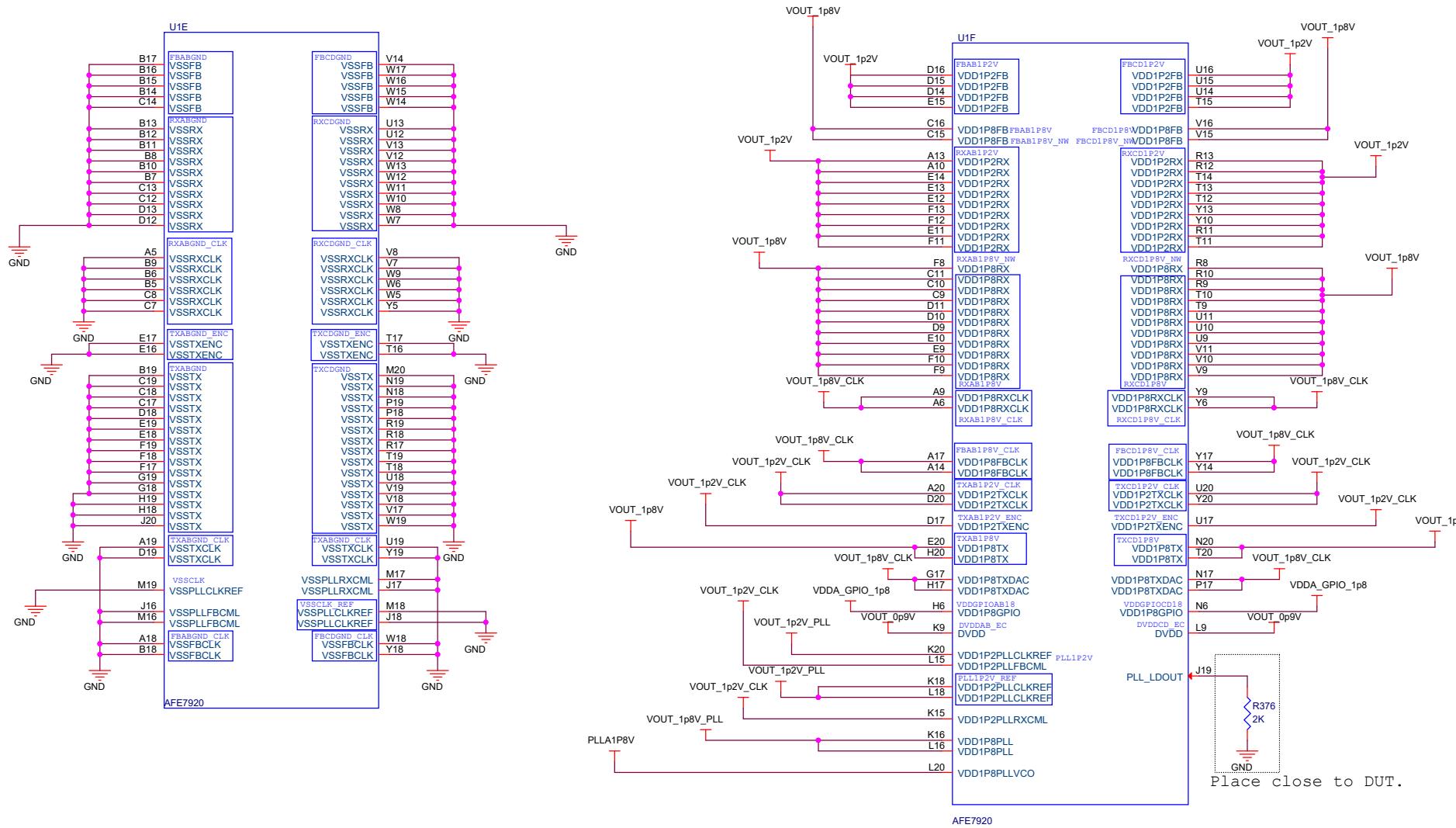
Sheet 10 of 32

Table 1. Summary of the main characteristics of the four groups of patients.

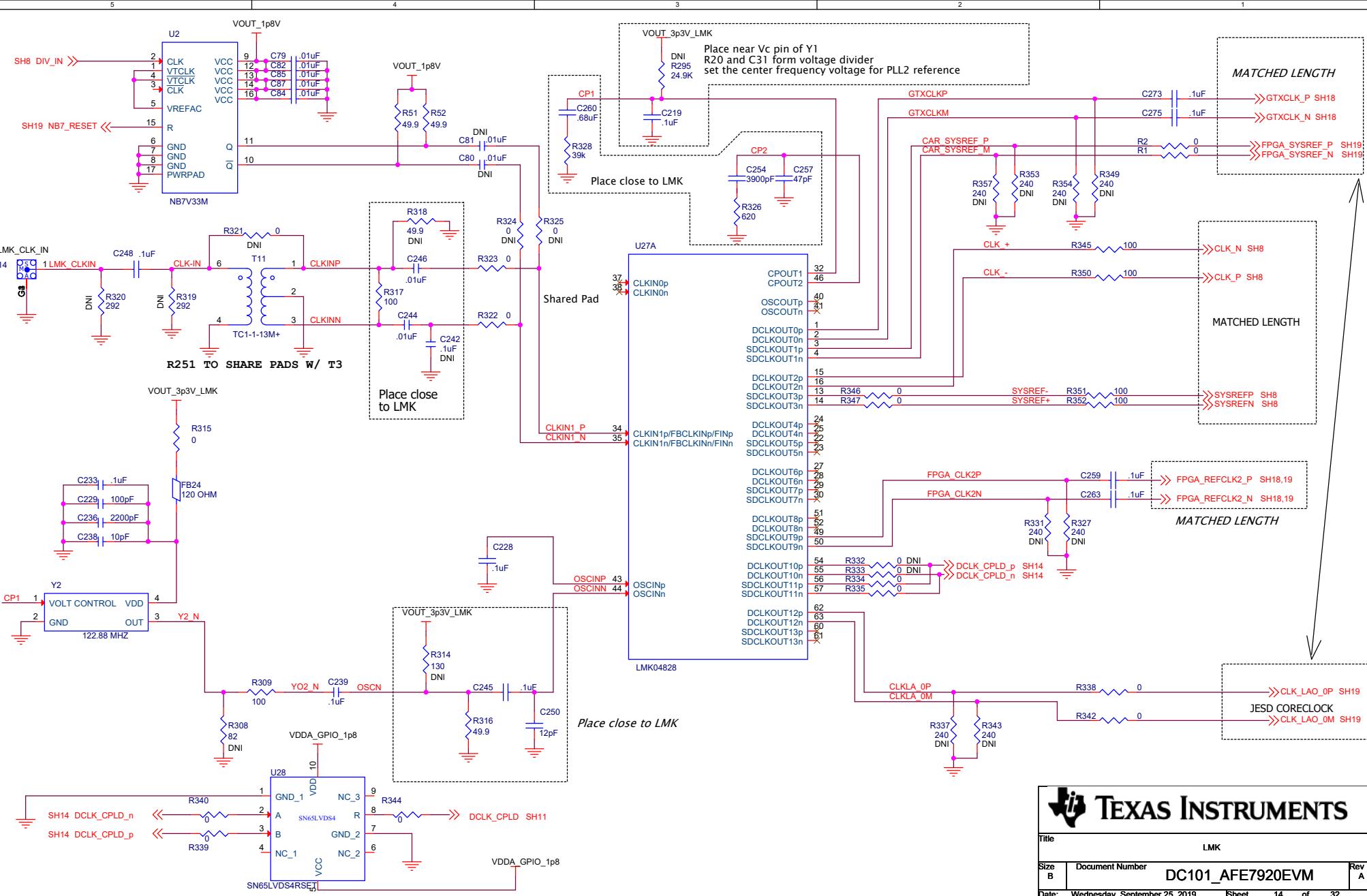
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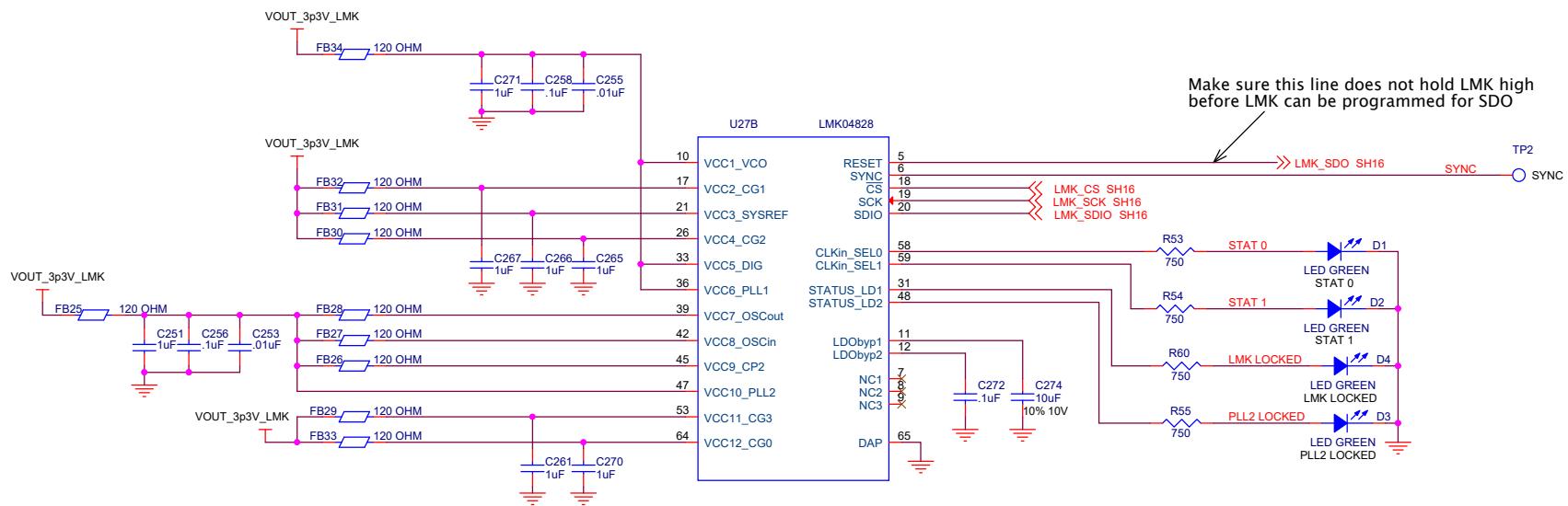


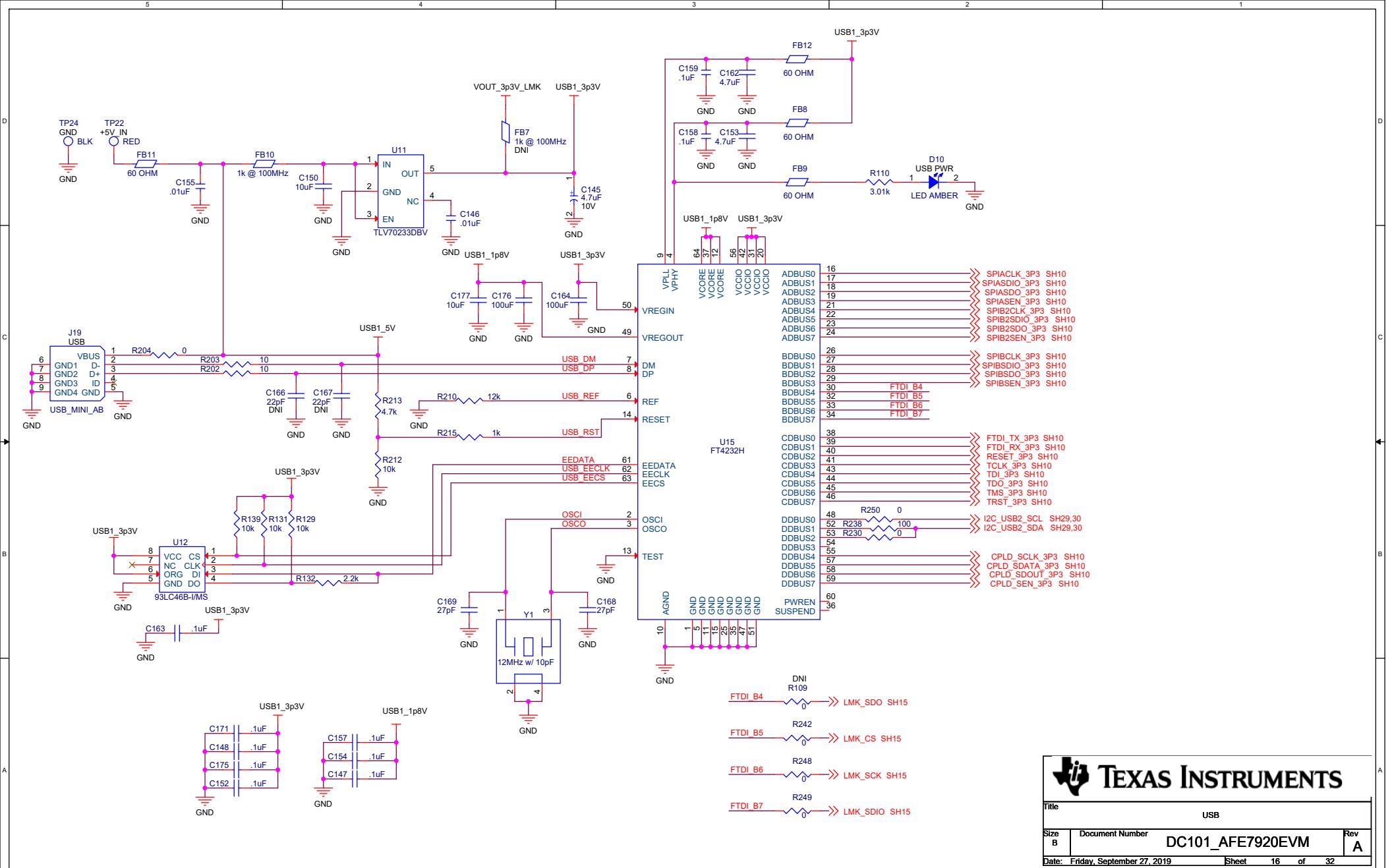




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| Title  |                            | DUT POWER 1      |    |       |
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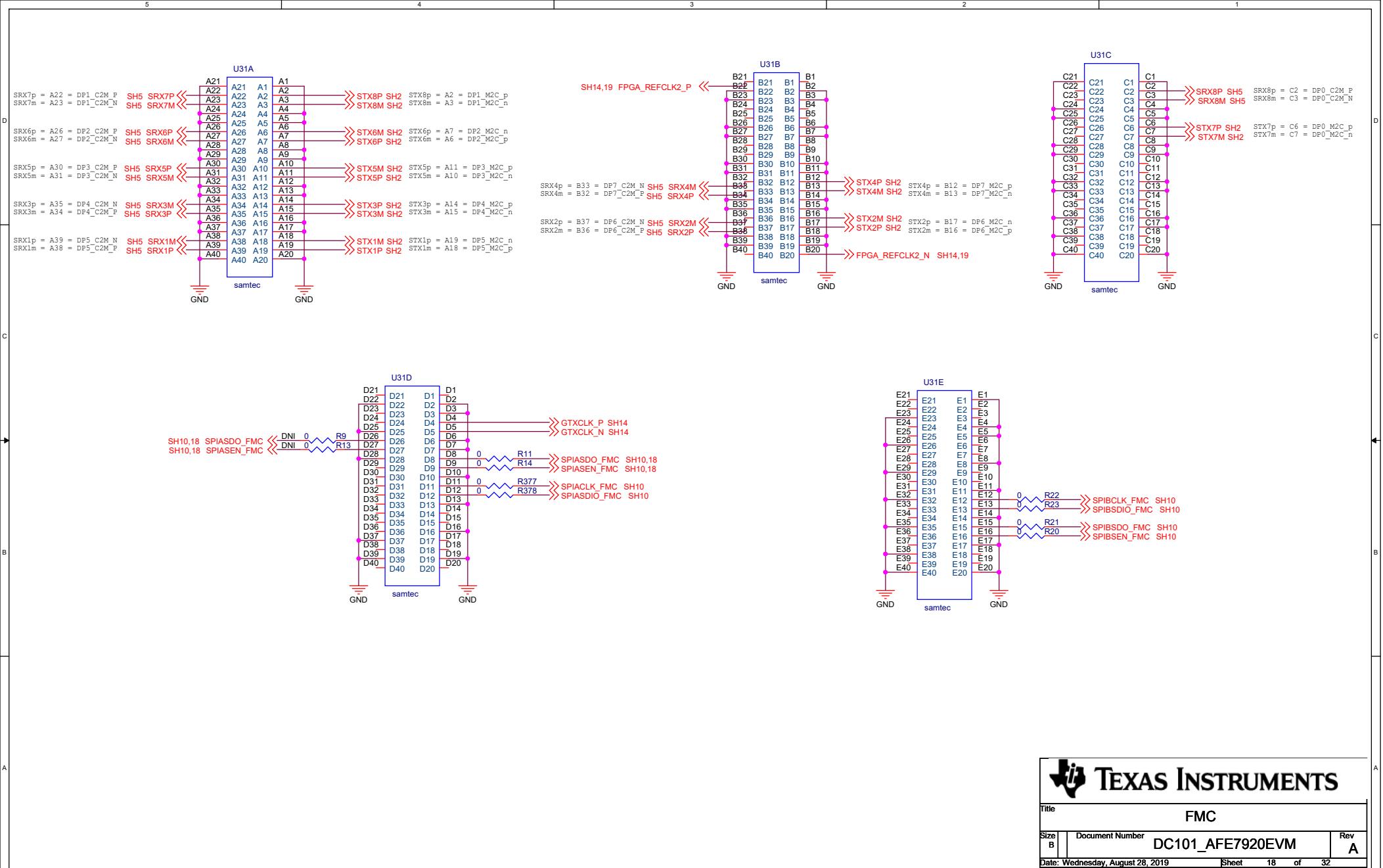




| Title                            |  |
|----------------------------------|--|
| USB                              |  |
| Size B                           | Document Number DC101_AFE7920EVM Rev A |
| Date: Friday, September 27, 2019 | Sheet 16 of 32                         |

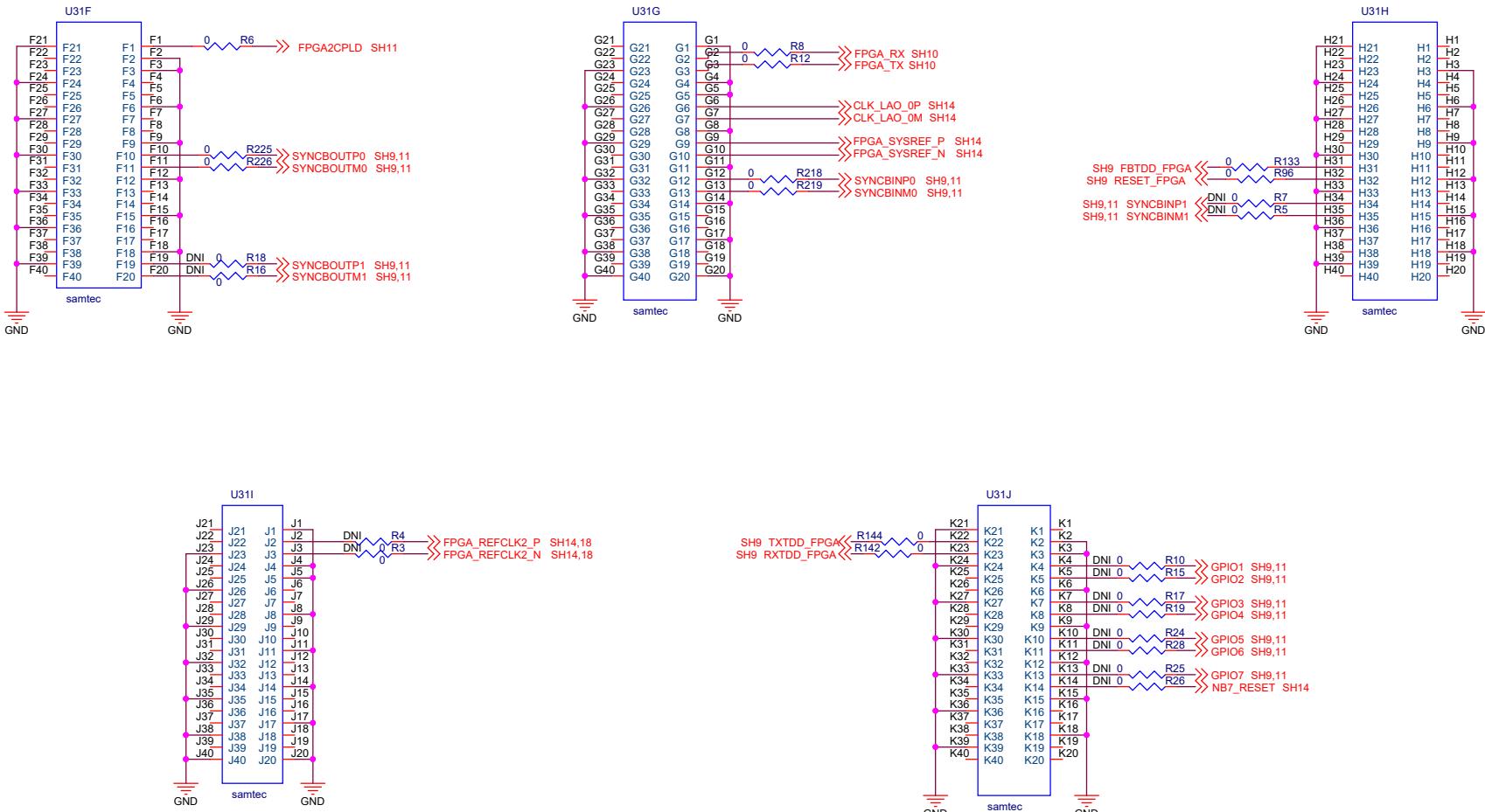


Place close to DUT.

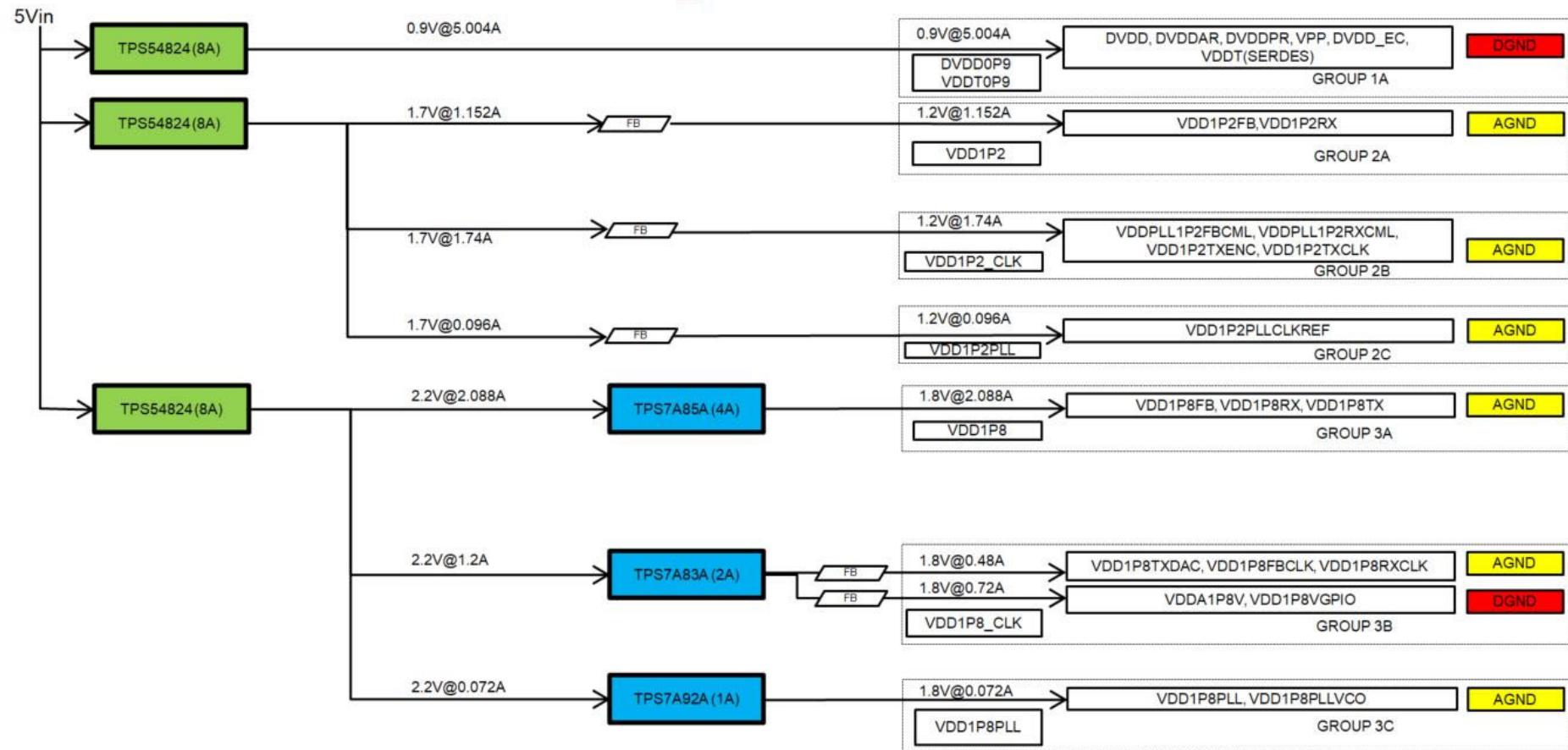


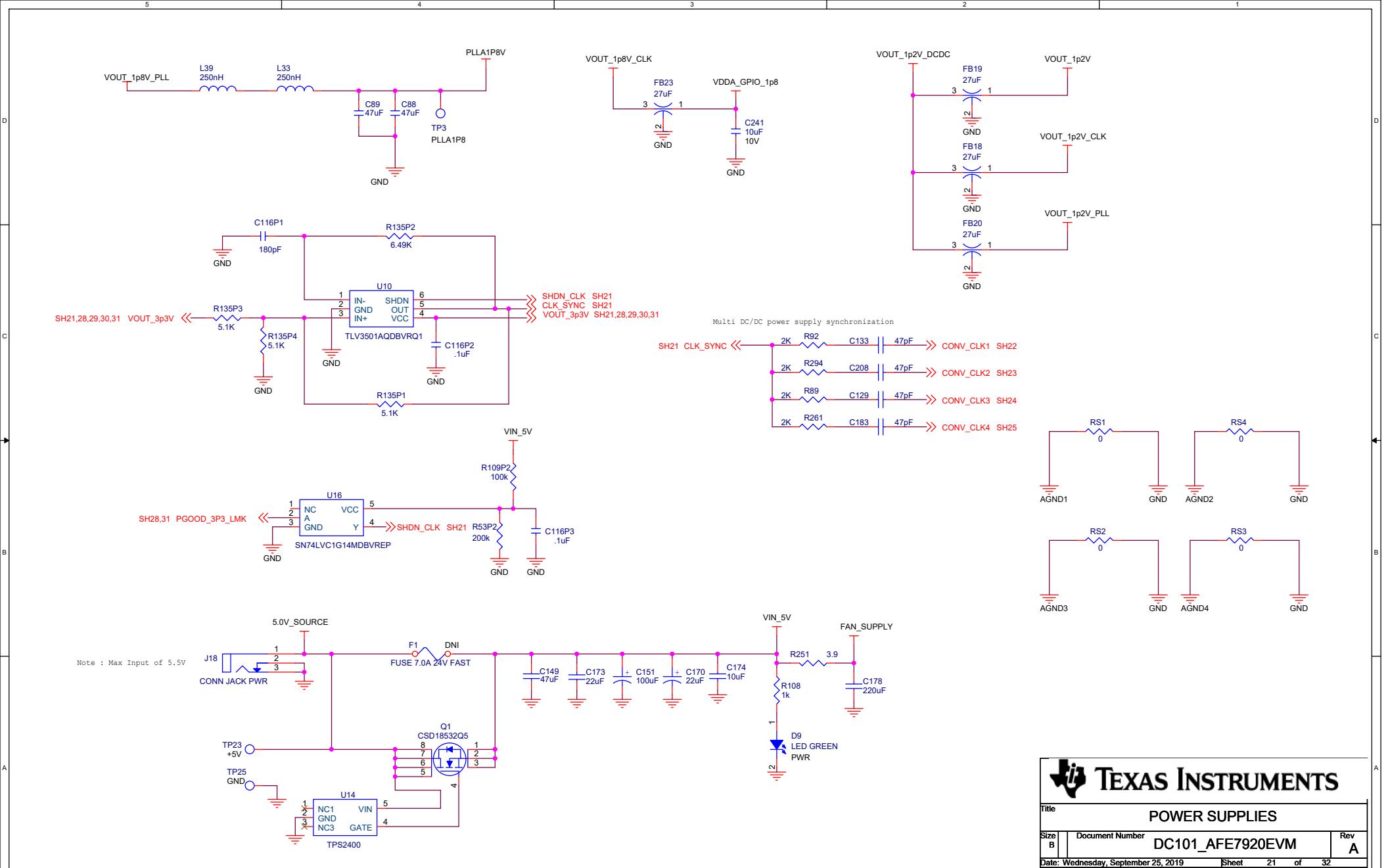
**TEXAS INSTRUMENTS**

| Title |                                  | FMC              | Rev |
|-------|----------------------------------|------------------|-----|
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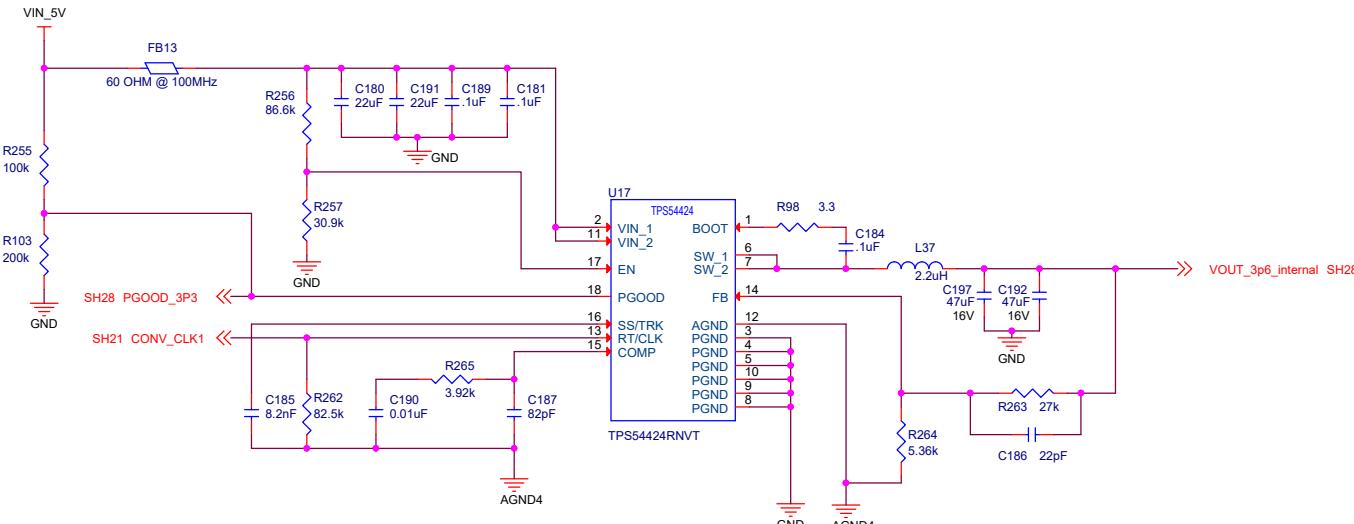


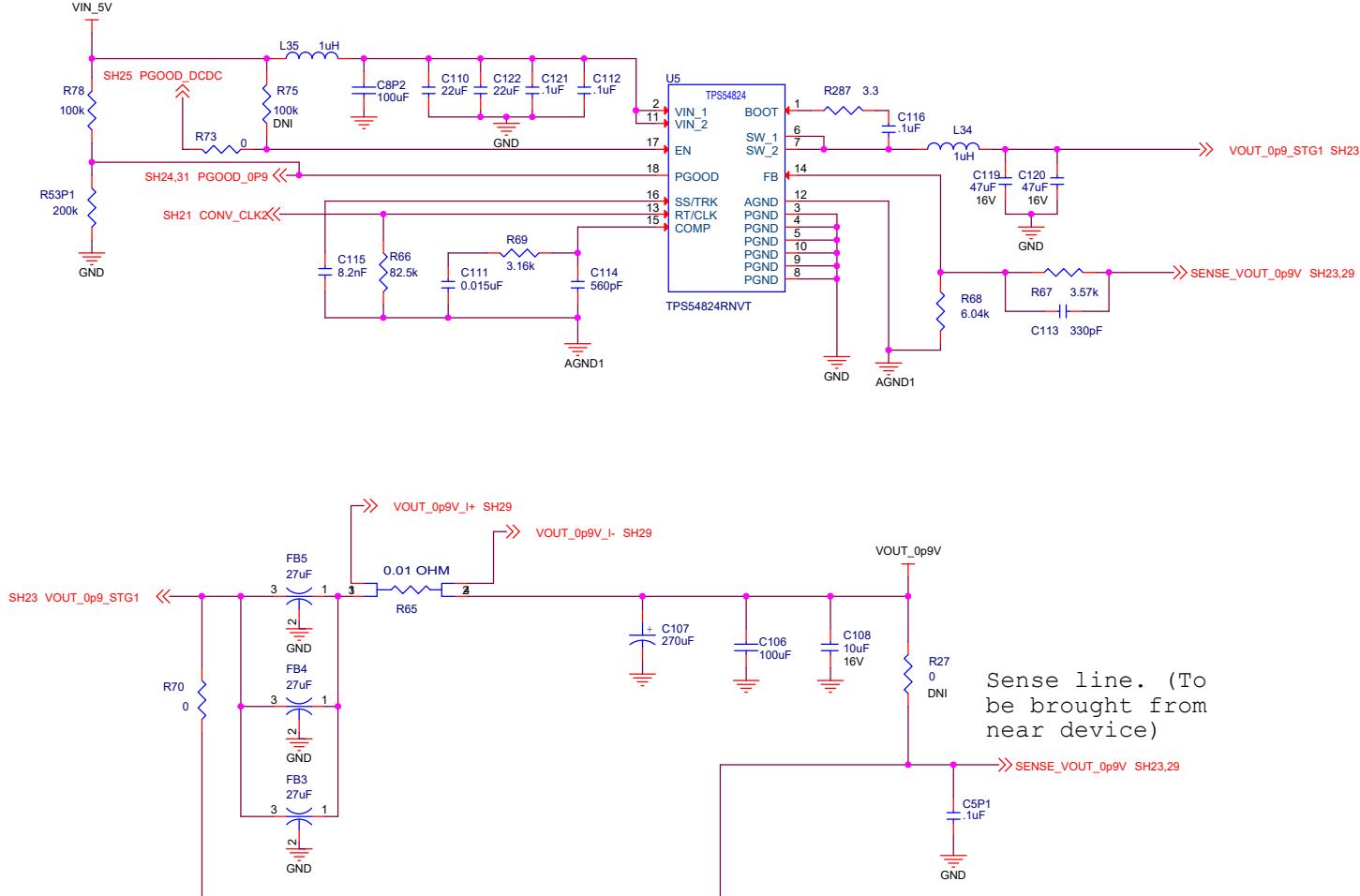
# 1x AFE78xx Block Diagram

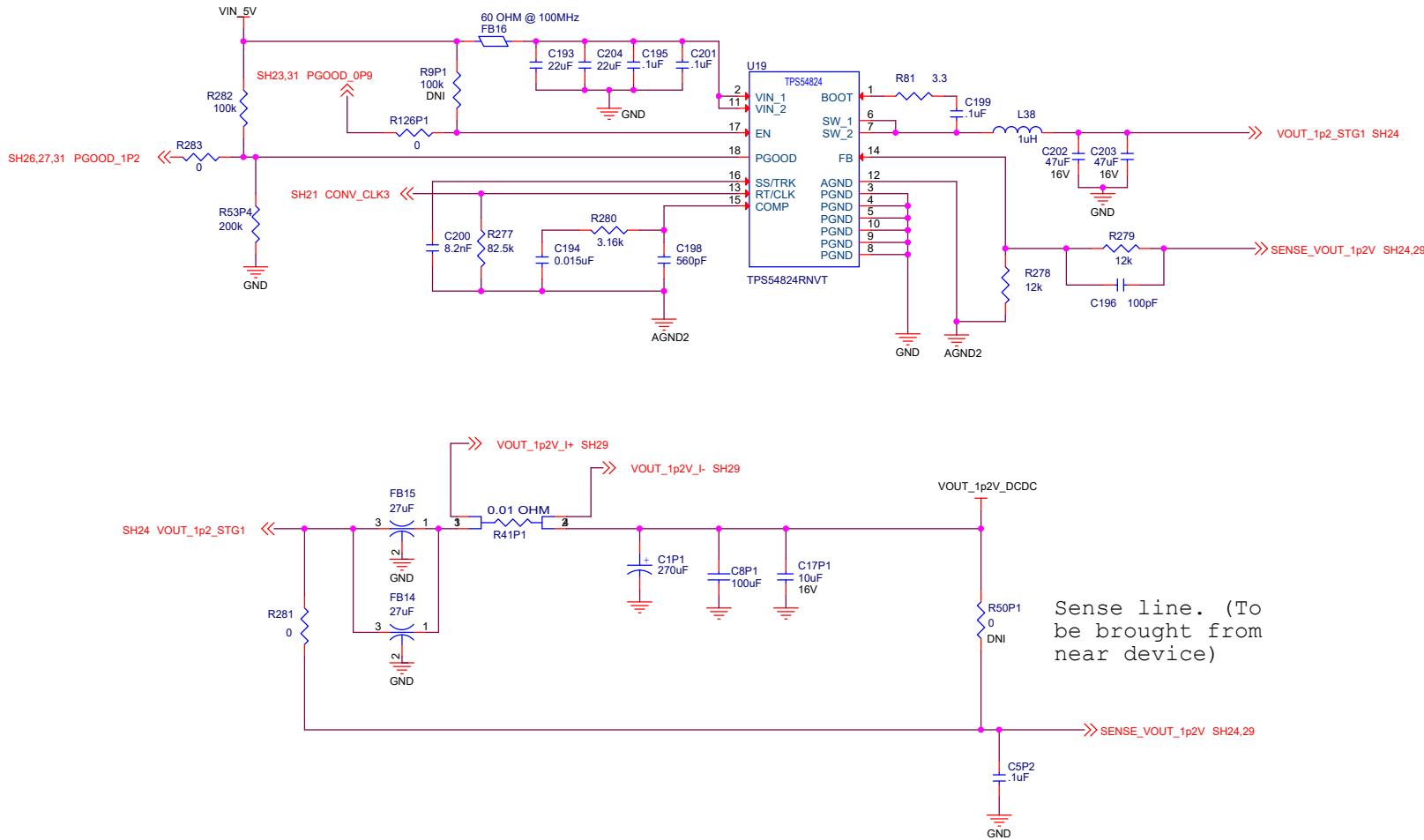




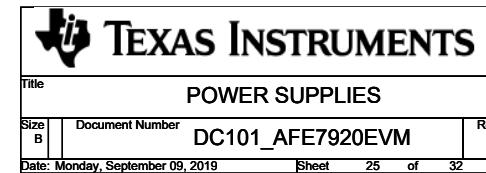
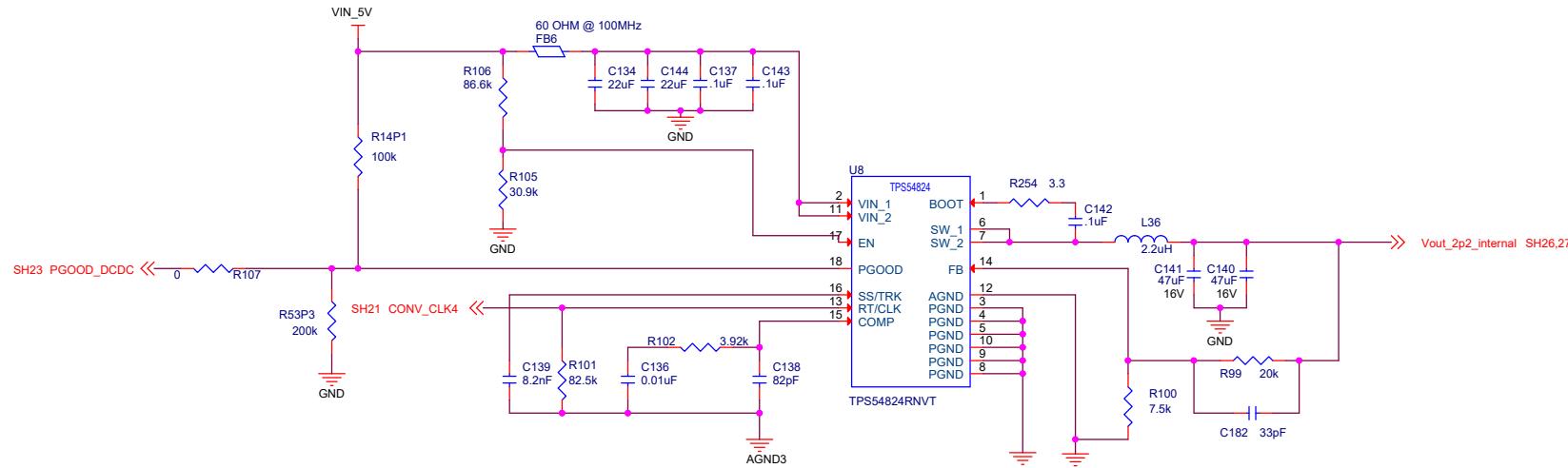
| POWER SUPPLIES                      |                  |     |
|-------------------------------------|------------------|-----|
| Size                                | Document Number  | Rev |
| B                                   | DC101_AFE7920EVM | A   |
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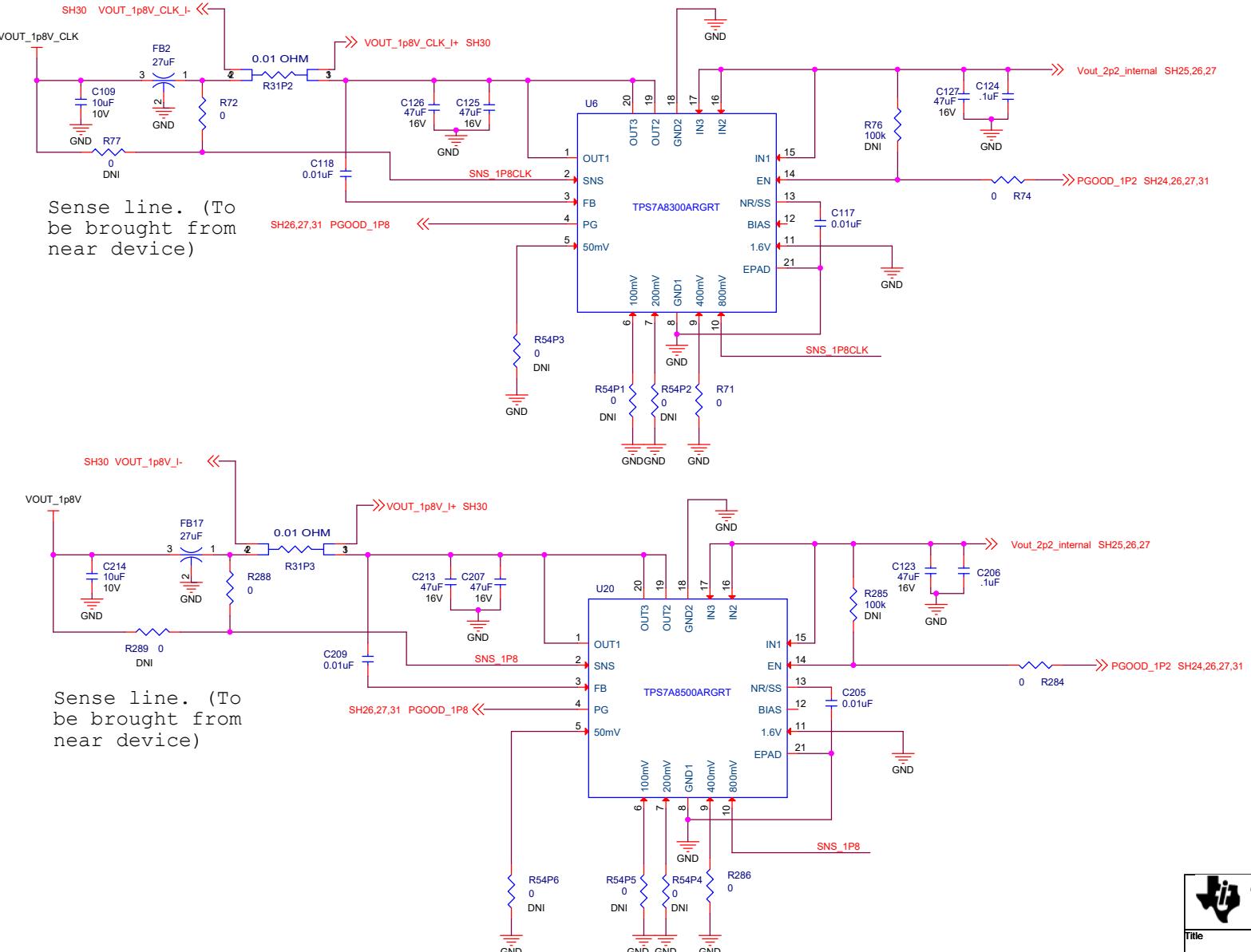




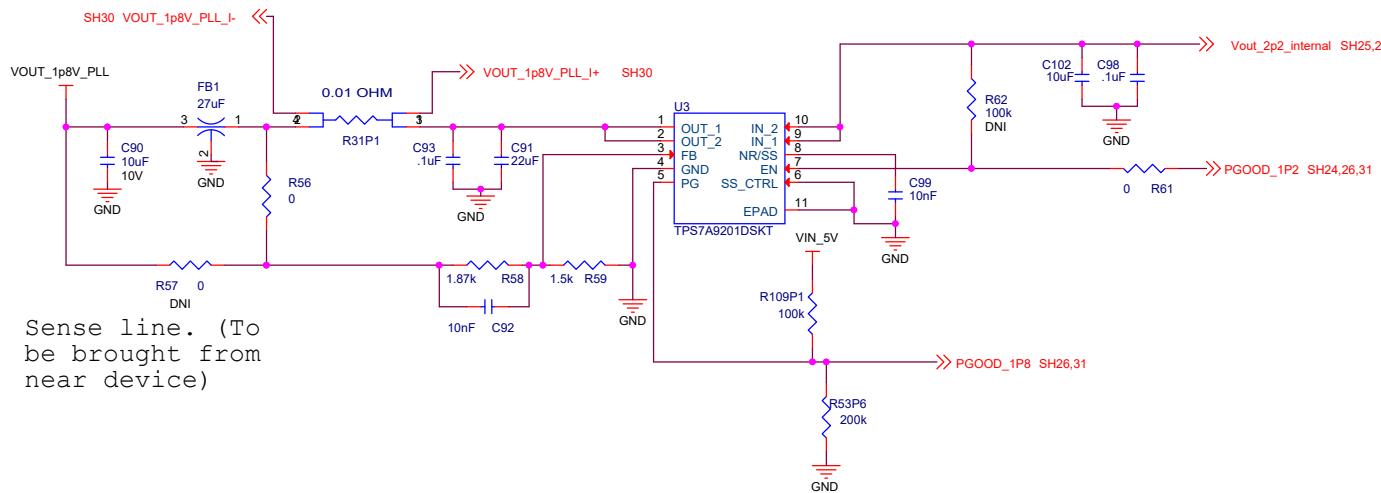


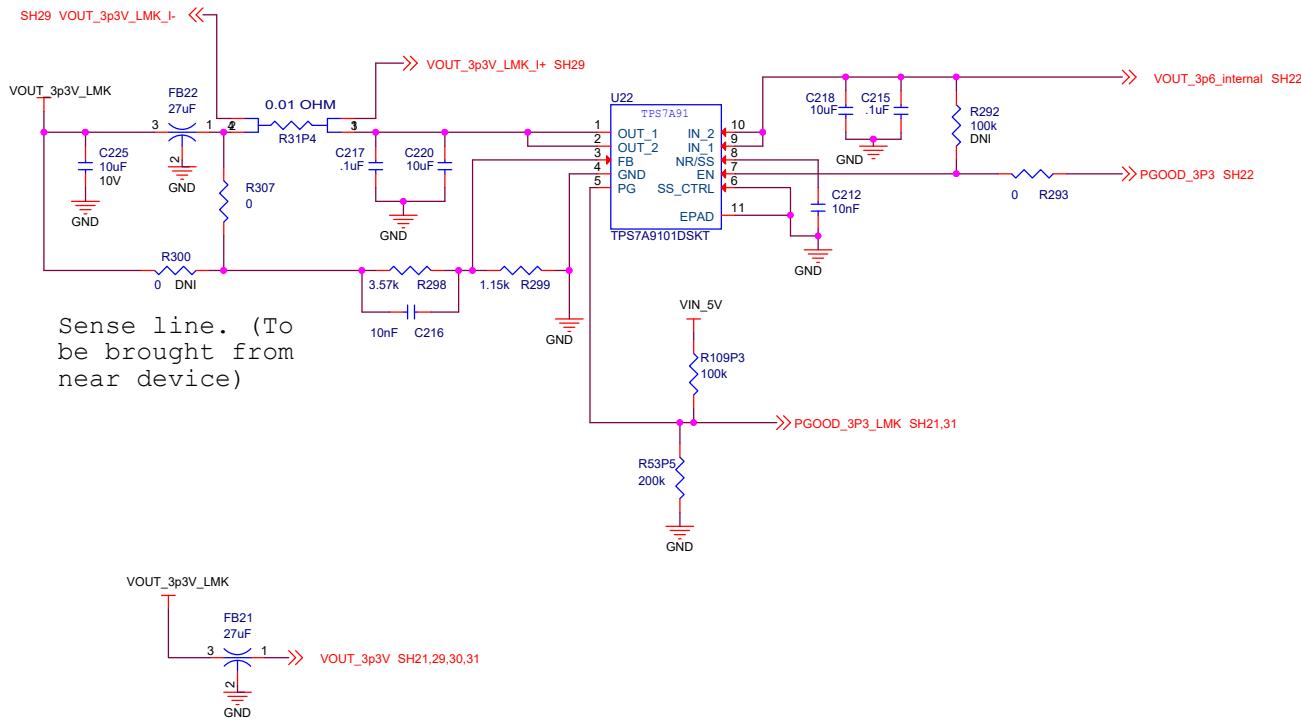
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| Size<br>B                        | Document Number | DC101_AFE7920EVM |    | Rev<br>A |
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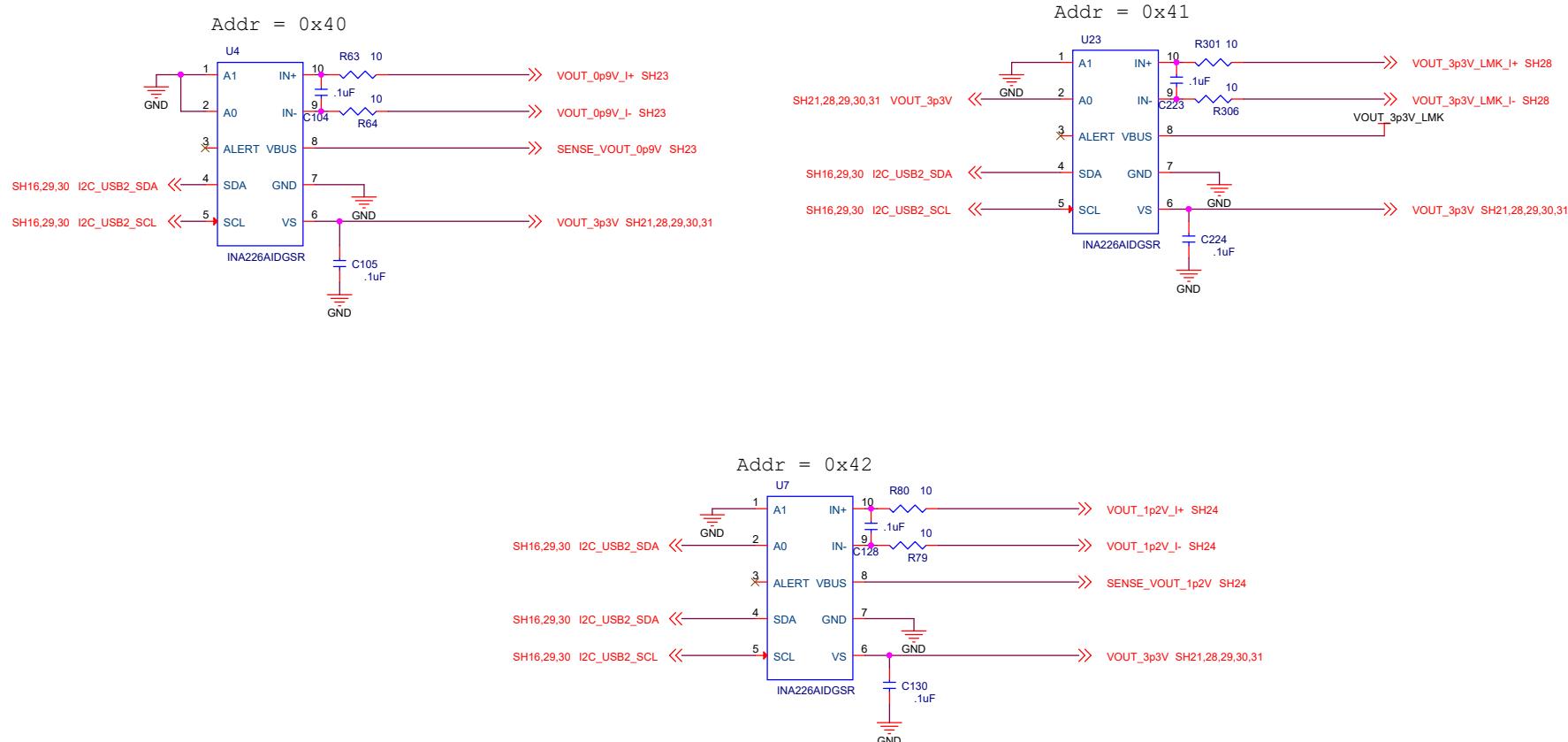


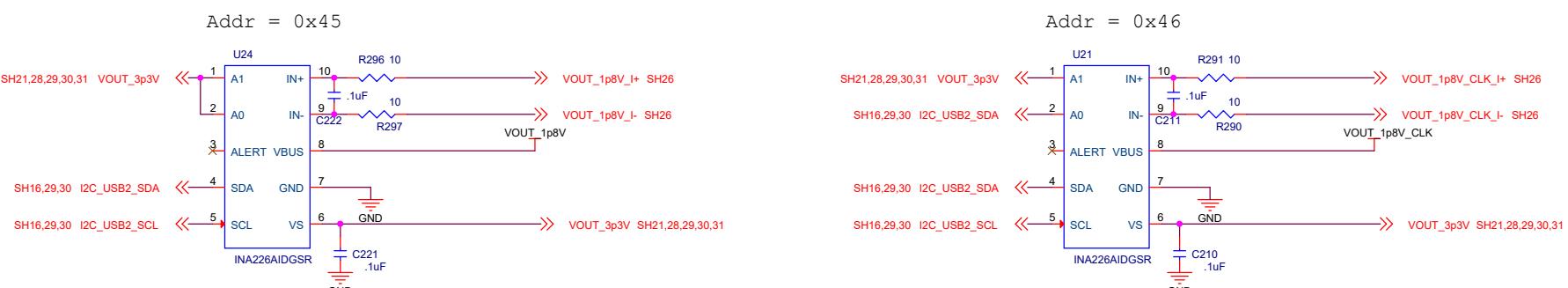
| POWER SUPPLIES                                  |                  |     |
|---|------------------|-----|
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| B   | DC101_AFE7920EVM | A   |
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|                                  |                 |                  |    |       |
|----------------------------------|-----------------|------------------|----|-------|
| Title                            |                 | POWER SUPPLIES   |    |       |
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All VBUS to be connected to sense lines of LDO

