Mercury+ KX2 FPGA Module

Known Issues and Changes

Revision 2

Purpose

The purpose of this document is to describe the known issues and the revision changes related to the Mercury+ KX2 FPGA module.

Product Information	Number	Revision	Name
Product	ME-KX2	Mercury+ KX2 R2	Mercury+ KX2 FPGA Module

Document Information	Reference	Version	Date	Author
Reference / Version / Date	D-0000-435-001	01	23.02.2017	DIUN

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1   Known Issues

In addition to the erratas from the components manufacturers, Enclustra is aware of the following issues with the Mercury+ KX2 R2 module.

Limited FTDI device functionality	
Description	The functionality of the FTDI device present on the Mercury+ KX2 FPGA module is limited. When using the synchronous FIFO interface, in certain temperature conditions, the timing path requirement between FPGA and FTDI device is not met.
Workaround	-
Status	This issue will be fixed in the next revision.

2   Functional Changes

The following functional changes were made from the Mercury+ KX2 FPGA module revision R1 to R2.

DDR supply voltage selection circuit	
Description	The DDR supply voltage selection circuit is now available on the Mercury+ KX2 FPGA module. Refer to the Mercury+ KX2 FPGA module user manual for more information.

FTDI functional changes	
Description	The functionality of the FTDI device has changed in the matter of available modes and control pins. UART mode is now available. Please refer to the Mercury+ KX2 FPGA module user manual for a full description of the FTDI operation.