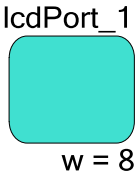
LCD Port
1.10

Features

- Common and segment selection
- Variable widths



General Description

The LCD port provides configuration as either common or segment for the appropriate I/O(s). The port allows for the creation of per-pin aliases, which may be viewed in the PSoC Creator Pin Editor and used in the generated port APIs.

When to use a Port

Use a port when a design needs to generate or access an off-device signal. Use an appropriate port for the type of signal being accessed. LCD ports are optimized for their specific application.

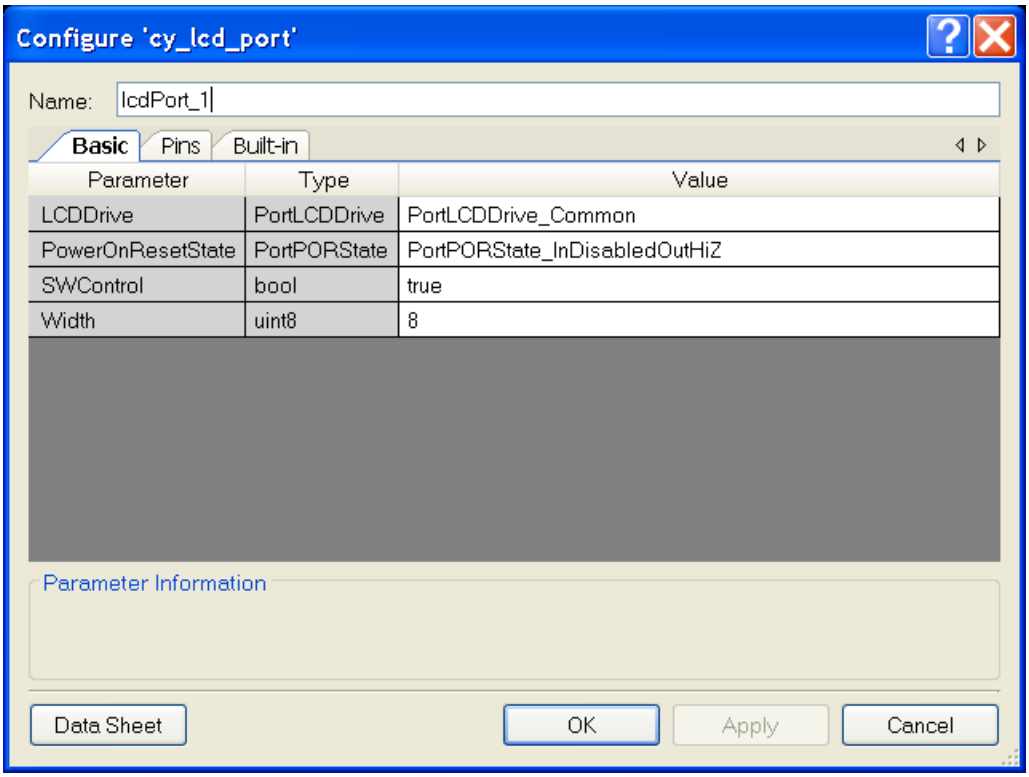
Input/Output Connections

The LCD port does not have any connections.

Component Parameters

Drag a Port onto your design and double-click it to open the Configure dialog.

Basic Tab



LCDDrive

This parameter configures all pins in the port as either common or segment drive pins.

PowerOnResetState

This parameter specifies the power on reset state of the port. Legal values include:

- **InDisabledOutHiZ** (default) – the input disabled the output is Hi-Z
- InEnabledOut1 – input enabled and the output is a logic one
- InEnabledOut0 – input enabled and the output is a logic zero
- InEnabledOutHiZ – input enabled and the output is Hi-Z

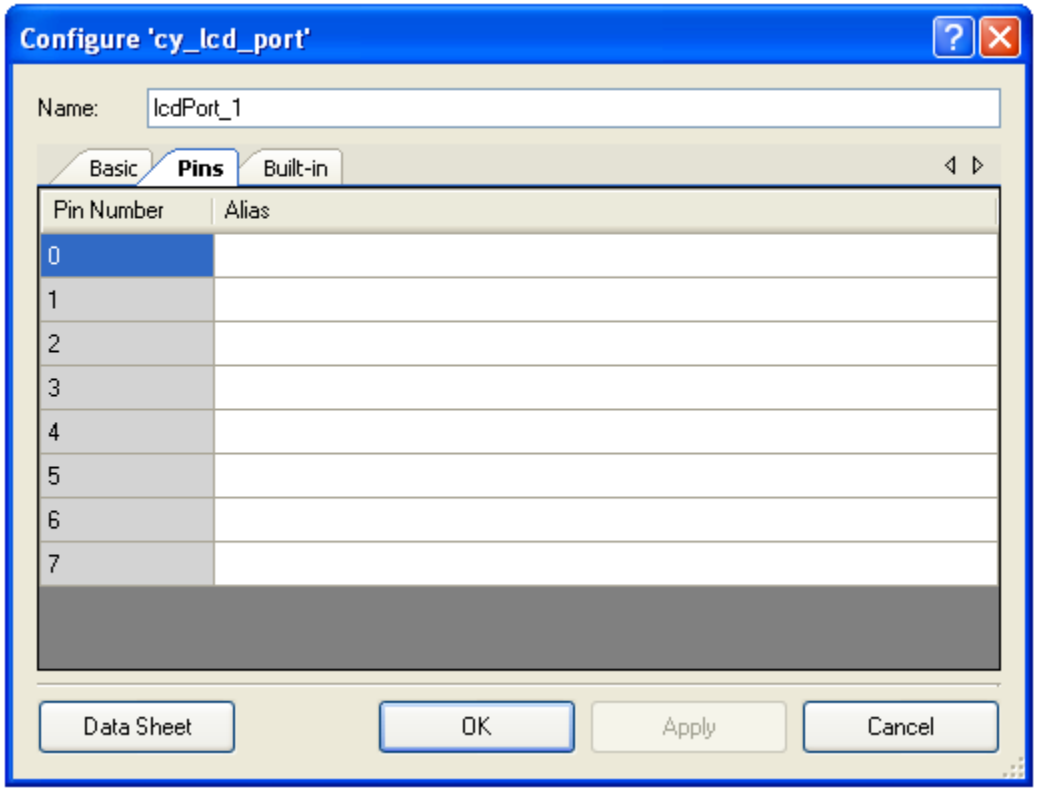
SWControl

Specifies whether the LCD Port will be controlled via software or the built-in hardware of the device.

Width

Specifies the width in bits of the logical port (default is 8).

Pins Tab



Alias

This parameter assigns an alias for each pin in the port. The alias is presented in the pin editor and in the generated APIs for the port.

Resources

All ports consume one physical pin, per bit of their width parameter.

Application Programming Interface

Not applicable.

DC and AC Electrical Characteristics

The following values are indicative of expected performance and based on initial characterization data.

5.0V/3.3V DC and AC Electrical Characteristics

Parameter	Typical	Min	Max	Units	Conditions and Notes
Input					
Input Voltage Range	---		Vss to Vdd	V	
Input Capacitance	---		---	pF	
Input Impedance	---		---	Ω	
Maximum Clock Rate	---		67	MHz	