Dr. Hsiang-Huang Wu Institute of Information Science, Academia Sinica Taipei, Taiwan

December 16, 2015

CREDIT Center at Prairie View A & M University

To whom it may concern,

I am applying for Post Doctoral Researcher because the vacancy matches my research interests pretty well.

I have very strong background of hardware design and testing in RTL because I have worked in the industry for tow years. Also, I have been the TA for the class Modern Digital System Design to teach the students how to prototype an application with the Xilinx FPGA. Beyond that, I am very interested in how high performance computing/parallel computing with multicore processors and GPUs help to realize an efficient application. It motivates me to study the theoretic foundation of the applications to know how to implement them elaborately. My current research centers around an establishment of a good match between machine learning applications and Cloud Computing, especially Hadoop MapReduce. Now, I am well prepared to develop the heterogeneous computing incorporating FPGA, multicore processors and GPUs into a software framework (e.g. hadoop MapReduce).

You will see after reviewing the enclosed resume. I have made numerous contributions to my advisers and employers, especially the parts of the implementations. Due to the rise of machine learning applications, I believe the research of machine learning onto the heterogeneous computing is going to play the critical role and I am ready for its coming.

I am confident that I am qualified for this position. Thank you for your time – I look forward to learning more about this opportunity!

Sincerely yours,

Hsiang-Huang Wu