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Computer Architecture
Homework 3

1)

a) $39 = (100111)_2$

$39 - 32 = 7 - 4 = 3 - 2 = 1 - 1 = 0$
 $2^5 \quad 2^2 \quad 2^1 \quad 2^0$

b) $(BA)_{16} = (186)_{10}$

$11 \times 16^1 + 10 \times 16^0$
 $176 + 10$

c) $(1010101)_2 = (55)_{16}$

$0101 \quad 0101$
 $5 \quad 5$

d) $(1011)_2$ 4-bit unsigned $= (11)_{10}$
 $2^3 + 2^1 + 2^0$
 $8 + 2 + 1$

e) $-4 = (1100)_2$ 4-bit signed 12's complement

$-8 + \underline{\quad} = -4$
 $-8 + 4 = -4$

2)

a) 4-bit unsigned: $14 + 6 = 4$

$$\begin{array}{r} 1110 \\ + 0110 \\ \hline 10100 \end{array}$$

b) 4-bit unsigned: $4 - 7 = 13$

$$\begin{array}{r} 0100 \\ + 1001 \\ \hline 1101 \end{array}$$

c) 8-bit unsigned: $236 + 34 = 14$

$236 - 128 = 108 - 64 = 44 - 32 = 12 - 8 = 4 - 4 = 0$
 $2^7 \quad 2^6 \quad 2^5 \quad 2^3 \quad 2^2$

$34 - 32 = 2 - 2 = 0$
 $2^5 \quad 2^1$

0	0	2^0	1
1	1	2^1	2
2	2	2^2	4
3	3	2^3	8
4	4	2^4	16
5	5	2^5	32
6	6	2^6	64
7	7	2^7	128
8	8	2^8	256
A	10	2^9	512
B	11		
C	12		
D	13		
E	14		
F	15		

d) 4-bit signed 12's comp: $6 + 6 = -4$

$$\begin{array}{r} 0110 \\ + 0110 \\ \hline 1100 \end{array}$$

e) 4-bit signed 12's comp: $-7 - 7 = 2$

$$\begin{array}{r} 1001 \\ + 1001 \\ \hline 10010 \end{array}$$

$$\begin{array}{r} 11 \\ 11101100 \\ + 00100010 \\ \hline 100001110 \end{array}$$

 $8 + 4 + 2$

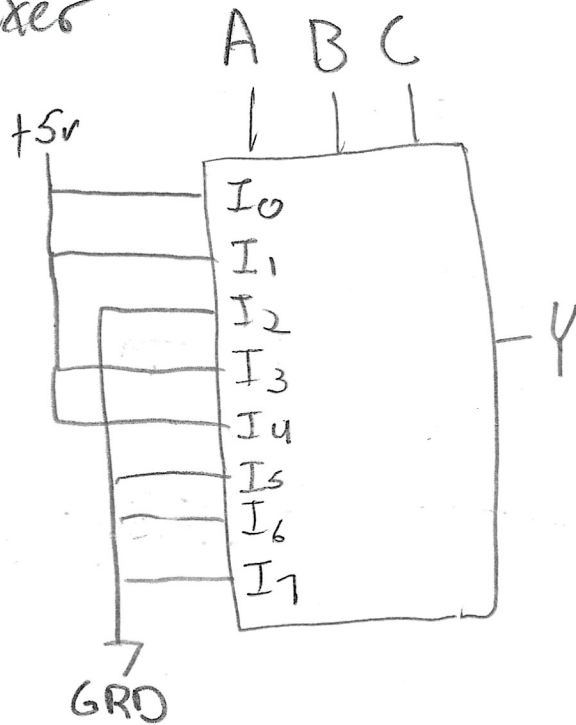
3) Give all the maxterms of the Boolean Function $Y = \overline{A}\overline{B} + \overline{A}B + A\overline{C} + AB + B\overline{C}$

AB \ C	00	01	11	10
0	1	1	0	1
1	1	0	1	1

$$Y = (\overline{A} + \overline{B} + C)(A + \overline{B} + \overline{C})$$

4)

a) 8:1 Multiplexer

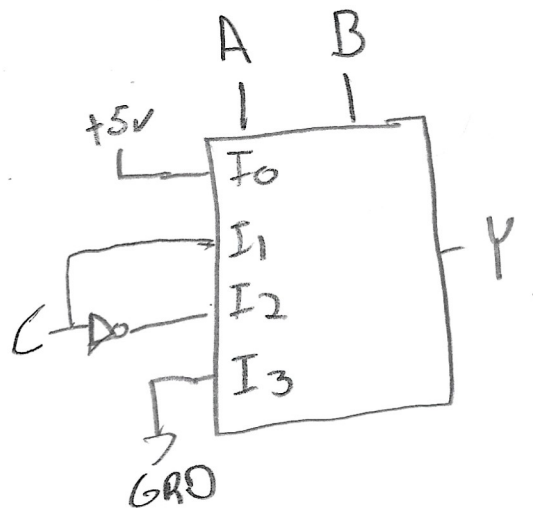


A	B	C	Y
0	0	0	1
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	0

b) 4:1 Multiplexer

AB \ C	00	01	11	10
0	1	0	0	1
1	1	1	0	0

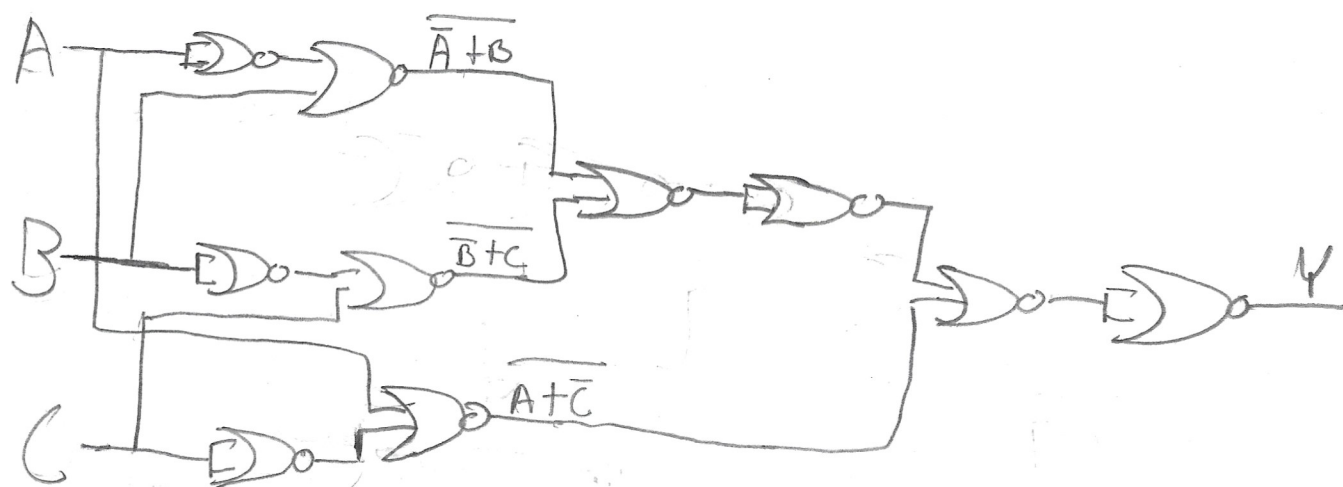
$I_0 = 1$ $I_1 = C$ $I_3 = 0$ $I_2 = \overline{C}$



5)

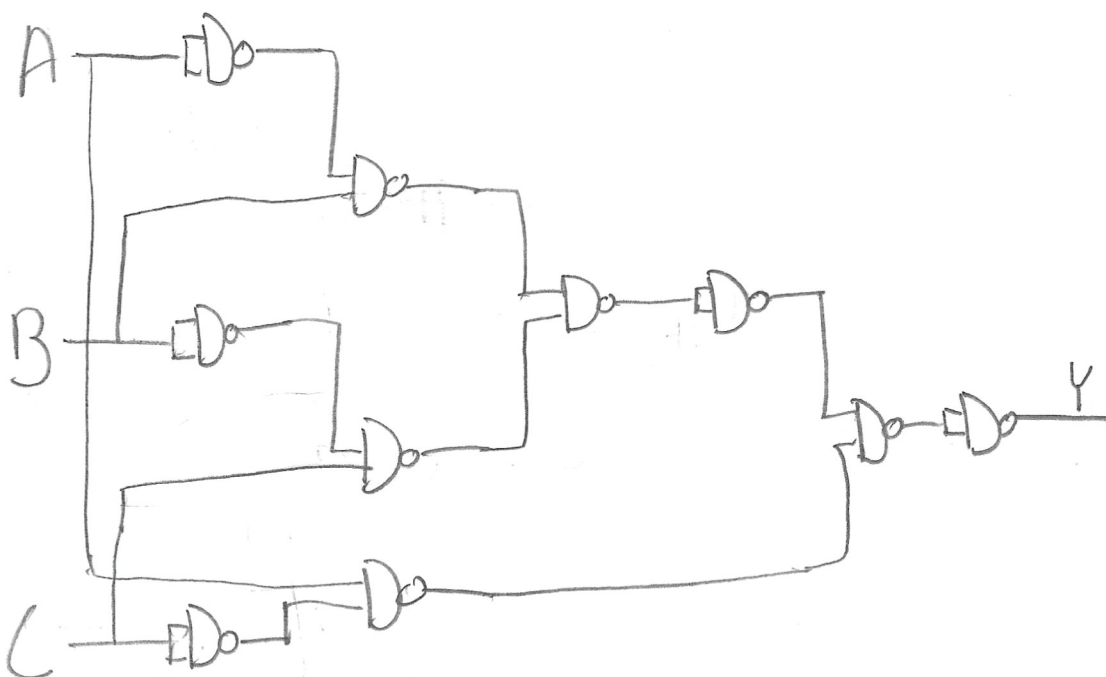
a) $Y = A\bar{B} + B\bar{C} + \bar{A}C$ using 2-input NOR gates only

$$Y = \overline{\overline{A+B}} + \overline{\overline{B+C}} + \overline{\overline{A+C}}$$

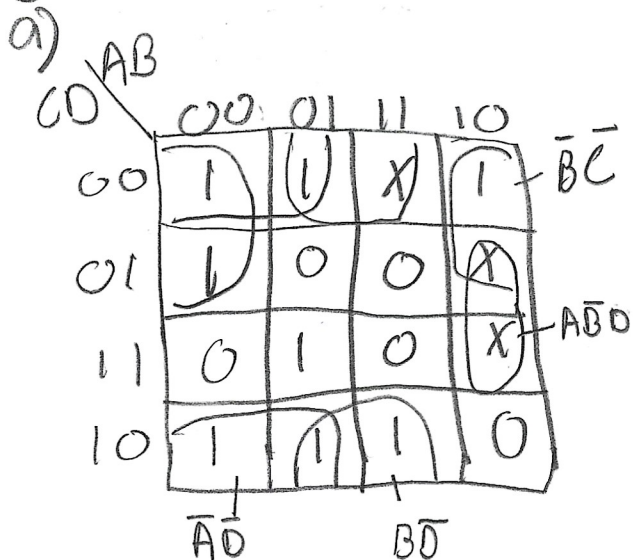


b) $Y = (A+\bar{B})(B+\bar{C})(\bar{A}+C)$ using 2-input NAND gates only

$$Y = \overline{\overline{A+B}} \overline{\overline{B+C}} \overline{\overline{A+C}} \quad Y = \overline{\overline{A+B}} \overline{\overline{B+C}} \overline{\overline{A+C}}$$

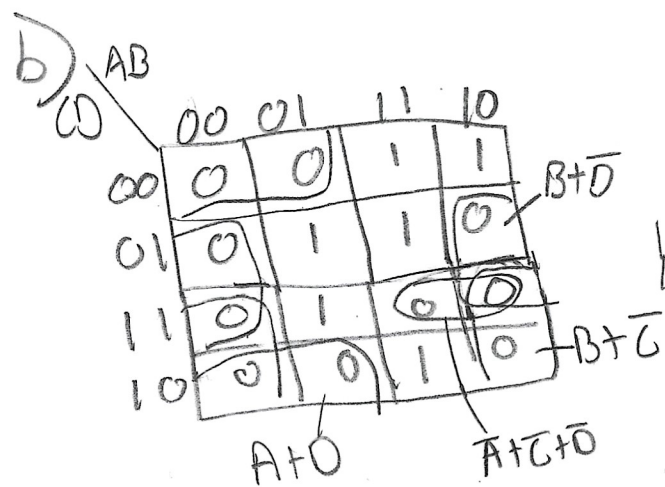


6)



Minterm grouping

$$Y = \overline{A}\overline{D} + \overline{B}\overline{C} + B\overline{D} + A\overline{B}\overline{D}$$



Maxterm grouping

$$Y = (A + D)(B + \overline{D})(B + \overline{C})(\overline{A} + \overline{C} + \overline{D})$$

7)

Control Signal ($F_2:0$)	Function $Y(A,B)$
000	\overline{A} or \overline{B}
101	\overline{A} and B
110	$B - A$