

# Automatic Pipeline Register Placement Through Backannotation in Chisel

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## Abstract

### 1. Introduction

### 2. Related Works

[1]

### 3. Project Objectives

### 4. Technical Approach

### 5. Results

### 6. Conclusion and Future Work

## References

- [1] J. Bachrach, H. Vo, B. Richards, Y. Lee, A. Waterman, R. Avižienis, J. Wawrzynek, and K. Asanović. Chisel: constructing hardware in a scala embedded language. In *Proceedings of the 49th Annual Design Automation Conference*, DAC '12.