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## LAB 1

This lab was about learning how to navigate the software we are using in this class. Some things I learned in this lab are: how to draw a schematic using synopsys, how to edit properties, how to turn a schematic into a preset you can use later, and how to use the waveform simulator to measure certain aspects of the circuit. Throughout this lab I didn't have any problems as the guide was straightforward.

### Requested Pictures:

Fig 13:

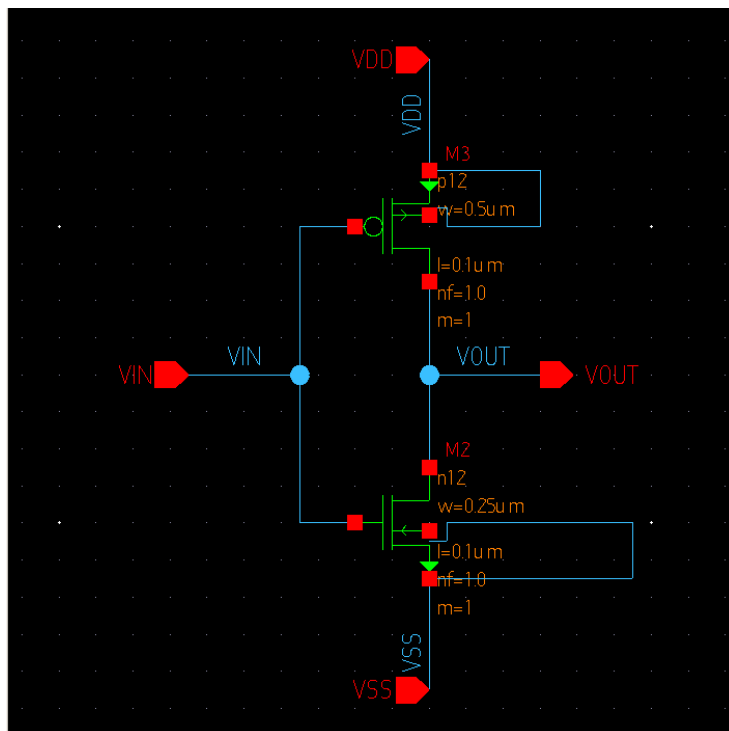


Fig 15:

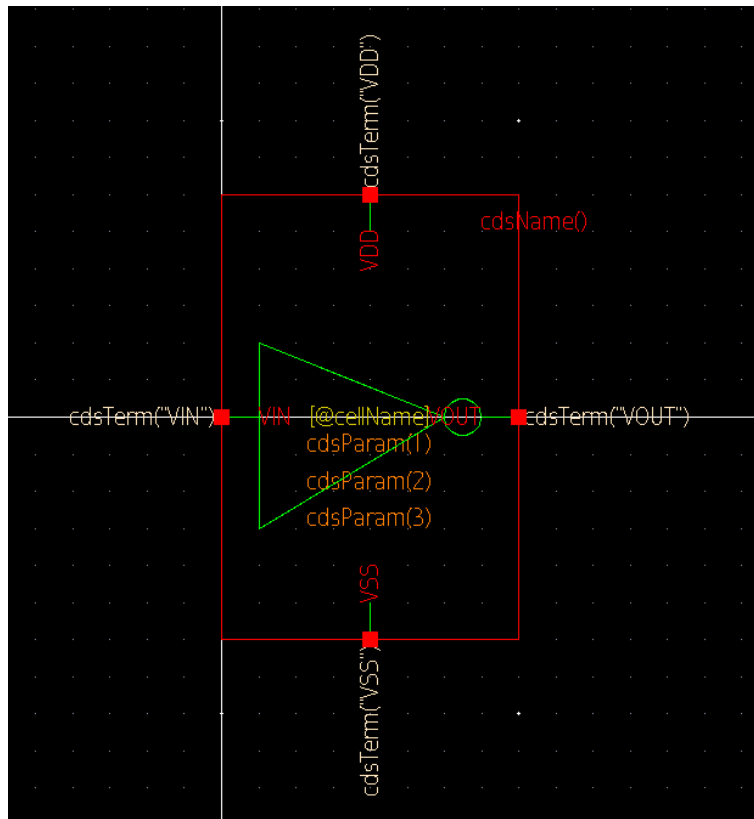


Fig 18:

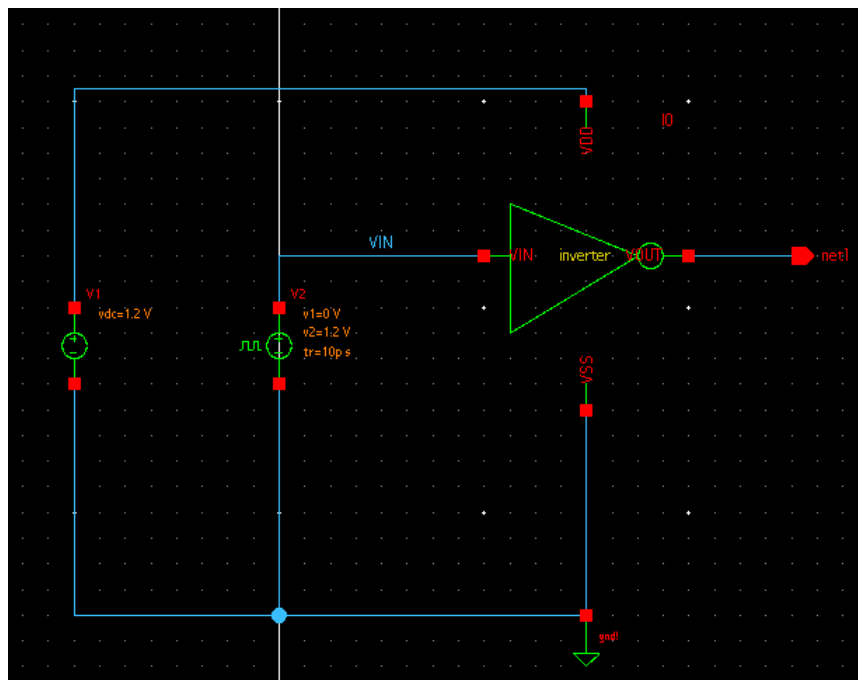


Fig 28:

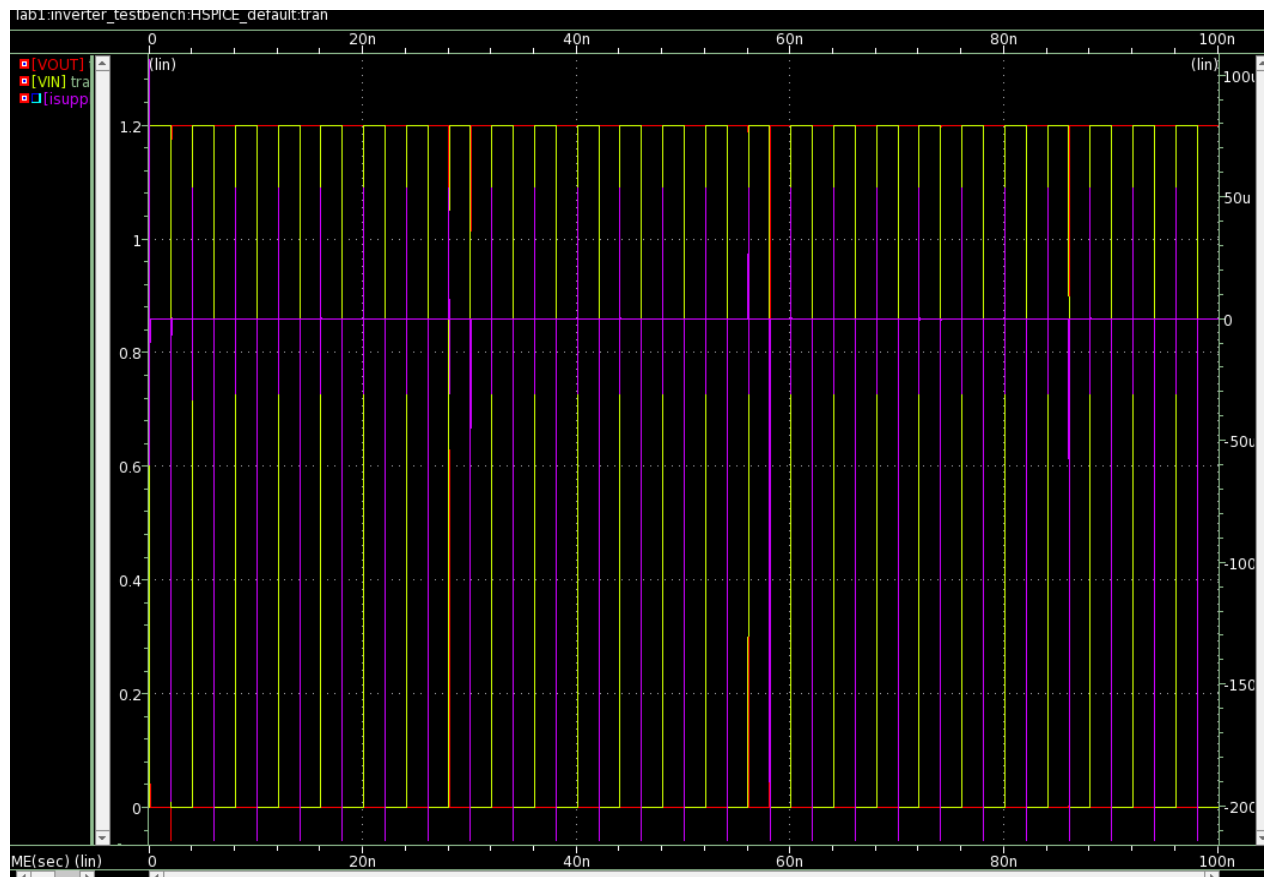


Fig 29:

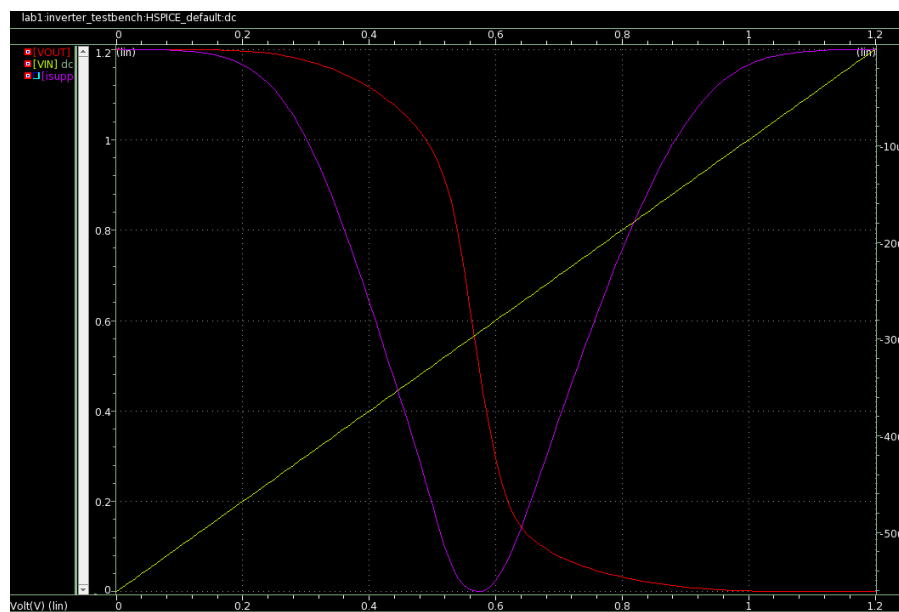


Fig 33:

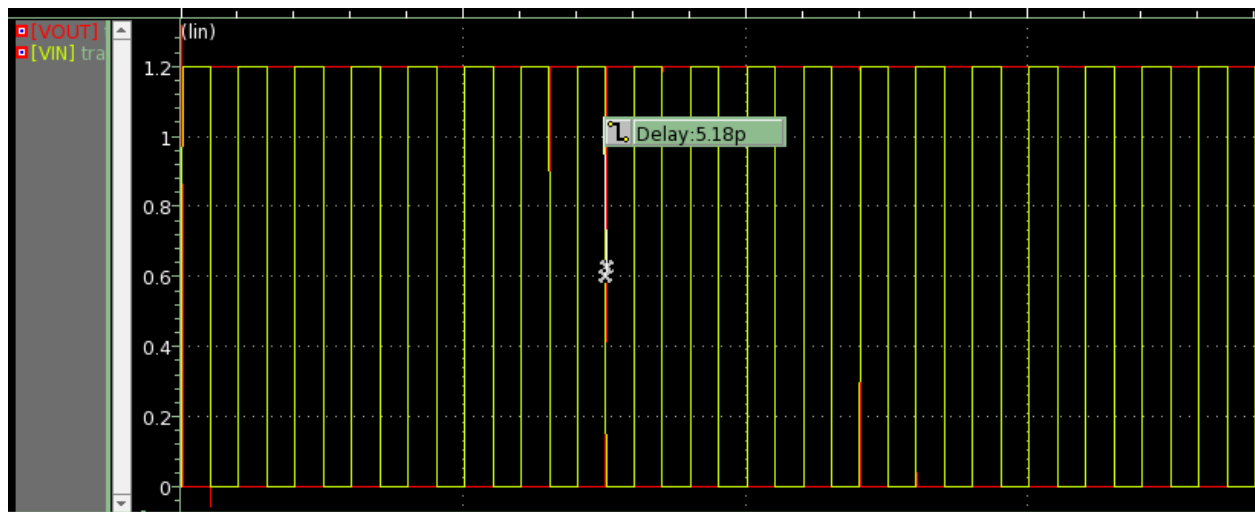


Fig 35:

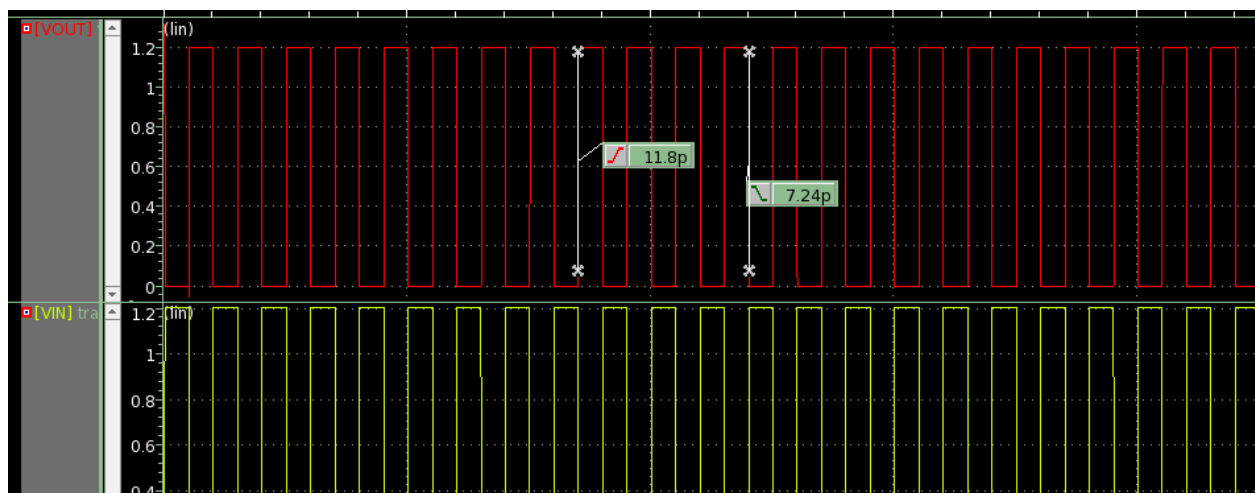


Fig 37:

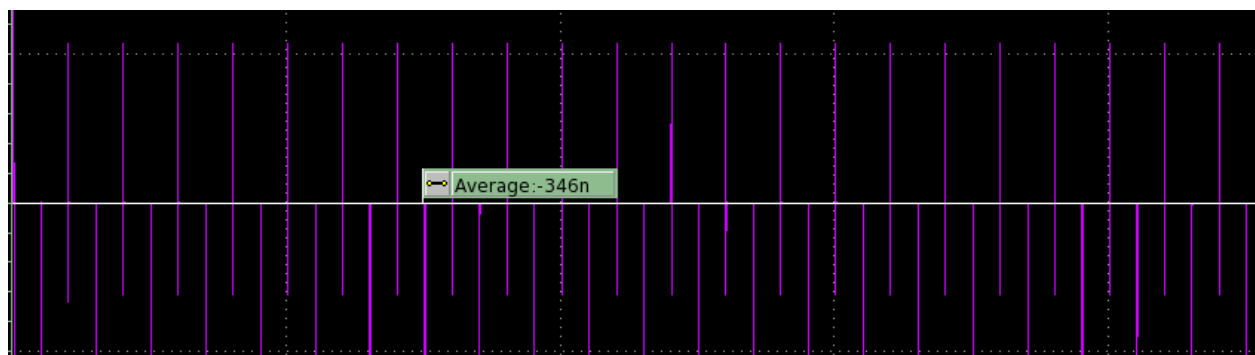
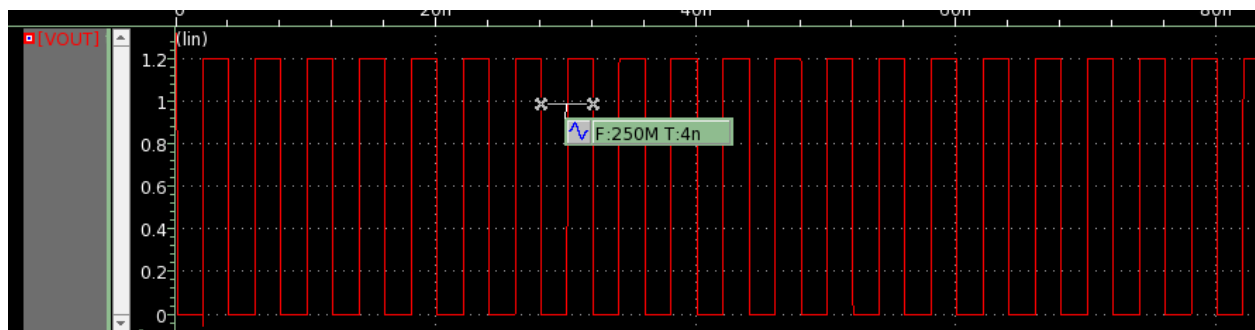


Fig 39:



### **Conclusion:**

Overall I did not have any issues. One thing that had me annoyed for a bit was that I came back later to finish my report and I didn't realize you could load a state for your waveforms. Luckily I figured it out.