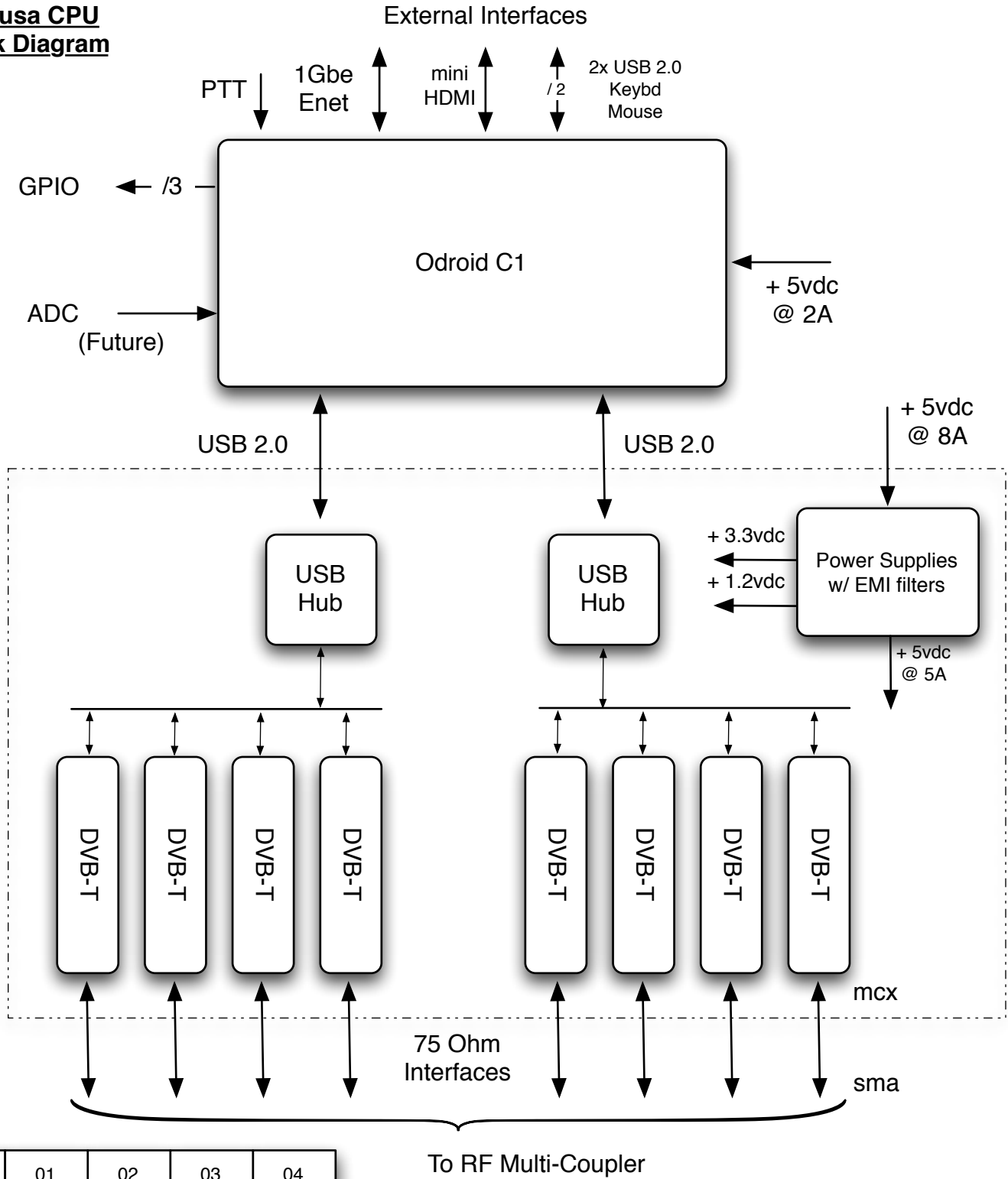


Medusa CPU
Block Diagram



GPIO	01	02	03	04
ATTN 0	0	0	0	x
ATTN 10 dB	0	0	1	x
ATTN 20 dB	0	1	0	x
ATTN 30 dB	1	0	0	x
Bypass BCB	x	x	x	0
BCB Active	x	x	x	1