

CAPE PENINSULA UNIVERSITY OF TECHNOLOGY

ENGINEERING FACULTY

**EFRC1.1 - Checklist and Evaluation
of Dissertation / Thesis Proposal**

Faculty	Engineering and the Built Environment	Department	Electrical Electronic and Computer	Degree	MEng: Electrical
Candidate	DIEUVEIL ORCEL THYS_DINGOU		Date submitted	26-10-2020	
Title	Design and development of the alice cru user logic firmware for the mid readout chain.				
Supervisor	Dr AK Raji				

		Yes	No	Un-clear	Comment
1.	Research Topic				
1.1	Is the research problem/question clearly stated?	X			Yes, adequate
1.2	Is the problem/question researchable?	X			
1.3	Is the topic significant?	X			Yes, very significant and relevant
1.4	Is the scope appropriate for the qualification?	X			Yes
1.5	Is the research appropriately delimited?	X			
1.6	Are the research aims clear?	X			Yes, very clear
1.7	Are the assumptions stated?	X			
1.8	Is the terminology adequately defined?	X			Yes, well defined
2.	Literature review				
2.1	Is the literature relevant to the problem?	X			Yes
2.2	Has an adequate conceptual framework been developed?	X			
2.3	Is the literature current?	X			Yes
2.4	Has the relationship between the research topic and previous research been outlined?	X			
2.5	Are textual referencing and bibliographic citation correct and consistent?	X			
3.	Methodology				
3.1	Does the research design address the research problems/questions?	X			Yes
3.2	Are the data collection/production methods appropriate?	X			
3.3	Are the data analysis methods appropriate?	X			
3.4	Have ethical considerations been addressed? ¹				
4.	General				
4.1	Is the proposal free of writing/typographical errors?	X			It seems to be
4.2	Does the proposal appear to be free of plagiarism? ²	X			
4.3	Is the research manageable in terms of timeframe?	X			
4.4	Is the budget allocated adequate?	X			Adequate

¹ Indicate whether ethical clearance through a research ethics committee is required.

² In the case of plagiarism, the proposal should be returned to the candidate with a warning. The candidate will need to re-submit (see Policy & Procedures on Plagiarism: HDC 2.1).

Submit (See Policy & Procedures on Flagging: HDC 2.1):			
Review Panel			
Dr Marco Adonis			
Dr Ali Almaktoof <i>AMA Almaktoof</i>			
Qualification			
DTech: Electrical Engineering			
DTech: Electrical Engineering			
Recommendations	TO BE APPROVED BY HDC		
Review Panel Chair	DR ATANDA RAJI	Date	27 NOVEMBER 2020