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Registers AN3308

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AN3308 Startup

AN3308 Startup sequence

4.2.3 Example of acceleration data

Table 9: "Output data registers content vs. acceleration (FS = ± 2 g, high-resolution mode)" provides a few basic examples of the data that is read in the data registers when the device is subject to a given acceleration. The values listed in the table are given under the hypothesis of perfect device calibration (i.e. no offset, no gain error,....) and practically show the effect of the BLE bit.

Table 9: Output data registers content vs. acceleration (FS = ± 2 g, high-resolution mode)



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