

# Linear Block Code Error Correction

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## 1 Summary

We introduce the (6,3) Hamming Linear Block Coding a error detecting and correcting algorithm. For this lab we will parse a string into 3-bit sized messages and encoded into 6-bit codewords. We then transmit our codewords as characters to a Windows client from a PIC32 MCU. To simulate a noisy communication medium our client introduces random errors into our message and returns it back to the server. We show that the Hamming LBC is capable of correcting up to 1-bit in error for each received codeword.

## 2 Introduction

### 2.1 Hamming Linear Block Encoding/Decoding Theory

The Hamming linear block code is a forward error detection and correction method where the original message is encoded to include more symbols to add redundancy to the original message. The process allows the receiver of the message to check and attempt to correct errors introduced by a noisy communications channel. In this lab we will use a (6,3) Hamming code generator matrix ( $G$ ) which we will transform a 3-bit messages ( $\vec{m}$ ) into 6-bit codewords ( $\vec{x}$ ).

$$\vec{x} = \vec{p}G \text{ where,} \quad (1)$$

$$G = \begin{bmatrix} 1 & 0 & 0 & 1 & 1 & 0 \\ 0 & 1 & 0 & 1 & 1 & 1 \\ 0 & 0 & 1 & 1 & 0 & 1 \end{bmatrix}$$

On the receiving side we first take our received codeword ( $\vec{r}$ ) and detect any errors in the received message. We do so by taking our codeword and checking it against a parity matrix ( $H$ ). Note that our linear block code produces a systematic codeword; meaning our message is separated into  $k$  message bits and  $(n-k)$  parity bits. [1]

$$\vec{z} = \vec{r}H \text{ where,} \quad (2)$$

$$H = \begin{bmatrix} 1 & 1 & 0 \\ 1 & 1 & 1 \\ 1 & 0 & 1 \\ 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{bmatrix}$$

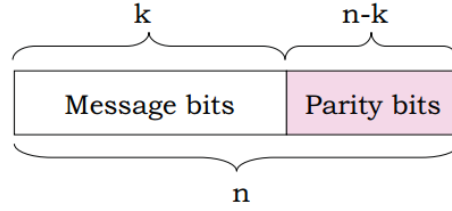


Figure 1: Example of Systematic Encoding Formatting [1]

The generator matrix ( $G$ ) is constructed from an identity matrix of size  $k \times k$  concatenated with a matrix  $A$  of size  $k \times (n - k)$  to the left. If we take matrix  $A$  we can create our parity matrix ( $H$ ) by concatenating it with a identity matrix of size  $(n - k) \times (n - k)$  on the bottom.

$$G_{k \times n} = [I_{k \times k} \mid A_{k \times (n-k)}] \quad H_{n \times k} = \begin{bmatrix} A_{k \times (n-k)} \\ I_{(n-k) \times (n-k)} \end{bmatrix}$$

The error detector referenced by equation 2 is said to detect an error when the syndrome ( $\vec{z}$ ) is non-zero. The error correction works by correcting the bit in the received codeword in reference to a minimized **Hamming Distance** between a syndrome in the set of possible valid syndromes ( $\vec{z}'$ ) and the calculated syndrome ( $\vec{z}$ ). We can calculate a set of possible valid syndromes ( $\vec{z}'$ ) by multiplying the parity check matrix ( $H$ ) by an identity ( $I$ ); where the identity simulates codewords where one bit is flipped. The result tells us an error in the MSB (bit-6) maps to a syndrome of  $\vec{z} = [1 \ 1 \ 0]$  and so on.

$$H = I * H = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix} * \begin{bmatrix} 1 & 1 & 0 \\ 1 & 1 & 1 \\ 1 & 0 & 1 \\ 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{bmatrix}$$

After we have our codeword corrected ( $\vec{r}'$ ), we can retrieve the original message ( $\vec{p}_r$ ) by using a (3,6) decoding matrix ( $R$ ). The decoding matrix reverses the steps taken by the generator matrix ( $G$ ).

$$\vec{p}_r = \vec{r}' R \text{ where,} \quad (3)$$

$$R = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \\ 0 & 0 & 0 \\ 0 & 0 & 0 \\ 0 & 0 & 0 \end{bmatrix}$$

Finally we wish to look at the performance of this Hamming Linear Block code encoder/decoder. The correction capacity for any given codeword is given by equation 4 where  $t$  denotes the number of errors that are detectable and correctable. In some instances the receiver may not be able to correct a received codeword but it is still detectable. The error detection (e) is given by equation 5.

$$t = \lfloor \frac{d_{min} - 1}{2} \rfloor \quad (4)$$

$$e = d_{min} - 1 \quad (5)$$

## 2.2 Hamming Linear Block Encoding/Decoding Implementation

For this lab we will transmit the message “*EE is my avocation*” and set up a communication channel between a PIC32 Microchip MCU and Windows desktop over Ethernet. The Windows machine will run a Visual Basic application which will receive a message from the PIC32 server and introduce random errors to simulate errors that may be introduced when transmitting over a noisy communications channel. VB client *Client2 v16B* will introduce at most 1-bit in error within a given message. VB Client *Client3 v16* will introduce up to 10-bits in error. After we will setup our PIC32 MCU to receive a message from the VB client in order to perform error detection and correction using the Hamming linear block scheme.

To use a (6,3) Hamming code requires our individual messages to be 3-bits in size. Therefore, we format our transmission string into messages 3-bits in size. We then will encode our message to produce 6-bit codewords. Since our message is using characters within the standard ASCII table, the MSB in all characters will always be zero. Therefore, ignoring the MSB can now take our 7-bit ASCII character and break it up 3 individual bits. A combination of 3 characters will end up creating a perfect set to complete 7 packets we can send.

$$\begin{aligned} 'E' &= [0100 \ 0101] \rightarrow [100 \ 0101] \\ 'E' &= [0100 \ 0101] \rightarrow [100 \ 0101] \\ '\_ ' &= [0100 \ 0000] \rightarrow [100 \ 0000] \end{aligned}$$

100	010	110	001	011	000	000
$\vec{p}_0$	$\vec{p}_1$	$\vec{p}_2$	$\vec{p}_3$	$\vec{p}_4$	$\vec{p}_5$	$\vec{p}_6$

Table 1: 3-Bit Message Composition for First 3 Characters

Our overall message is 18 characters in length. Distributing our 18 characters into 7 packets of length 3-bits produces 42 packets. Those 42 packets will next be encoded using our (6,3) Hamming linear block encoder to produce packets of 6-bits. Finally, we will pad each 6-bit packet with two leading zeros and send it as a 8-bit ASCII character. The final transmitted packet sequence for the first 3 characters are shown in table 2 by applying equation 1.

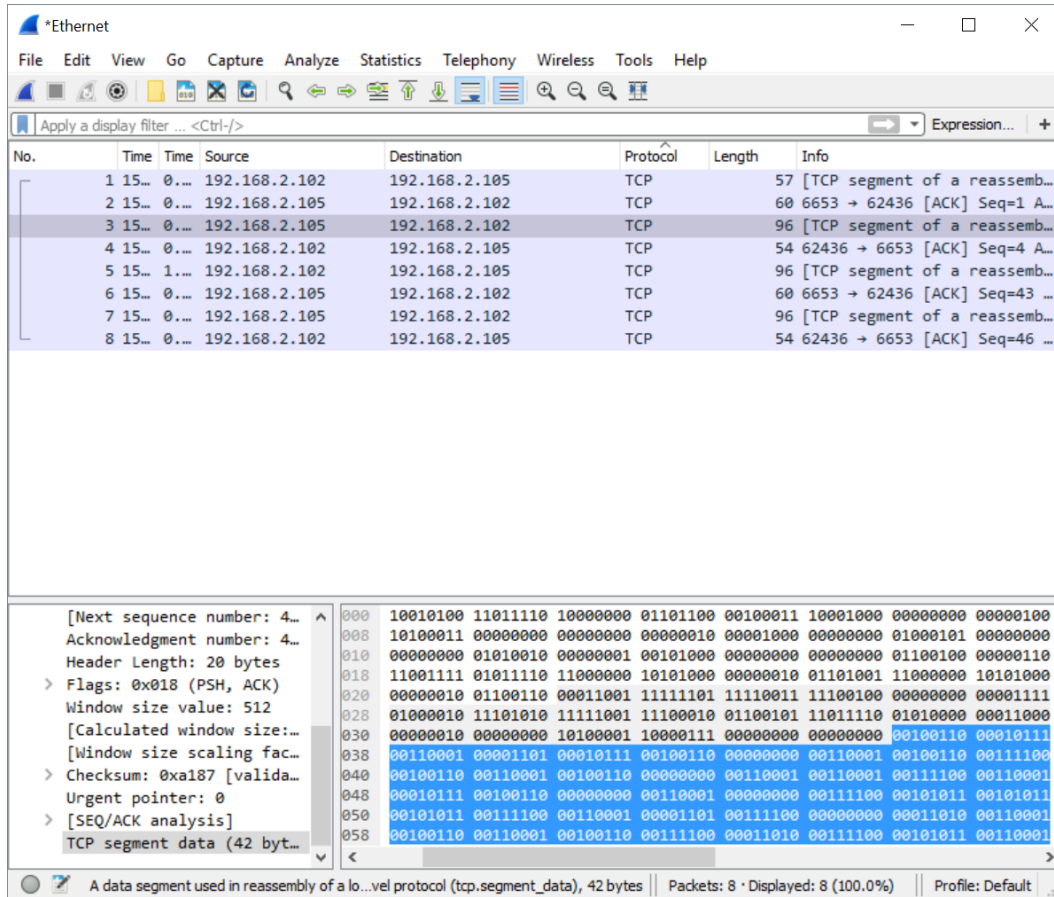
0010 0110	0001 0111	0011 0001	0000 1101	0001 1010	0010 0110	0000 0000
$\vec{x}_0$	$\vec{x}_1$	$\vec{x}_2$	$\vec{x}_3$	$\vec{x}_4$	$\vec{x}_5$	$\vec{x}_6$

Table 2: 6-Bit Codewords Composition for First 3 Characters

### 3 Discussion

To begin we need several functions that will allows us to create our As will be seen in order to analyze our experiments, we send the PIC32 MCU server a a return message back. The returned message may have errors randomly distributed throughout as to simulate a noisy communications channel. The server will receive the message and detect and attempt to correct any errors. With the code provided (see Appendix) a uncorrectable error will light up the PIC32's red LED (LED0). A correctable error will flash the PIC32's yellow LED (LED1) as it goes about correcting any errors.

We can use WireShark to analyze the packet transmitted from our server PIC32 MCU to our VB client. We expect the first seven packets to be as demonstrated in table 2. The 42 packets that make up our overall transmission can be seen in highlighted figure 2.



### 3.1 Control Case: No Errors Introduced

Next we want to ensure decoding of a message with no errors introduced works as designed. The expected transmitted message from the Windows Desktop with no errors will be the same as that seen when the message was initially transmitted to the Windows Desktop client referenced by figure 2. As can be seen in the transmitted message from the Windows Desktop client, no errors were introduced.

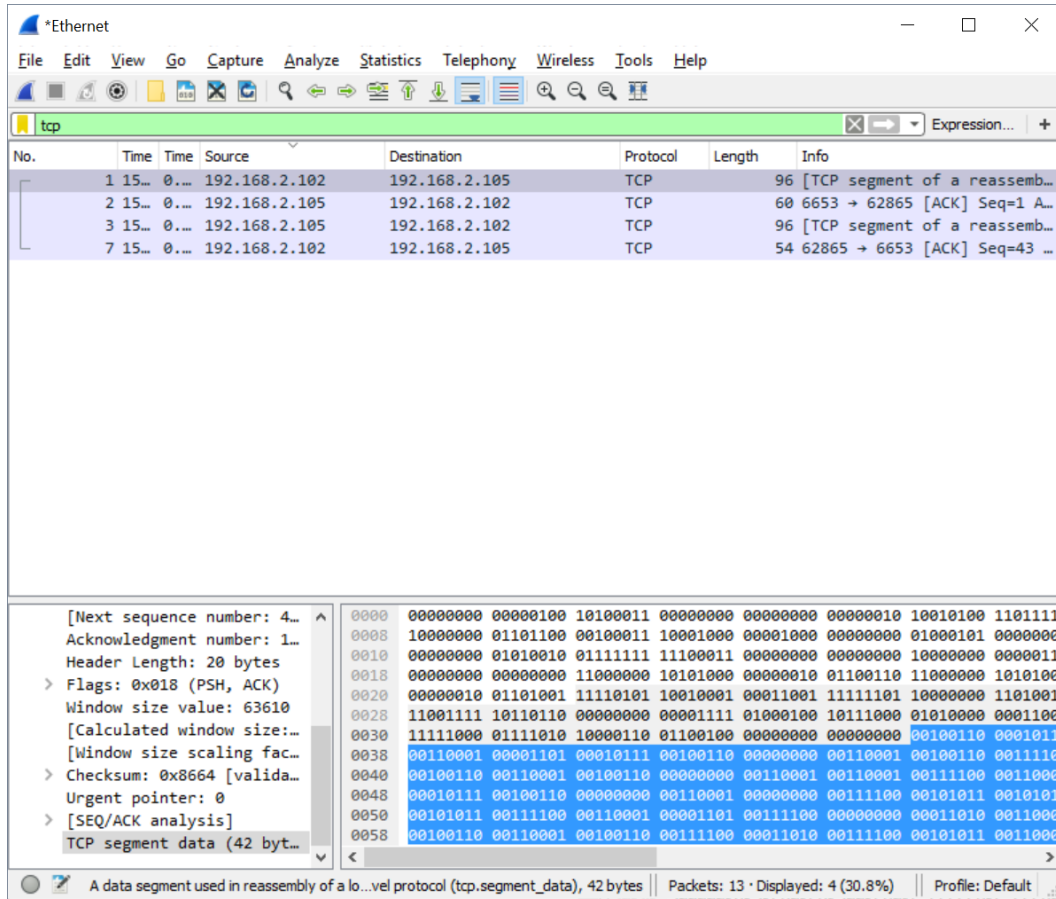


Figure 3: WireShark Capture of 42 Packets Tx Windows Desktop → PIC32 MCU with No Error

The message echoed by the PIC32 server should be the same as that transmitted to it since there were no errors introduced. Comparing figure 3 with figure 4 we can see no discrepancies.

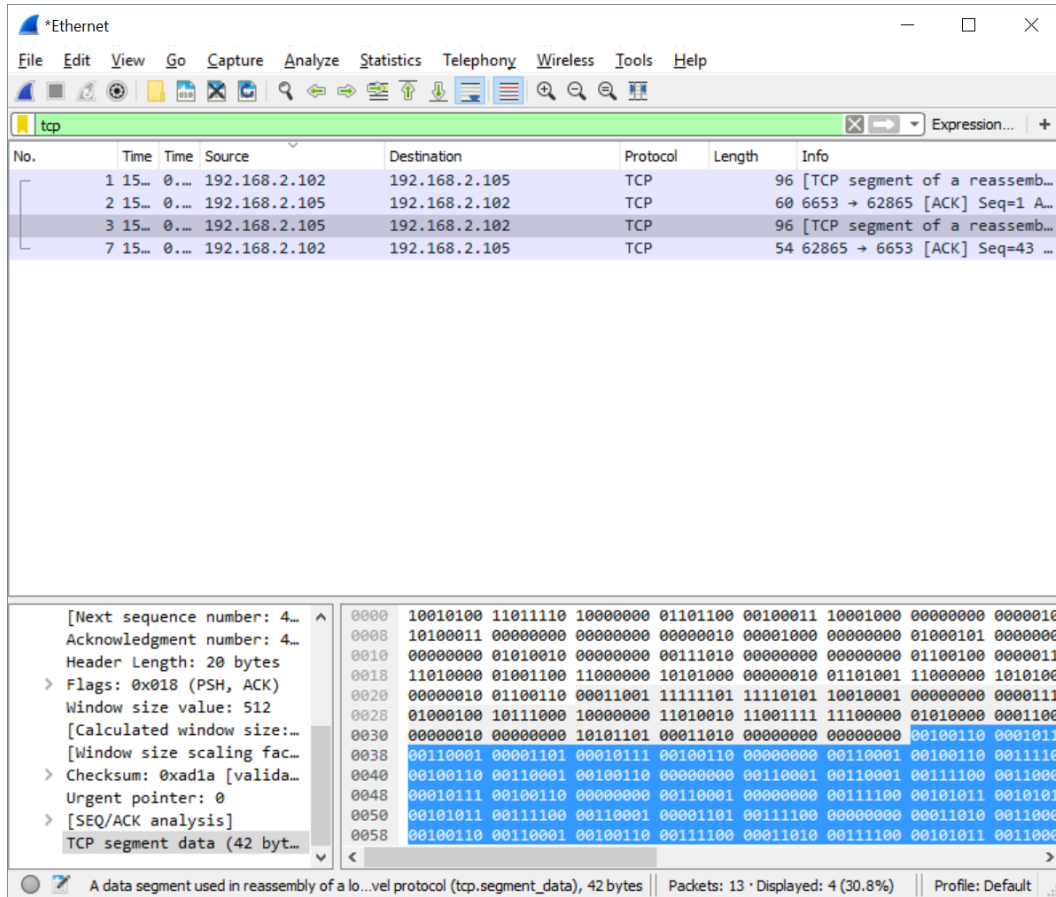


Figure 4: WireShark Capture of 42 Packets Tx PIC32 MCU → Windows Desktop with No Error

### 3.2 1-Bit Error

Next we show that for one bit in error our Hamming error detector and corrector will be able to detect any introduced errors. The error in this 1-bit error test was introduced into the 48th row of the transmission. A 8-bit sequence of 0x00 was converted to 0x01.

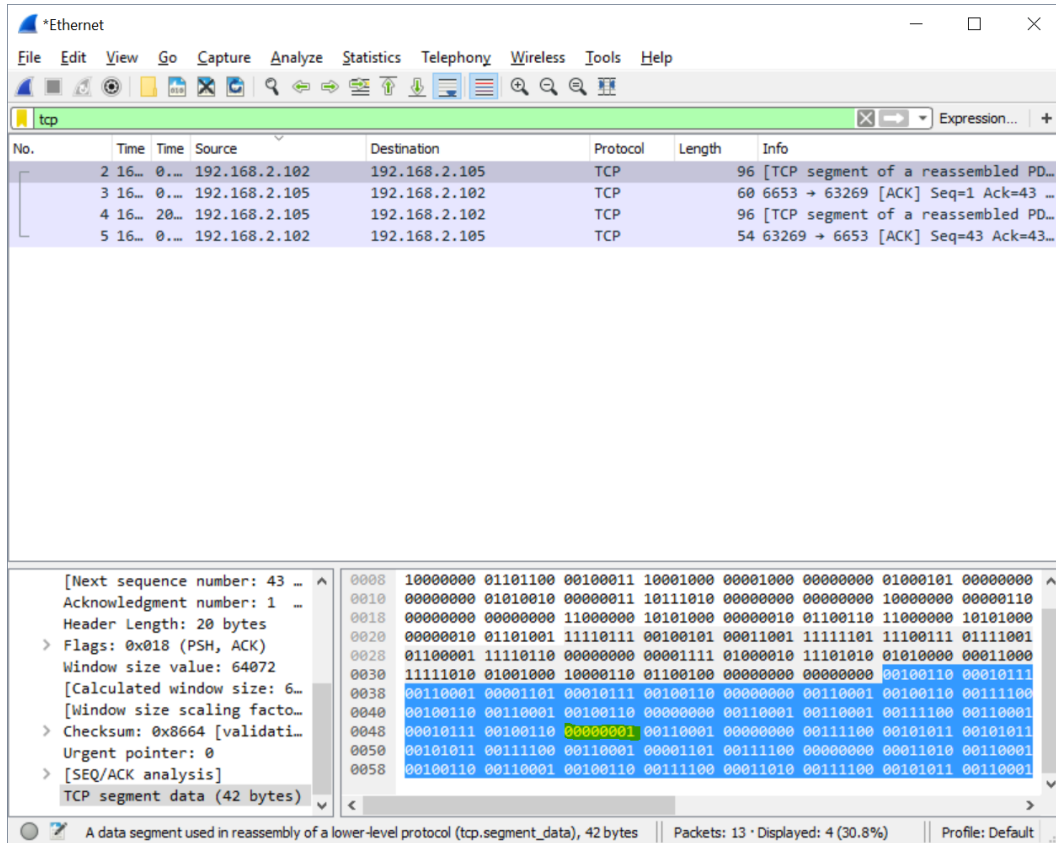


Figure 5: WireShark Capture of 42 Packets Tx Windows Desktop → PIC32 MCU with 1-Bit Error

The error highlighted in figure 5 can be seen corrected in the echoed message from the PIC32. In reality this introduced error effected one of the parity bits so our original message was still intact, but the test shows that LBC can even correct errors in the parity portion of a transmission.



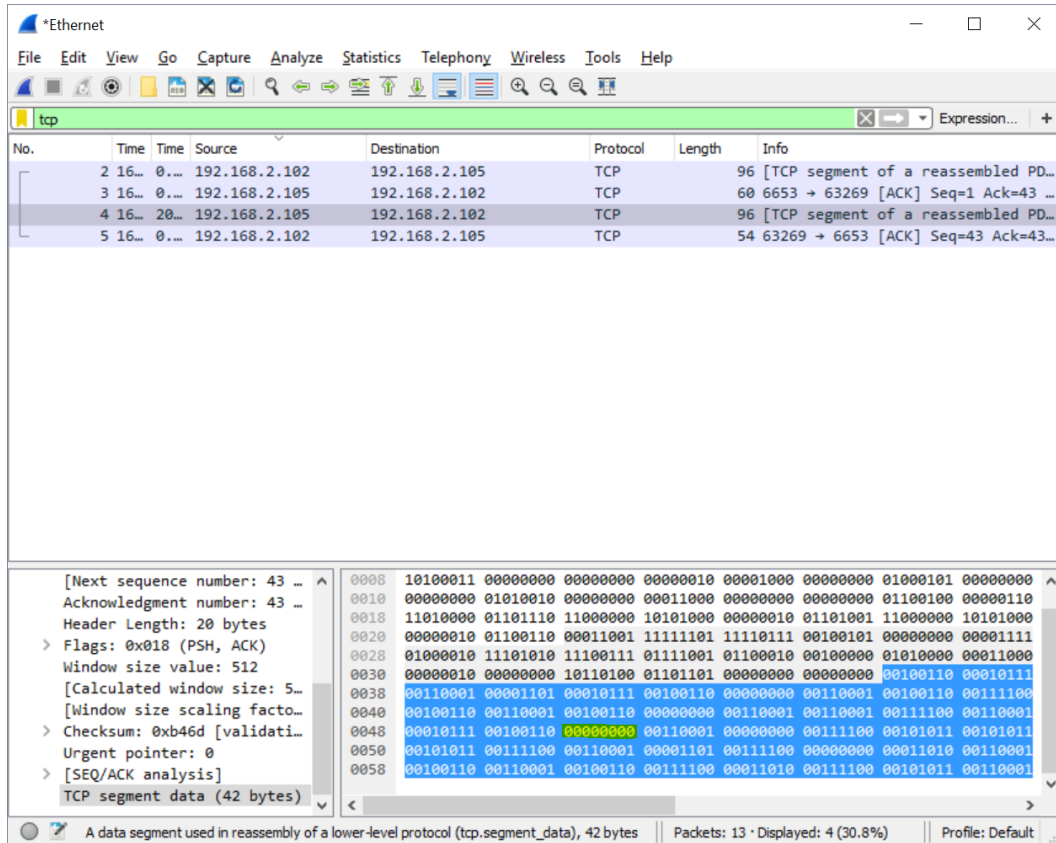


Figure 6: WireShark Capture of 42 Packets Tx PIC32 MCU → Windows Desktop with 1-Bit Error

### 3.3 Multiple Errors in Transmission

Our last test will show that in extreme cases where more than one bit in error in introduced to our transmission, our Hamming decoder has the capability to correct the error. In the next example we introduce 8 bit in error to the packet. There are two bit errors that occur in the two MSB of the packet. Recall that the two MSB are padded zeros, so these bit errors are ignored.

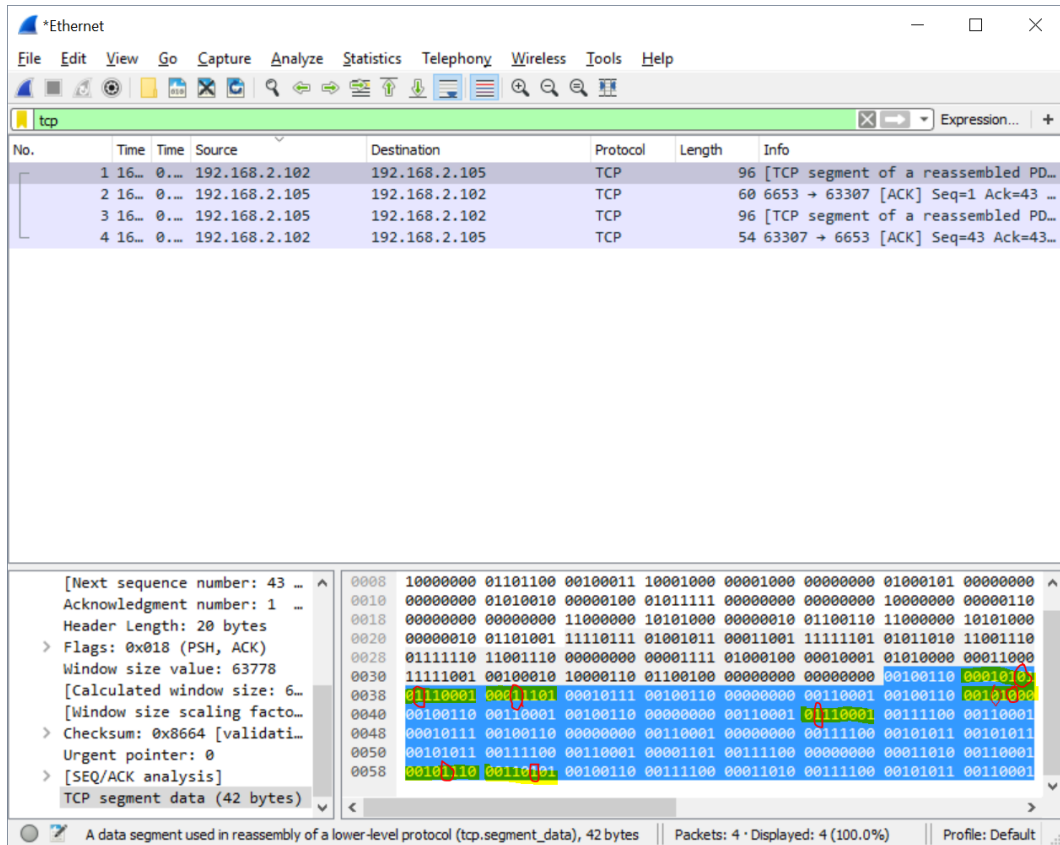


Figure 7: WireShark Capture of 42 Packets Tx Windows Desktop → PIC32 MCU with 8-Bit Errors

The corrected message sent echoed by the server corrects the issues it could. In the second byte, we can see how the second bit from the left was corrected back to a binary 1. Our third highlighted error was also corrected so its incorrect bit was flipped. Our fourth byte sequence which contained an error was changed to all zeros. This is behavior coded for when an error is detected but could not be corrected. Since there were more than one bit and error in the message the receiver could not correct the message. In this scenario typically we would have the receiver re-transmit the message.

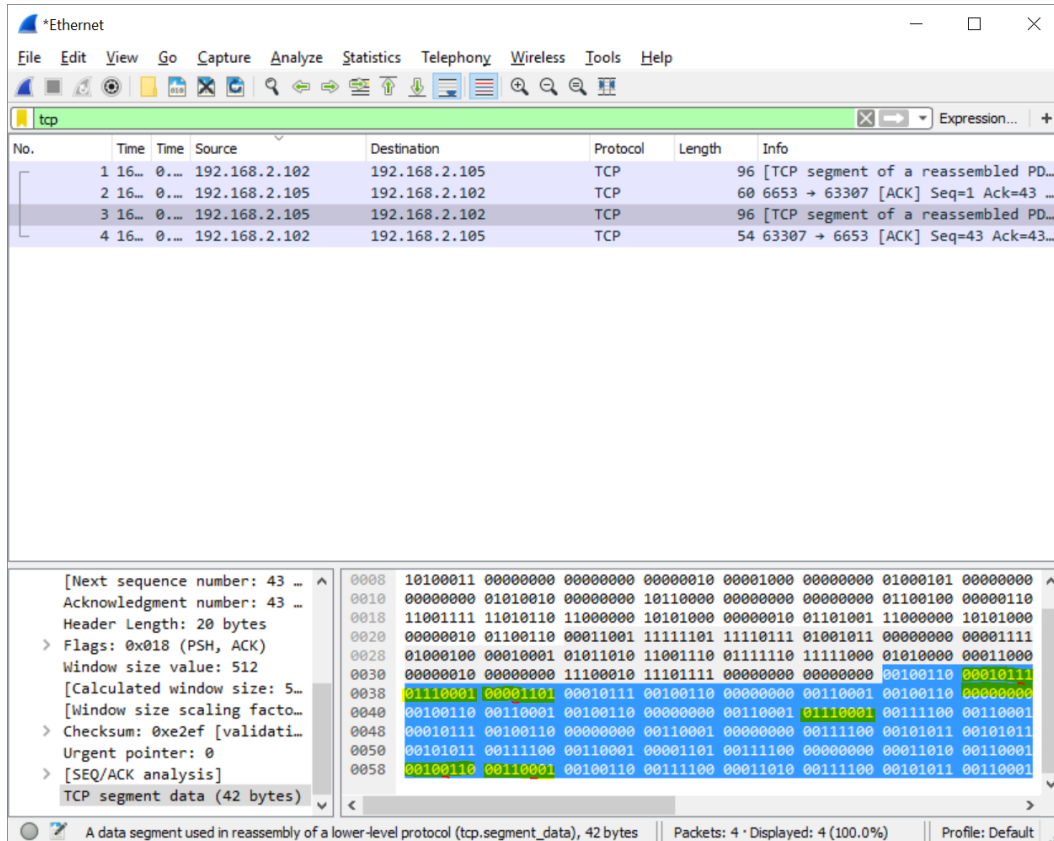


Figure 8: WireShark Capture of 42 Packets Tx PIC32 MCU  $\rightarrow$  Windows Desktop with 8-Bit Errors

As is seen in the two byte sequences that contained errors in the higher two MSB locations, our implementation is not at its peak efficiency. Hypothetically our message size is  $k = 3$  and our codeword size is  $n = 6$ , we can say our **code rate**  $= \frac{k}{n} = \frac{6}{3} = \frac{1}{2}$ . However, since we pad our codeword with two leading zeros it can be said that our code rate decreases to code rate  $= \frac{3}{8}$ .

The padding of zeros only causes our data rate to decrease. A way we could improve our Hamming linear block encoder/decoder is by fully utilizing the entire length of our 8-bit packets. It will complicate the decoding process since it would require that we extract bits from other various packets to construct our codeword.

## 4 Conclusion

We have introduced another form of error detection and correction that has been proven to correct up to one in error. The Hamming linear block code is useful in forwarding error correction to reduce the need for retransmission of data corrupted by the transmission medium. The technique is effective in correcting a message in its use of codewords which convert a message to a sequence where the difference between each codeword is at least two bits. By maximizing the difference between messages we can improve the performance of this error detection and correction scheme.

## References

- [1] Hari Balakrishnan and George Verghese. Introduction to EECS II: Digital Communication Systems, 2010.

## Appendix

Main program source code available on my GitHub:

<https://github.com/dtrejod/myece4532/blob/master/lab4/ECE4532%20PIC32%20BSD%20Server/source/main.c>