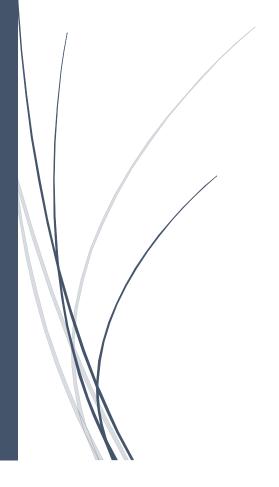
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ECSE 331: Electronics

Laboratory Report No. 5 McGill University



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Design of a BJT Amplifier

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Abstract—The purpose of laboratory experiment is to design a BJT amplifier using the 2N2222A npn transistor that we studied in Lab 4 using the NI Elvis-II test instrument. In the laboratory, an amplifier with a gain of $50~\rm V/V$ was built. Then in the second part, its gain at various temperatures were measured using the Bode plot function of the Elvis Instrument. Experimental results showed that as the temperature of the transistors increases, the gain also increases.

Index Terms-BJT, transistors, Amplifier

I. Introduction

THE goal of this laboratory was to build, test, and explore the behavior of the BJT amplifier using the NI Elvis-II test instrument. The performance of the amplifier was tested under the required constrains of bandwidth and RMS level. The effect of temperature on the operation of the transistors was also tested and found.

II. EXPERIMENTS PROCEDURES AND RESULT

- A. BJT Amplifier Circuit Design
- B. BJT Amplifier Performance Testing
- C. Temperature Effects of BJT Amplifier

III. CONCLUSION

To conclude, this lab was very helpful for using a BJT transistor. We built an amplifier that produced a gain of around 50 V/V for a frequency range of over $100\,\mathrm{kHz}$. The behavior of the amplifier was tested at various temperatures, the gain was found using the Bode plot at room temperature (27 °C), $-10\,^\circ\mathrm{C}$, $-19\,^\circ\mathrm{C}$ and $63\,^\circ\mathrm{C}$. The differences in the gain were very small, but overall satisfying. Our gain increased slightly as we decreased the temperature of the BJT. This could have been due to the fact that, at lower temperature, there are less mobile charge carriers.

This work is the report of the laboratory section of course ECSE 331 offered at McGill University.

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