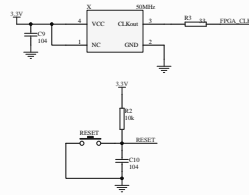
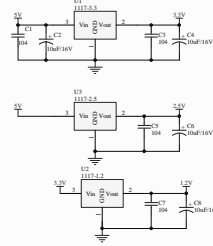
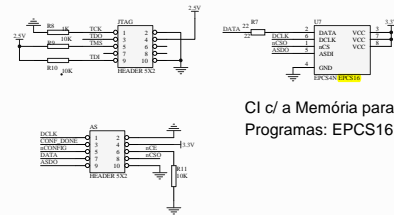
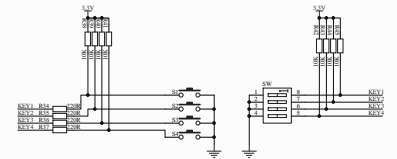
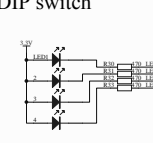
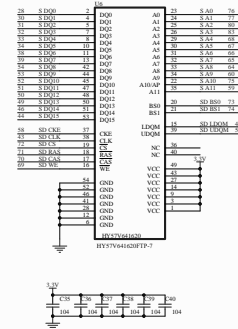
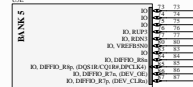
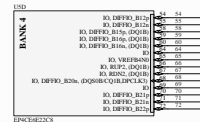
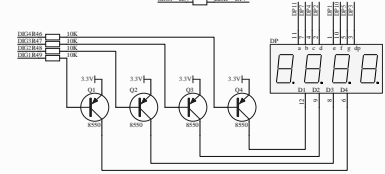


## Crystal and reset



SEG4	R50	2.20R	DP1
SEG3	R51	2.20R	DP2
SEG0	R52	2.20R	DP11
SEG7	R53	2.20R	DP3
SEG5	R54	2.20R	DP10
SEG2	R55	2.20R	DP8
SEG6	R56	2.20R	DP5
SEG1	R57	2.20R	DP7



Temperature Sensor  
(sensor de Temperatura LM75)

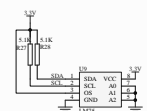
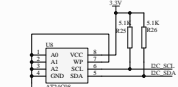
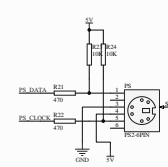
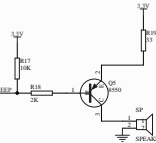
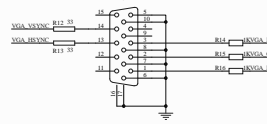
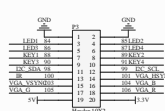
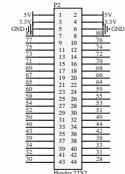


Diagrama Esquemático do Kit RZ-EasyFPGA

A1	A2.2 <i>Size</i> <i>Number</i>	
<i>Revision</i>		
Date:	2014/12/9	Sheet of
File:		