

EasyFPGA 2.1 - Development board pin allocation table

FPGA_CLK 23

RESET button 25

Keys	Pin number	Pull-up!
KEY1	88	"0" quando
KEY2	89	acionadas!
KEY3	90	
KEY4	91	Atenção!
Dial switch	Pin number	As chaves e
ckey1	88	os botões
ckey2	89	estão
ckey3	90	ligados
ckey4	91	juntos!!!

LED	Pin number	
led1	87	Acionados
led2	86	com "0"
led3	85	
led4	84	

UART	Pin number
UART_TXD	114
UART_RXD	115

IIC	Pin number
SCL	112
SDA	113
I2C_SCL	99
I2C_SDA	98

PS2	
PS_CLOCK	119
PS_DATA	120

IR	
IR	100

VGA	Pin number
VGA_HSYNC	101
VGA_VSYNC	103
VGA_B	104
VGA_G	105
VGA_R	106

LCD 1602 12864	Pin number	Conector do LCD
	GND	1
	5V	2
	Background	3
LCD1 RS	141	4
LCD2 RW	138	5
LCD3 E	143	6
LCD4 D0	142	7
LCD5 D1	1	8
LCD6 D2	144	9
LCD7 D3	3	10
LCD8 D4	2	11
LCD9 D5	10	12
LCD10 D6	7	13
LCD11 D7	11	14
	5V	15
	GND	16
	5V	17
	N/C	18
	5V	19
	GND	20

Componente: **EP4CE6E22C8**
Tensão **3,3V LVTTTL** 8 mA

Set unused pins "as input tri-stated"
Menu Assignments > Device > Device and Pin Options >
Unused Pins > Reserve all unused pins: As input tri-stated

Buzzer	Pin number
beep	110

Digital tube	Pin number	
DIG1	133	Enables
DIG2	135	habilitam
DIG3	136	com "0"
DIG4	137	
SEG0 a	128	Segmentos
SEG1 b	121	acionam
SEG2 c	125	com "0"
SEG3 d	129	
SEG4 e	132	
SEG5 f	126	
SEG6 g	124	
SEG7 .	127	

SDRAM	Pin number
S DQ0	28
S DQ1	30
S DQ2	31
S DQ3	32
S DQ4	33
S DQ5	34
S DQ6	38
S DQ7	39
S DQ8	54
S DQ9	53
S DQ10	52
S DQ11	51
S DQ12	50
S DQ13	49
S DQ14	46
S DQ15	44

S A0	76
S A1	77
S A2	80
S A3	83
S A4	68
S A5	67
S A6	66
S A7	65
S A8	64
S A9	60
S A10	75
S A11	59

SD BS0	73
SD BS1	74
SD LDQM	42
SD UDQM	55

SD CKE	58
SD CLK	43
SD CS	72
SD RAS	71
SD CAS	70
SD WE	69