ERET

Exception Return.

Syntax

`ERET{cond}`

where:

cond

is an optional condition code.

Usage

In a processor that implements the Virtualization Extensions, you can use `ERET` to perform a return from an exception taken to Hyp mode.

Operation

When executed in Hyp mode, `ERET` loads the PC from ELR_hyp and loads the CPSR from SPSR_hyp. When executed in any other mode, apart from User or System, it behaves as:

- `MOVS PC, LR` in the ARM instruction set
- `SUBS PC, LR, #0` in the Thumb instruction set.

Notes

`ERET` is UNPREDICTABLE if it is executed in ThumbEE state or in User or System mode.

`ERET` is the preferred synonym for `SUBS PC, LR, #0` in the Thumb instruction set.

Architectures

This ARM instruction is available in ARMv7 architectures that include the Virtualization Extensions.

This 32-bit Thumb instruction is available in ARMv7 architectures that include the Virtualization Extensions.

There is no 16-bit Thumb version of this instruction.

See also

Concepts

Using the Assembler:

- *Processor modes, and privileged and unprivileged software execution*.

Reference

- *MOV and MVN*
- *SUBS pc, lr*
- *Condition codes*.