System Information

Field	Value
Architecture Information	
Architecture	x86_64
CPU op-mode(s)	32-bit, 64-bit
Address sizes	46 bits physical, 48 bits virtual
Byte Order	Little Endian
CPU Details	
CPU(s)	12
On-line CPU(s) list	0-11
Vendor ID	GenuineIntel
Model name	13th Gen Intel(R) Core(TM) i7-1365U
CPU family	6
Model	186
Thread(s) per core	2
Core(s) per socket	10
Socket(s)	1
Stepping	3
CPU(s) scaling MHz	60%
CPU max MHz	5200.0000
CPU min MHz	400.0000
BogoMIPS	5376.00

Field	Value
Flags	fpu, vme, de, pse, tsc, msr, pae, mce, cx8, apic, sep, mtrr, pge, mca, cmov, pat, pse36, clflush, dts, acpi, mmx, fxsr, sse, sse2, ss, ht, tm, pbe, syscall, nx, pdpe1gb, rdtscp, lm, constant_tsc, art, arch_perfmon, pebs, bts, rep_good, nopl, xtopology, non-stop_tsc, cpuid, aperfmperf, tsc_known_freq, pni, pclmulqdq, dtes64, monitor, ds_cpl, vmx, smx, est, tm2, ssse3, sdbg, fma, cx16, xtpr, pdcm, sse4_1, sse4_2, x2apic, movbe, popcnt, tsc_deadline_timer, aes, xsave, avx, f16c, rdrand, lahf_lm, abm, 3dnowprefetch, cpuid_fault, epb, ssbd, ibrs, ibpb, stibp, ibrs_enhanced, tpr_shadow, flexpriority, ept, vpid, ept_ad, fsgs-base, tsc_adjust, bmi1, avx2, smep, bmi2, erms, invpcid, rdseed, adx, smap, clflushopt, clwb, intel_pt, sha_ni, xsaveopt, xsavec, xgetbv1, xsaves, split_lock_detect, user_shstk, avx_vnni, dtherm, ida, arat, pln, pts, hwp, hwp_notify, hwp_act_window, hwp_epp, hwp_pkg_req, hfi, vnmi, umip, pku, ospke, waitpkg, gfni, vaes, vpclmulqdq, tme, rdpid, movdiri, movdir64b, fsrm, md_clear, serialize, pconfig, arch_lbr, ibt, flush_l1d, arch_capabilities
Virtualization Features	
Virtualization	VT-x
Caches	
L1d	352 KiB (10 instances)
L1i	576 KiB (10 instances)
L2	6.5 MiB (4 instances)
L3	12 MiB (1 instance)
NUMA	
NUMA node(s)	1
NUMA node0 CPU(s)	0-11
Vulnerabilities	
Gather data sampling	Not affected
Itlb multihit	Not affected
L1tf	Not affected
Mds	Not affected
Meltdown	Not affected
Mmio stale data	Not affected
Reg file data sampling	Vulnerable: No microcode
Retbleed	Not affected
Spec rstack overflow	Not affected

Field	Value
Spec store bypass	Mitigation; Speculative Store Bypass disabled via prctl
Spectre v1	Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Spectre v2	Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSB-eIBRS SW sequence; BHI BHI_DIS_S
Srbds	Not affected
Tsx async abort	Not affected

H/W path	Device	Class	Description
/0/1		memory	32GiB System Memory
/0/1/0		memory	16GiB SODIMM Synchronous 560
/0/1/1		memory	16GiB SODIMM Synchronous 560
/0/13		memory	64KiB BIOS
/0/1e		memory	96KiB L1 cache
/0/1f		memory	64KiB L1 cache
/0/20		memory	2560KiB L2 cache
/0/21		memory	12MiB L3 cache
/0/22		memory	256KiB L1 cache
/0/23		memory	512KiB L1 cache
/0/24		memory	4MiB L2 cache
/0/25		memory	12MiB L3 cache
/0/100/14.2		memory	RAM memory