

**Borad Name : Mango-m32f2**

Revision : V1.1

DATE:2012.05.30

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## Part Reference

**U : Component**  
**X : Crystal**  
**C : Capacitor**  
**R : Resistor**  
**SW : Switch**  
**LD : Chip LED**  
**CON : Connectors**  
**H : Hole**  
**TP : Test Point**  
**V : ESD Varistor**  
**JP : Jumper Connector**  
**Q : Transistor**

**TITLE**

Title **Mango-m32f2**

Size  
A

Document Number  
<Doc>

Rev  
1.1

Date: Wednesday, May 30, 2012

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BLOCK DIAGRAM

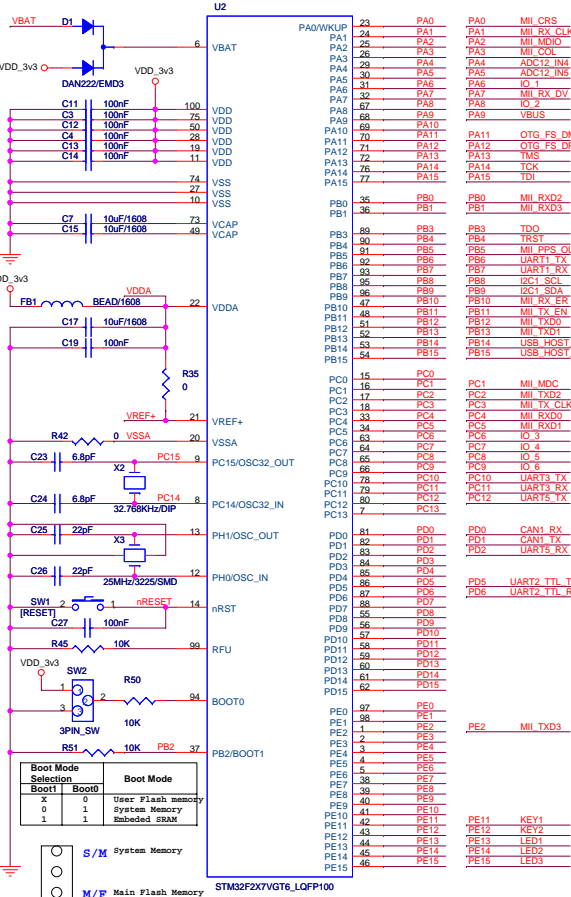
BLOCK DIAGRAM		
Title		
Mango-m32f2		
Size	Document Number	Rev
A3	<Doc>	0.1
Date:	Thursday, April 26, 2012	Sheet 2 of 4

Revision Histroy

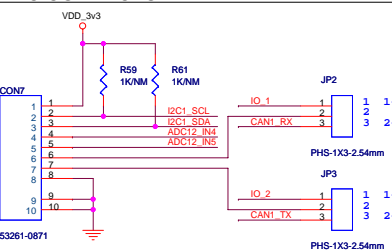
DATE:2011.09.27	V0.1	01. START
DATE:2012.04.19	V1.0	01. VDD_3v3_RTL8201A 3.3v 연결 02. PA9 -> VBUS로 네트 변경 03. PB6, PB7 -> USART1 TX, RX 로 네트 변경 04. UART1, UART3 커넥터 위치 변경 05. PB8, PB9 -> I2C1_SCL, I2C1_SDA 로 네트 변경 06. PD0, PD1 -> CAN1_RX, CAN1_TX 로 네트 변경 07. R91, R92, R93 추가 08. PD8, PD9 -> UART3_TX, UART3_RX 로 네트 변경 09. PC10, PC11 -> UART4_TX, UART4_RX 로 네트 변경 10. R88, R87 네트 UART4_TX, UART4_RX로 변경 11. CON11 실크 변경 12. J-TAG 커넥터 20P F로 변경
DATE:2012.05.30	V1.1	1. PD8, PD9네트 변경 UART3_TX, UART3_RX 제거 2. PC10, PC11 UART3_TX, UART3_RX 네트 변경 3. PC12, PD2 UART5_TX, UART5_RX 네트 변경 4. CON11 UART5_TTL로 실크 변경 5. UART4_TX, UART4_RX 네트 제거 6. 보드 리비전 V1.1 로 변경

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A3	<Doc>	1.0
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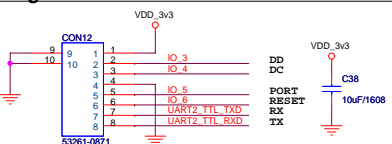
# STM32F2



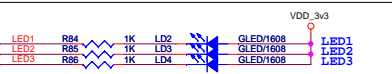
## I2C CONNECTOR



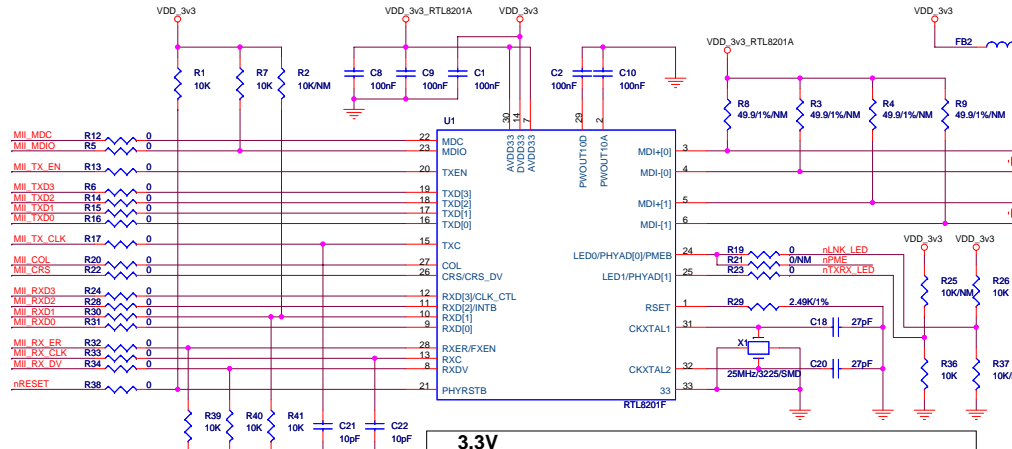
## ZigBee CONNECTOR



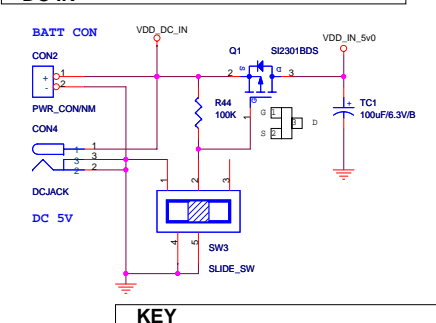
**LED**



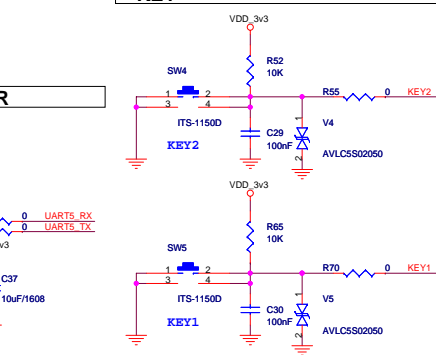
## RTL8201F



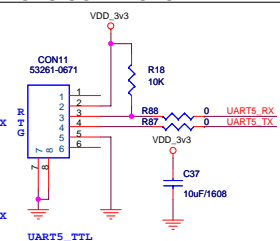
## DC IN



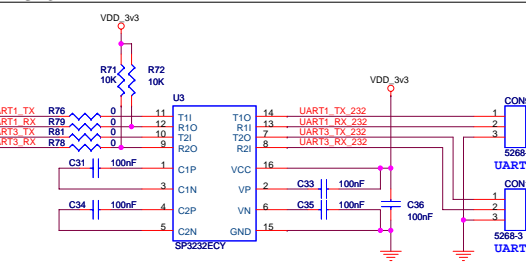
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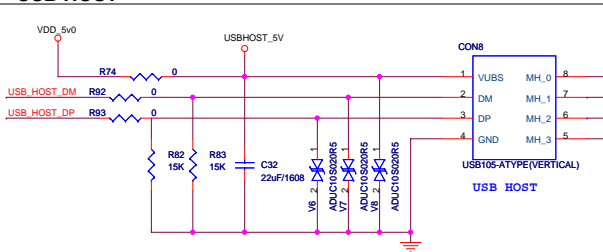
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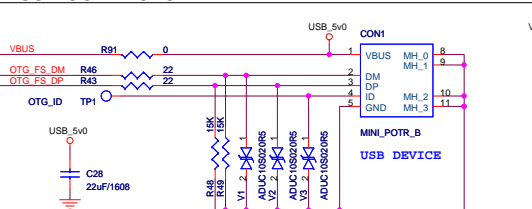
## RS232



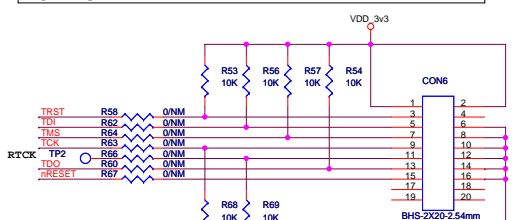
## USB HOST



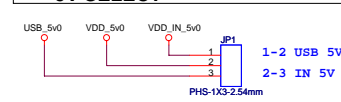
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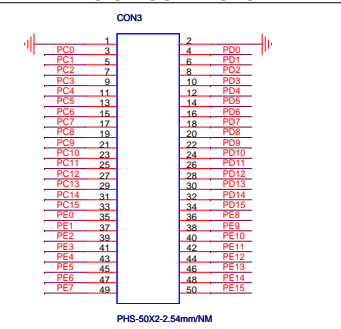
J-TAG



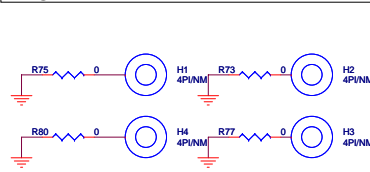
5V SELECT



## EXPANSION CONNECTOR



HOLE
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<b>MAIN</b>				
Title				
<b>Mango-m32f2</b>				
Size C	Document Number <Doc>			Rev 0.1
Date	Workorder		Month	Year
	Mar 30, 2015		March	2015