# Hamiltonian Simulation Circuit

# David Ponarovsky

February 22, 2023

#### Abstract

In this work we deal with the problem of designing efficient circuit to simulate the development in time of the initial state by the given  $\Delta$ -local Hamiltonian. We suggest an heuristics and compare the circuit depth obtained by them to the circuit which is yielded by naive approach.

### 1 The Problem.

Generate a circuit, using no more than 10 qubits, that approximates the unitary  $e^{-iH}$  where H is the qubit Hamiltonian of a **LiH** (lithium hydride) molecule. The **LiH** Hamiltonian is composed of 276 Pauli strings, and can be found HERE. The approximation error is defined in the next section, and should be less than 0.1. The circuit should be composed of the CX and single qubit gates only.

**Definition 1.** Consider the following relation  $\prec$ . We will say that  $H_i \prec H_j$  if  $H_j$  could obtained by applying the replacements  $I \mapsto X$  or  $I \mapsto Z$ . For example, if  $H_1 = IXIXZ$  and  $H_2 = IXZXZ$  then  $H_1 \prec H_2$ , observe also that  $I^{\otimes n} \prec H_i$  for any  $H_i$ . Note that this relation defines an order (transitive).

**Definition 2** (Order Tree). Consider the directed graph G = (V, E), such that the vertices associated with the Hamiltonian terms, and there is a directed edge between  $H_i$  and  $H_j$  if  $H_i \prec H_j$ . We will say that G is the ordered tree of H.

**Lemma 1.** There is a classical algorithm which compute for given H with m terms over n qubits the associated ordered tree, at  $O\left(m^2n\right)$  classical time.

*Proof.* Algorithm.

**Algorithm 1:** Construct the ordered tree from given local terms  $H_1, H_2...H_m$ .

```
2 if H \prec H_v then
        H \leftrightarrow H_v
        call in recursion (v, H_v)
 4
 5 end
 6 else
        Let L be an empty list.
 7
        Let w \leftarrow \emptyset
 8
        for \{v,u\} \in E do
 9
           if H_v \prec H_u then
10
             L \leftarrow L \cup \{H_u\}
11
12
            else if H_u \prec H_v then
13
             w \leftarrow u
14
            end
        end
16
        if L \neq \emptyset then
17
            Define new vertex v', and edge \{v, v'\}
18
            Define the edges \{v', u\} for any u \in L
19
            Remove the edges \{v, u\} for any u \in L
20
        end
21
        else
22
           call in recursion (w, H)
23
        end
25 end
```

**Definition 3.** Let  $H_i$  be a single term of the Hamiltonian, we will define the support $(H_i)$  to be a vector  $v \in \mathbb{F}_2^n$  such that  $v_j = 1$  if  $H_i$  act non-trivally on the jth qubit and 0 else.

Noitce that each term  $H_i$  such that support $(H_i) = 1^a 0^b = (1, 1...1, 0, ..., 0)$  can be simulated in parallel with term  $H_j$  with complementary support (i.e  $0^a 1^b$ ). In addition, if the support isn't a continues segement of 1's then one could perform rotate the base by permutation at a cost of the "mixing size". [COMMENT] Change G to be the graph which each node v = (P, base change).

**Definition.** Denote by m the number of local Hamiltonian terms and consider the graph  $G = (\{H_i\}, E)$  which is a wighted complte graph over m vertices. Such that the wight function defined by  $w(u, v) = \#\{j|H_v^j \neq H_u^j \text{ and } H_v^j, H_u^j \neq I\}$ . We will call to such G the Qubic embedding or shortly just G.

**Theorem.** Given a  $\Delta$ -local Hamiltonian over quabits and m terms such that  $n > \frac{m}{\Delta+1}$  then there is a circuit which simulate a single Turturize step at depth:

$$D(n,m) \le \log_{\frac{\Delta+1}{\Delta}}(m) \cdot \frac{\Delta}{1+\Delta} n \cdot (2+2\Delta)$$
$$= \log_{\frac{\Delta+1}{\Delta}}(m) \cdot 2\Delta n$$

Let us abuse the notation and denote by H also the random variable which count the support size of local term which is sampled uniformly from the total Hamiltonian.

**Theorem.** [COMMENT] Change to uniform drown  $\Delta$ -local Hamiltonian. Given an Hamiltonian with expected support  $\mathbb{E}[H] := \mathbb{E}_{\sim i} \left[ \mathbf{Ham}(support(H_i)) \right]$  then there is a circuit which simulate a single step  $e^{iH\Delta t}$  at depth:

$$\begin{split} D\left(n,m\right) \leq & m\left(1 + \mathbb{E}_{\sim i}[H]\right) \\ & + 4w\left(\text{MST}\left(G\right)\right) + \left(2 - \frac{1}{m}\right)n^2 \end{split}$$

Where w(MST(G)) is the weight of the minimal spanning tree of G. Please, noitce that in our case n = 10, m = 276.

Before continuing to the proof let's analyze the naive approach, just handle each of Hamiltonian terms one by one. Assume the the hamming weight of the support of  $H_i$  equal to  $d_i$ . so we pay two steps to rotate each wire into the parity base and uncompute it in the end. then we could apply the CX from each qubit in the support to a chosen one which will sum the wires parity and finally will propagate the sum trough the  $RZ(\theta\Delta t)$  gate (rotation by the coefficient of the term and the step size). So in total we pay  $2 + 2d_i$  for each term and therefore the whole circuit will require:

$$D^{\text{naive}}(n, m) = \sum_{i} 2 + 2d_{i} = m \sum_{i} \frac{2 + 2d_{i}}{m} = 2m (1 + \mathbb{E}_{\sim i}[H])$$

Hence the theorem inequality could be written as:

$$D(n,m) \le \frac{1}{2} D^{\text{naive}}(n,m) + 4w \left( \text{MST}(G) \right) + \left( 2 - \frac{1}{m} \right) n^2$$

And therefore, for an Hamiltonians which have a grater number of terms than the number of the qubits which they act on, we expect to see an improvement which is almost an half of the naive solution. In fact this improvement is much more significant than might seem as simulating up to  $\varepsilon$ -error requires  $\mathbf{Poly}\left(\frac{1}{\varepsilon}\right)$  repetitions [COMMENT] add a cition, so for full simulation we have obtained a  $\sim (\frac{1}{2})^{poly(\frac{1}{\varepsilon})}$  speed-up factor.

**Solution.** Let's formulate the claim which mentioned earlier, suppose that  $H_i, H_j$  are terms which their supports are disjoint and also they lay on continues segments. Then we can simulate the terms in parallel.

**Lemma 2.** let  $H_i$ ,  $H_j$  and integers a, b, c, da < c, d < b such the supports of the terms are  $0^a 1^b, 1^c 0^d$ . Then we can simulate  $e^{iH_i + H_j}$  with circuit with depth  $2 + \max_{i,j} 2 \cdot \mathbf{Ham}(support(H_l))$ .

Proof. It's clears that the first rotation, and the final uncompute stages remain unchanged. So we should deal only with 2-qubits operators (the cnot's). By the a < c restriction it follows that each CX gate in the naive implementation of the  $H_i$  cross only elements which  $H_j$  act trivially on. Hence if at time-step k the original operations in each of the naive implementations were  $CX(i_1,i_2), CX(j_1,j_2)$  then  $j_1,j_j \notin [i_1,i_2]$ . Repeating on the above argument, but consider the d < b constraint instead yields that  $i_1,i_j \notin [j_1,j_2] \Rightarrow [i_1,i_2] \cap [j_1,j_2] = \emptyset$  and therefore we can put those gates on the same time step

**Corollary.** Assume that  $H_i, H_j$  have a disjoint supports. Then by swapping the wires (including phase swap) into continues segments, we obtain a simulation at cost of :  $cost(Permutation) + 2 + \max_{i,j} 2 \cdot \mathbf{Ham}(\operatorname{support}(H_l))$ .

The permutation which swapping between  $v_1, v_2...v_k \leftrightarrow u_1, u_2...u_k$  where  $v_i, u_j \in [n]$  are wires/qubits indices could be done by swapping each of the pair separately. Namely,  $\mathbf{SWAP}(v_i, u_i)$  for each of the pairs. Figure 1 presents the two qubit gate  $\mathbf{SWAP}$  and Figure 2 is an example of the generalization obtained by concatenation such gates.

#### **SWAP Gate**

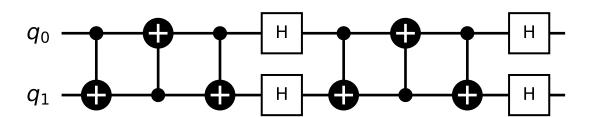


Figure 1: The implementation of the two-qubit **SWAP** gate. The left part replace the bit values of the qubits, then we are rotate into the dual space and swap the phases finally we return back to the original space.

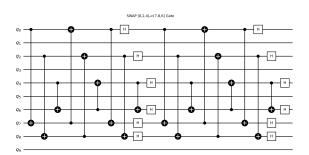


Figure 2: Example for the general swap gate **SWAP**, In that example  $v = (e_1, e_2, e_4)$  and  $u = (e_7, e_8, e_6)$ . And notice that the mapping swap between  $e_4 \leftrightarrow e_6$  wires.

Now Let  $v \in \mathbb{F}_2^n$ , we will use the notation  $\mathbf{SWAP}^v$  to name the family of circuits such that for every  $v_i = 1$  the *i*'th wire will be swapped with *j*'th wire such that  $j \leq \mathbf{Ham}(v)$ . In other words, we could think about a circuit  $C \in \mathbf{SWAP}^v$  as the one who lifting (advancing) the interesting wires to the top of the circuit.

In similar way for  $u, v \in \mathbb{F}_2^n$  we will name by  $\mathbf{SWAP}^{v,u}$  the family of gates such that for every  $C_1 \in \mathbf{SWAP}^v$  and  $C_2 \in \mathbf{SWAP}^{v,u}$  we get that  $C_2C_1 \in \mathbf{SWAP}^u$ .

To understand the rational behind those notations consider the following possible terms:

- $H_1 = IXXII$
- $H_2 = XIIZI$
- $H_3 = IIXIX$
- $H_4 = XIIZI$

In case we have decided to simulate first  $H_1, H_2$  which can be done in parallel after swapping the first and the third qubit. Then we already have made half of the way trough the rotation in which  $H_3, H_4$  can be compute in parallel. That insight bring us to the next Lemma.

**Definition.** Let G' be the graph which is obtained from G by indentify each vertex with his support.

**Lemma 3.** Let v, u be vertices in G' which defined as which by definition are also match to a pair of vectors in  $\mathbb{F}_2^n$ . Then:

$$Depth\left( \mathbf{SWAP}^{v,u} \right) \leq \mathbf{Ham}(v,u) \cdot Depth\left( \mathbf{SWAP} \right)$$

*Proof.* By induction over w(v, u). For the base case consider u, v such u differ from v only by single coordinate. Denote it by j. Observes that if  $\mathbf{Ham}(u) < \mathbf{Ham}(v)$  then  $\mathbf{SWAP}^{v,u}$  is just the indentity. So we can assume that  $\mathbf{Ham}(u) = \mathbf{Ham}(v) + 1$  and that  $v_j = 0, u_j = 1$ , denote by j' the wire which  $SWAP^v$  sends j into. then it's sufficient to apply the gate  $\mathbf{SWAP}(\mathrm{Ham}(v) + 1, j')$ .

Assume the correctness of the lemma for any  $d' \leq d$ , denote again by j one of the coordinate in which v, u act different, And consider the vector  $u' = u \oplus v_j \Rightarrow u'_j = v_j$ , if  $\mathbf{Ham}(u,v) = d$  then  $\mathbf{Ham}(u',v) = d - 1$  and therefore there exist a circuit  $C^{v,u'} \in \mathbf{SWAP}^{v,u'}$  at depth  $\mathbf{Ham}(v,u') \cdot \mathrm{Depth}(\mathbf{SWAP})$ . By the fact that  $\mathbf{SWAP}^{v,u} = \mathbf{SWAP}^{v,u'}\mathbf{SWAP}^{u',u}$  and because  $\mathbf{Ham}(u',u) = 1$  we get that:

$$D\left(\mathbf{SWAP}^{v,u}\right) = D\left(\mathbf{SWAP}^{v,u'}\right) + D\left(\mathbf{SWAP}^{u',u}\right)$$
  

$$\leq \left(\mathbf{Ham}(v,u') + \mathbf{Ham}(u,u')\right) \cdot \operatorname{Depth}\left(\mathbf{SWAP}\right)$$
  

$$= \mathbf{Ham}(v,u) \cdot \operatorname{Depth}\left(\mathbf{SWAP}\right)$$

And that finishes the proof

**Corollary.** Let  $P = \langle v_1, v_2...v_l \rangle$  a vertices path in the graph G as defined above. And let U be circuit such that for each  $v_i \in P$  U apply the circuit  $U_i$  at the [COMMENT] I should define it base. Then there exist an implementation for that circuit at depth:

$$\begin{split} &D(U) = \sum_{i} D\left(U_{i}\right) + D\left(\mathbf{SWAP}^{v_{i}, v_{i+1}}\right) \\ &= \sum_{i} D\left(U_{i}\right) + \mathbf{Ham}(v_{i-1}, v_{i}) \cdot \operatorname{Depth}\left(\mathbf{SWAP}\right) \\ &= \sum_{i} D\left(U_{i}\right) + w(v_{i-1}, v_{i}) \cdot \operatorname{Depth}\left(\mathbf{SWAP}\right) \\ &= w\left(P\right) \cdot \operatorname{Depth}\left(\mathbf{SWAP}\right) + \sum_{i} D\left(U_{i}\right) \end{split}$$

**Expectation of intersect terms.** Denote by  $X_i^j$  the indector which assign 1 to the event that the *i* term act non trivality on the *j*th qubit. Then it's clear that

$$\mathbb{E}\left[\text{intersects}\right] \leq \mathbb{E}\left[\sum_{i,k}^{m} n \cdot X_{i}^{j} X_{k}^{j}\right] = \binom{m}{2} n p^{2}$$

If we sample the terms form a uniform distribution over the  $\Delta$ -local terms then  $p = \binom{n-1}{\Delta-1} / \binom{n}{\Delta} = \frac{\Delta}{n}$ . And therfore the exception of the intersections is less than  $\binom{m}{2} \frac{\Delta^2}{n}$ . Hence by markov inequality we obtin that

$$\mathbf{Pr}\left[\text{intersects} \ge \frac{1}{2} \binom{m}{2}\right] \le \frac{2\Delta^2}{n}$$

The Algorithm.

# **Algorithm 2:** Simulate a single step $e^{iH\Delta t}$

```
1
2 |\psi\rangle \leftarrow the inital state. T \leftarrow \mathrm{MST}(G).
3 P \leftarrow ordering of the vertices \langle v_1,..v_l \rangle according to the DFS scanning of T.

4
5 for v \in P do
6 |\psi\rangle \leftarrow \mathrm{SWAP}^{v_{i-1},v_i}
7 |\text{while } \exists H_i, H_j \text{ s.t } supp(H_i) = v = \overline{supp(H_j)} \text{ do}
8 |\psi\rangle \leftarrow U(H_i)_v \otimes U(H_j)_{\overline{v}} |\psi\rangle
9 |\text{mark } H_i, H_j.
10 |\text{end}
11 end
12
13 complete the left terms by the naive approach.
14 |\text{return } |\psi\rangle
```

**Lemma 4.** Let  $\{X_i\}$  be a monotonic set of numbers bounded by  $2\mu$  with expectation  $\mu$  and variance  $\sigma^2$ . Then:

$$\sum \frac{1}{m} \max\left\{X_i, X_{i+\frac{n}{2}}\right\} \le \mu + \frac{1}{2}\sigma^2$$

*Proof.* Define  $Y: \Omega \to \mathbb{R}$  to be the random variable which is the hist of the X by  $\mu$  i.e  $Y = X - \mu$ . From the linearity of expectation we get that  $\mathbb{E}[X] = \mu + \mathbb{E}[Y]$ . let's note by  $Y^s$  and  $Y^{as}$  the symmetric and asymmetric parts of Y. the  $\square$ .

$$\mathbb{E}[X] = \frac{1}{2}M + \mathbb{E}[X^s - M] = \frac{1}{2}M + \mathbb{E}[(X - M)^s + (X - M)^{as}] = \frac{1}{2}M + \frac{1}{2}\mu + \mathbb{E}[(X - M)^{as}] \qquad \Box$$

**Lemma 5.** Let  $H = \frac{1}{m} \sum H_i$  be Hamiltonian such each term  $H_i$  is a generalized pauli operator. Let P be random pauli which sampled uniformly. Then:

**Lemma 6.** Majoritiy principle. We can always assume that the more then quarter of the paulis are identity. [COMMENT] doesn't correct, but can be passed if we change the definition of the solid tensor to include only differ coordinates.

$$U^{t} = \sqrt{X}^{\dagger} \sqrt{X} U \sqrt{X} \sqrt{X}^{\dagger} U \sqrt{X}^{\dagger} \sqrt{X} ... U \sqrt{X}^{\dagger} \sqrt{X}$$