

Classiq

May 18, 2022

The problem. Generate a circuit, using no more than 10 qubits, that approximates the unitary e^{-iH} where H is the qubit Hamiltonian of a **LiH** (lithium hydride) molecule. The **LiH** Hamiltonian is composed of 276 Pauli strings, and can be found [HERE](#). The approximation error is defined in the next section, and should be less than 0.1. The circuit should be composed of the CX and single qubit gates only.

Definition. Let H_i be a single term of the Hamiltonian, we will define the $\text{support}(H_i)$ to be a vector $v \in \mathbb{F}_2^n$ such that $v_j = 1$ if H_i act non-trivially on the j th qubit and 0 else.

Noitce that each term H_i such that $\text{support}(H_i) = 1^a 0^b = (1, 1, \dots, 1, 0, \dots, 0)$ can be simulated in parallel with term H_j with complementary support (i.e $0^a 1^b$). In addition, if the support isn't a continues segment of 1's then one could perform rotate the base by permutation at a cost of the "mixing size".

Definition. Consider the graph $G \subset K_{2^n}$ such that $v \in G$ iff there exists a term H_i such that $v = \text{support}(H_i)$. Define the edges to be the complete set $\{(u, v) : u, v \in G\}$ and finally define a wight function over the edges by $w(u, v) = \mathbf{Ham}(u, v)$. We will call to such G the Qubic embedding or shortly just G .

Theorem. Given an Hamiltonian with expected support $\mathbb{E}[H] := \mathbb{E}_{\sim_i}[\mathbf{Ham}(\text{support}(H_i))]$ then there is a circuit which simulate a single step $e^{iH\Delta t}$ at depth:

$$D(n, m) \leq m \left(1 + \mathbb{E}_{\sim_i}[H] \right) + 4w(\text{MST}(G)) + \left(2 - \frac{1}{m} \right) n^2$$

Where $w(\text{MST}(G))$ is the weight of the minimal spanning tree of G . Please, noitce that in our case $n =$

$10, m = 276$.

Before continuing to the proof let's analyze the naive approach, just handle each of Hamiltonian terms one by one. Assume the the hamming weight of the support of H_i equal to d_i . so we pay two steps to rotate each wire into the parity base and uncompute it in the end. then we could apply the CX from each qubit in the support to a chosen one which will sum the wires parity and finally will propogate the sum trough the $RZ(\Delta t)$ gate (rotation by the coefficient of the term and the step size). So in total we pay $(2 + 2d_i)$ for each term and therefore the whole circuit will require:

$$D^{\text{naive}}(n, m) = \sum_i (2 + 2d_i) = m \sum_i \frac{2 + 2d_i}{m} = 2m \left(1 + \mathbb{E}_{\sim_i}[H] \right)$$

Hence the theorem inequality could be written as:

$$D(n, m) \leq \frac{1}{2} D^{\text{naive}}(n, m) + 4w(\text{MST}(G)) + \left(2 - \frac{1}{m} \right) n^2$$

And therefore, for an Hamiltonians which have a grater number of terms than the number of the qubits which they act on, we expect to see an improvement which is almost an half of the naive solution. In fact this improvement is much more significant than might seem as simulating up to ε -error requires **Poly** $\left(\frac{1}{\varepsilon}\right)$ repetitions, so for full simulation we have obtained a $\sim \left(\frac{1}{2}\right)^{\text{poly}\left(\frac{1}{\varepsilon}\right)}$ speed-up factor.

Solution. Let's formulate the claim which mentioned earlier, suppose that H_i, H_j are terms which their supports are disjoint and also they lay on continues segments. Then we can simulate the terms in parallel.

Lemma. let H_i, H_j and integers $a, b, c, da < c, d < b$ such the supports of the terms are $0^a 1^b, 1^c 0^d$. Then we can simulate $e^{iH_i + H_j}$ with circuit with depth $2 + \max_{i,j} 2 \cdot \mathbf{Ham}(\text{support}(H_i))$.

Proof. It's clear that the first rotation, and the final uncompute stages remain unchanged. So we should deal only with 2-qubits operators (the cnot's). By the $a < c$ restriction it follows that each CX gate in the naive implementation of the H_i cross only elements which H_j act trivially on. Hence if at time-step k the original operations in each of the naive implementations were $CX(i_1, i_2), CX(j_1, j_2)$ then $j_1, j_2 \notin [i_1, i_2]$. Repeating on the above argument, but consider the $d < b$ constraint instead yields that $i_1, i_2 \notin [j_1, j_2] \Rightarrow [i_1, i_2] \cap [j_1, j_2] = \emptyset$ and therefore we can put those gates on the same time step \square

Corollary. Assume that H_i, H_j have a disjoint supports. Then by swapping the wires (including phase swap) into continuous segments, we obtain a simulation at cost of : $\text{cost}(\text{Permutation}) + 2 + \max_{i,j} 2 \cdot \mathbf{Ham}(\text{support}(H_i))$.

The permutation which swapping between $v_1, v_2 \dots v_k \leftrightarrow u_1, u_2 \dots u_k$ where $v_i, u_j \in [n]$ are wires/qubits indices could be done by swapping each of the pair separately. Namely, $\mathbf{SWAP}(v_i, u_i)$ for each of the pairs. Figure 1 presents the two qubit \mathbf{SWAP} and Figure 2 is an example of the generalization obtained by concatenation such gates.

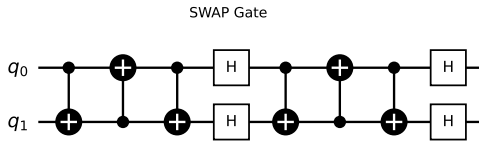


Figure 1: The implementation of the two-qubit \mathbf{SWAP} gate. The left part replace the bit values of the qubits, then we are rotate into the dual space and swap the phases finally we return back to the original space.

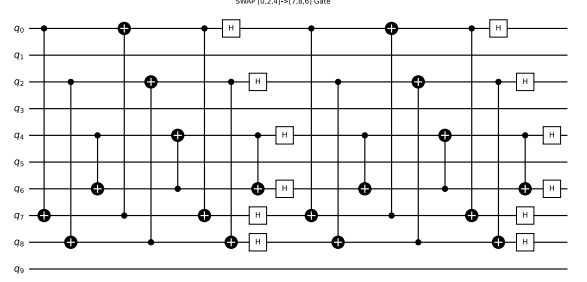


Figure 2: Example for the general swap gate \mathbf{SWAP} , In that example $v = (e_1, e_2, e_4)$ and $u = (e_7, e_8, e_6)$. And notice that the mapping swap between $e_4 \leftrightarrow e_6$ wires.

Now Let $v \in \mathbb{F}_2^n$, we will use the notation \mathbf{SWAP}^v to name the family of circuits such that for every $v_i = 1$ the i 'th wire will be swapped with j 'th wire such that $j \leq \mathbf{Ham}(v)$. In other words, we could think about a circuit $C \in \mathbf{SWAP}^v$ as the one who lifting (advancing) the interesting wires to the top of the circuit.

In similar way for $u, v \in \mathbb{F}_2^n$ we will name by $\mathbf{SWAP}^{v,u}$ the family of gates such that for every $C_1 \in \mathbf{SWAP}^v$ and $C_2 \in \mathbf{SWAP}^{v,u}$ we get that $C_2 C_1 \in \mathbf{SWAP}^u$.

To understand the rational behind those notations consider the following possible terms:

- $H_1 = I \otimes X \otimes X \otimes I \otimes I$
- $H_2 = X \otimes I \otimes I \otimes Z \otimes I$
- $H_3 = I \otimes I \otimes X \otimes I \otimes X$
- $H_4 = X \otimes I \otimes I \otimes Z \otimes I$

In case we have decided to simulate first H_1, H_2 which can be done in parallel after swapping the first and the third qubit. Then we already have made half of the way trough the rotation in which H_3, H_4 can be compute in parallel. That insight bring us to the next Lemma.

Lemma. Let v, u be vertices in G which by definition are also match to a pair of vectors in \mathbb{F}_2^n . Then:

$$\text{Depth}(\mathbf{SWAP}^{v,u}) \leq \mathbf{Ham}(v, u) \cdot \text{Depth}(\mathbf{SWAP})$$

Proof. By induction over $\mathbf{Ham}(v, u)$. For the base case consider u, v such u differ from v only by single coordinate. Denote it by j .

First case $v_j = 0, u_j = 1$, denote by j' the wire which \mathbf{SWAP}^v sends j into. then it's sufficient to apply the gate $\mathbf{SWAP}(\mathbf{Ham}(v) + 1, j')$. Second case $v_j = 1, u_j = 0$, Note that $(\mathbf{SWAP}^{v,u})^\dagger = \mathbf{SWAP}^{u,v}$ and in our case the gates are involve only Clifford gates and therefore the inverse gate has the same length as it's origin. In other hand the inverse gate matches to the first case above.

Assume the correction of the lemma for any $d' \leq d$ and consider the vector $u' = u \oplus e_j \Rightarrow u'_j = v_j$, if $\mathbf{Ham}(u, v) = d$ then $\mathbf{Ham}(u', v) = d - 1$ and therefore there exist a circuit $C^{v,u'} \in \mathbf{SWAP}^{v,u'}$ at depth $\mathbf{Ham}(v, u') \cdot \text{Depth}(\mathbf{SWAP})$. By the fact that $\mathbf{SWAP}^{v,u} = \mathbf{SWAP}^{v,u'} \mathbf{SWAP}^{u',u}$ and because $\mathbf{Ham}(u', u) = 1$ we get that:

$$\begin{aligned} D(\mathbf{SWAP}^{v,u}) &= D(\mathbf{SWAP}^{v,u'}) + D(\mathbf{SWAP}^{u',u}) \\ &= (\mathbf{Ham}(v, u') + \mathbf{Ham}(u, u')) \cdot \text{Depth}(\mathbf{SWAP}) \\ &= \mathbf{Ham}(v, u) \cdot \text{Depth}(\mathbf{SWAP}) \end{aligned}$$

And that finishes the proof \square

Corollary. Let $P = \langle v_1, v_2 \dots v_l \rangle$ a vertices path in the graph G as defined above. And let U be circuit such that for each $v_i \in P$ U apply the circuit U_i at the **[COM-MENT]** I should define it base. Then there exist an implementation for that circuit at depth:

$$\begin{aligned} D(U) &= \sum_i D(U_i) + D(\mathbf{SWAP}^{v_i, v_{i+1}}) \\ &= \sum_i D(U_i) + \mathbf{Ham}(v_{i-1}, v_i) \cdot \text{Depth}(\mathbf{SWAP}) \\ &= \sum_i D(U_i) + w(v_{i-1}, v_i) \cdot \text{Depth}(\mathbf{SWAP}) \\ &= w(P) \cdot \text{Depth}(\mathbf{SWAP}) + \sum_i D(U_i) \end{aligned}$$

The Algorithm.

The general concept. We will identify each pair of

Algorithm 1: Simulate a single step $e^{iH\Delta t}$

$|\psi\rangle \leftarrow$ the initial state. $T \leftarrow \text{MST}(G)$.

$P \leftarrow$ ordering of the vertices $\langle v_1, \dots, v_l \rangle$ according to the **DFS** scanning of T .

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for  $v \in P$  do
     $|\psi\rangle \leftarrow \mathbf{SWAP}^{v_{i-1}, v_i}$ 
    while  $\exists H_i, H_j$  s.t  $\text{supp}(H_i) = v = \overline{\text{supp}(H_j)}$ 
        do
             $|\psi\rangle \leftarrow U(H_i)_v \otimes U(H_j)_{\bar{v}} |\psi\rangle$ 
            mark  $H_i, H_j$ .
    end
end

```

complete the left terms by the naive approach.

return $|\psi\rangle$

Lemma. Let X be a random monotonic variable with expectation μ and variance σ^2 . Then:

$$\sum \frac{1}{m} \max \{X_i, X_{i+\frac{n}{2}}\} \leq \mu + \frac{1}{2}\sigma^2$$

Proof. Define $Y : \Omega \rightarrow \mathbb{R}$ to be the random variable which is the hist of the X by μ i.e $Y = X - \mu$. From the linearity of expectation we get that $\mathbb{E}[X] = \mu + \mathbb{E}[Y]$. let's note by Y^s and Y^{as} the symmetric and asymmetric parts of Y . the \square .

$$\begin{aligned} \mathbb{E}[X] &= \frac{1}{2}M + \mathbb{E}[X^s - M] = \frac{1}{2}M + \mathbb{E}[(X - M)^s + (X - M)^{as}] \\ &= \frac{1}{2}M + \frac{1}{2}\mu + \mathbb{E}[(X - M)^{as}] \end{aligned}$$

Lemma. Let $H = \frac{1}{m} \sum H_i$ be Hamiltonian such each term H_i is a generalized pauli operator. Let P be random pauli which sampled uniformly. Then:

Randomization Trick.